PATENTS ONLY (ref OG 7/28/92)

To the Honorable Co Please record the att.

Name of Party(ies) confering an 1. interest:

> Yasuyuki KIMURA Satoshi SHIMIZU Masakatsu Yokota Ken SUYAMA

Alexsander DEC

APR 0 6 2004

10271777/ marks:

_marks:

Name and Address of Party(ies) 2. receiving an interest:

RENESAS TECHNOLOGY CORP. 4-1, Marunouchi 2-chome Chiyoda-ku, Tokyo, JAPAN

EPOCH MICROELECTRONICS, INC. 7 Skyline Drive, Suite 265 Hawthorne, NY USA

- 3. Description of the interest conveyed: Assignment
- 4. Application number(s) or patent number(s).

Additional sheet attached? ___ Yes XXX No

If this document is being filed together with a new application, the execution date of the application is: _

DATE

A. Patent Application No.(s)

B. Patent No.(s)

10/739,117

Name and address of party to whom correspondence concerning document should be mailed:

> John R. Mattingly Mattingly, Stanger & Malur, P.C. 1800 Diagonal Road, Suite 370 Alexandria, Virginia 22314

- 6. Number of applications and patents involved:
- Amount of fee enclosed or authorized to be charged: \$40.00
- The Commissioner is hereby authorized to charge Deposit Account No. 50-1417 if no credit card payment form is attached.

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9. Execution date of attached document: December 16, 2003 & January 5, 2004

10. To the best of my knowledge and belief, I declare under penalty of perjury under the laws of the United States of America that the foregoing information is true and correct and that any attached copy is a true copy of the original document.

Sheinath Malur

Name of Person Signing

April 6, 2004

Total number of pages being submitted:

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PATENT REEL: 015183 FRAME: 0907

ASSIGNMENT

(譲渡証)

As a below named inventor, I hereby declare that:

IN CONSIDERATION of the sum of One Dollar (\$1.00) or the equivalent thereof, and other good and valuable consideration paid tome a citizen of Japan by Renesas Technology Corp., a corporation organized under the laws of Japan located the following the laws of Japan located the laws of Japan, and Epoch Microelectronics, Inc., a corporation organized under the laws of New York, U.S.A., located at 7 Skyline Drive, Suite 265, Hawthorne, NY 10532, U.S.A., receipt of which is hereby acknowledged I do hereby sell and assign to saidRenesas Technology Corp., and Epoch Microelectronics, Inc., their successors and assigns, all my right, title and interest, in and for the United States of America, in and to

SEMICONDUCTOR INTEGRATED CIRCUIT HAVING BUILT-IN PLL CIRCUIT

invented by me (if only one is named below) or us (if plural inventors are named below) and described in the application for United States Letters Patent therefor, executed on even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the entire ownership of the said Letters Patent when granted, to be held and enjoyed by said Renesas Technology Corp., and Epoch Microelectronics, Inc., their successors, assigns or other legal representatives, to the full end of term for which said Letters Patent may be granted, as fully and entirely as the same would have been held and enjoyed by me or us if this assignment and sale had not been made:

And I hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of the above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And I do hereby authorize and request the Commissioner of Patents to Issue said Letters Patent to said Renesas Technology Corp., and Epoch Microelectronics, Inc.

Signed on the date(s) indicated aside signatures:

	INVENTOR(S) (発明者フルネームサイン)	Date Signed (署名日)
1)	Yasuyuki KIMURA	16/December / 2003
2)	Satoshi SHIMIZU	16/December / 2003
·)	Satoshi SHIMIZU Masakatsu YakotA	16th / Acc. /2003
)	Masakatsu YOKOTA	January 5, 2004
	Aleksander DEC	01/05/2004
_		

PATENT REEL: 015183 FRAME: 0908

RECORDED: 04/06/2004