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1. Name of conveying party(ies):

Hirotsugu KOJIMA  
Teruo KITAMURA

2. Name and address of receiving party(ies):

Name: Renesas Technology Corp.

Address: 4-1, Marunouchi 2-chome

Chiyoda-ku

17497 U.S. PTO  
10/823581  
041404

Additional names(s) of conveying party(ies)  Yes  No

3. Nature of conveyance:

- Assignment  Merger
- Security Agreement  Change of Name
- Other

City: Tokyo State/Prov.: \_\_\_\_\_

Country: Japan ZIP: \_\_\_\_\_

Execution Date: 2/27/04 and 3/10/04

Additional name(s) & address(es)  Yes  No

4. Application number(s) or patent numbers(s):

10823581

If this document is being filed together with a new application, the execution date of the application is: 2/27/04 & 3/10/04

Patent Application No. Filing date

B. Patent No.(s)

04/15/2004 EFDRES 00000024 10823581

02 FC:8021

40.00 OF

Additional numbers  Yes  No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Mitchell W. Shapiro

Registration No. 31,568

Address: Miles & Stockbridge P.C.

1751 Pinnacle Drive

Suite 500

City: McLean State/Prov.: VA

Country: United States ZIP: 22102

6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 3.41):.....\$ 40.00

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9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Mitchell W. Shapiro

April 14, 2004

Name of Person Signing

Signature

2

Date

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PATENT  
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**ASSIGNMENT**  
( 譲渡証 )

As a below named inventor, I hereby declare that:

IN CONSIDERATION of the sum of One Dollar (\$1.00) or the equivalent thereof, and other good and valuable consideration paid to me citizen of Japan by Renesas Technology Corp., a corporation organized under the laws of Japan, located at 4-1, Marunouchi 2-chome, Chiyoda-ku, Tokyo, Japan, receipt of which is hereby acknowledged I do hereby sell and assign to said Renesas Technology Corp., its successors and assigns, all my right, title and interest, in and for the United States of America, in and to

**SEMICONDUCTOR INTEGRATED CIRCUIT AND ELECTRONIC SYSTEM**

invented by me (if only one is named below) or us (if plural inventors are named below) and described in the application for United States Letters Patent therefor, executed on even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the entire ownership of the said Letters Patent when granted, to be held and enjoyed by said Renesas Technology Corp., its successors, assigns or other legal representatives, to the full end of term for which said Letters Patent may be granted as fully and entirely as the same would have been held and enjoyed by me or us if this assignment and sale had not been made;

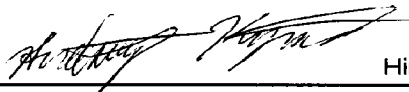
And I hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of the above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And I do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said Renesas Technology Corp.,

Signed on the date(s) indicated aside our signatures:

INVENTOR(S)  
(発明者フルネームサイン)

Date Signed  
(署名日)

1)  Hirotsugu KOJIMA

Feb., 27, 2004

2) Teruo KITAMURA Teruo KITAMURA

10 / March / 2004

3) \_\_\_\_\_

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4) \_\_\_\_\_

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9) \_\_\_\_\_

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10) \_\_\_\_\_

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