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**PATENT** REEL: 015333 FRAME: 0545

## Assignment

Whereas,	1/84/4.
AAIIGIGIGIGIG	1/44-00,

SACHIKO EDO

Tokyo, Japan

of

hereinafter called assignor(s), have invented certain improvements in CLOCK SYNCHRONOUS TYPE SEMICONDUCTOR MEMORY DEVICE

and executed an application for Letters Patent of the United States of America therefor on March 8  $20^{04}$ ; and

Whereas,

Elpida Memory, Inc 2-1, Yaesu 2-chame,

Chuo-ku, Tokyo, Japan (assignee), desires to acquire the entire right, title, and interest in the application and invention, and to any United States patents to be obtained therefor:

Now therefore, for valuable consideration, receipt whereof is hereby acknowledged,

I/We, the above named assignor(s), hereby sell, assign and transfer to the above named assignee, its successors and assigns, the entire right, title and interest in the application and the invention disclosed therein for the United States of America, including the right to claim priority under 35 U.S.C. §119, and I/we request the Commissioner of Patents to issue any Letters Patent granted upon the invention set forth in the application to the assignee, its successors and assigns; and I/We will execute without further consideration all papers deemed necessary by the assignee in connection with the United States application when called upon to do so by the assignee.

I/₩e hereby a	authorize and request my attorne	ys SUGHRUE, MION, ZINN, MACPEAK & SEAS o
2100 Pennsylvani	a Avenue, N.W., Washington, D.C.	2 <mark>0037-3202 to insert here in parentheses (Appl</mark> ication
number	, filed	the filing date and application
number of said a	application when known.	
March <b>Date</b> : 2004		Les Gelie SACHIKO EDO
Date:	s/	
Date:	s/	
Witness: Mar 20	ch 8, 04 Jaka	shi Czana
Marc	h 8, 04	Die A

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