

700134712

H-460-06

PATENTS ONLY (ref OG 7/28/92)

To the Honorable Commissioner of Patents and Trademarks:
Please record the attached original document or copy thereof.

- 1. Name of Party(ies) conveying an interest: HITACHI, LTD.
- 2. Name and Address of Party(ies) receiving an interest: RENESAS TECHNOLOGY CORP.
4-1, Marunouchi 2-chome
Chiyoda-ku, Tokyo, JAPAN

3. Description of the interest conveyed: Assignment of Entire Interest.
Original assignment filed 7/27/98
Reel 09383, Frame 0160

4. Application number(s) or patent number(s).
Additional sheet attached? ___ Yes XXX No

If this document is being filed together with a new application,
the execution date of the application is: _____
DATE

- A. Patent Application No.(s)
- B. Patent No.(s)
5,847,073
5,962,347
6,074,253
6,309,980

5. Name and address of party to whom correspondence concerning document should be mailed:

John R. Mattingly
Mattingly, Stanger & Malur, P.C.
1800 Diagonal Road, Suite 370
Alexandria, Virginia 22314

- 6. Number of applications and patents involved: One
- 7. Amount of fee enclosed or authorized to be charged: \$40.00
- 8. The Commissioner is hereby authorized to charge Deposit Account No. 50-1417 if no credit card payment form is attached.

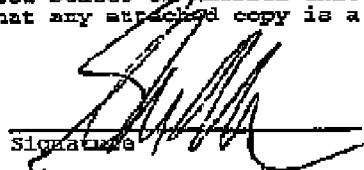
DO NOT USE THIS SPACE

9. Execution date of attached document: November 18, 2004

10. To the best of my knowledge and belief, I declare under penalty of perjury under the laws of the United States of America that the foregoing information is true and correct and that any attached copy is a true copy of the original document.

Shrinath Malur
Name of Person Signing

Signature



November 30, 2004
Date

Total number of pages being submitted: 2

CH \$160.00 501417 5847013

CH \$160.00 501417 5847013

ASSIGNMENT

WHEREAS, Hitachi, Ltd., is the exclusive owner, by assignment, of the following applications for Letters Patents of the United States of America:

- U.S. Patent No. 5,874,013, issued February 23, 1999
- U.S. Patent No. 5,962,347, issued October 5, 1999
- U.S. Patent No. 6,074,958, issued June 13, 2000
- U.S. Patent No. 6,309,980, issued October 30, 2001

WHEREAS, Renesas Technology Corporation, a corporation organized under the laws of Japan, having its place of business at 4-1, Marunouchi 2-chome, Chiyoda-ku, Tokyo, Japan and is desirous of acquiring the right, title and interest, in, to and under the Patents, and the inventions covered thereby, as now owned by Hitachi, Ltd.,

NOW, THEREFORE, for good and valuable consideration, the sufficiency and receipt of which is hereby acknowledged, the said Hitachi, Ltd. has sold, assigned, transferred and set over and does hereby sell, assign, transfer and set over to the said Renesas Technology Corp., its entire, right, title and interest in the said inventions, and any reissue or reissues of said Letters Patents which may be granted, the same to be held and enjoyed by said Renesas Technology Corporation for its own use and enjoyment, and for the use and enjoyment of its successors, assigns, or other legal representatives, to the end of the term for which said Letters Patents of the United States are granted or reissued as fully and entirely as the same would have been held and enjoyed by the said Hitachi, Ltd., if this assignment and sale had not been made.

Hitachi, Ltd.

11/18/2004
Date



By: Yasuo SAKUTA, Patent Attorney
Title: Executive Officer
Intellectual Property Group
(Authorized Signing Officer)



UNITED STATES DEPARTMENT OF COMMERCE
Patent and Trademark Office
ASSISTANT SECRETARY AND COMMISSIONER
OF PATENTS AND TRADEMARKS
Washington, D.C. 20231



700134712

DECEMBER 06, 2004

PTAS

MATTINGLY, STANGER & MALUR, P.C.
JOHN R. MATTINGLY
1800 DIAGONAL ROAD, SUITE 370
ALEXANDRIA, VIRGINIA 22314

UNITED STATES PATENT AND TRADEMARK OFFICE
NOTICE OF RECORDATION OF ASSIGNMENT DOCUMENT

THE ENCLOSED DOCUMENT HAS BEEN RECORDED BY THE ASSIGNMENT DIVISION OF THE U.S. PATENT AND TRADEMARK OFFICE. A COMPLETE MICROFILM COPY IS AVAILABLE AT THE ASSIGNMENT SEARCH ROOM ON THE REEL AND FRAME NUMBER REFERENCED BELOW.

PLEASE REVIEW ALL INFORMATION CONTAINED ON THIS NOTICE. THE INFORMATION CONTAINED ON THIS RECORDATION NOTICE REFLECTS THE DATA PRESENT IN THE PATENT AND TRADEMARK ASSIGNMENT SYSTEM. IF YOU SHOULD FIND ANY ERRORS OR HAVE QUESTIONS CONCERNING THIS NOTICE, YOU MAY CONTACT THE EMPLOYEE WHOSE NAME APPEARS ON THIS NOTICE AT 703-308-9723. PLEASE SEND REQUEST FOR CORRECTION TO: U.S. PATENT AND TRADEMARK OFFICE, ASSIGNMENT DIVISION, BOX ASSIGNMENTS, CG-4, 1213 JEFFERSON DAVIS HWY, SUITE 320, WASHINGTON, D.C. 20231.

RECORDATION DATE: 11/30/2004

REEL/FRAME: 015409/0602
NUMBER OF PAGES: 2

BRIEF: ASSIGNMENT OF ASSIGNOR'S INTEREST (SEE DOCUMENT FOR DETAILS).

ASSIGNOR:
HITACHI, LTD.

DOC DATE: 11/18/2004

ASSIGNEE:
RENESAS TECHNOLOGY CORP.
4-1, MARUNOUCHI 2-CHOME
CHIYODA-KU, TOKYO, JAPAN

5-15-97

08/257,167

5-1

SERIAL NUMBER: ~~08819251~~
PATENT NUMBER: 5847013
TITLE: SUPERABSORBING POLYMERIC NETWORKS

FILING DATE: 03/17/1997
ISSUE DATE: 12/08/1998 *- 2 - 2 3-79*

SERIAL NUMBER: 09188371
PATENT NUMBER: 5962347
TITLE: SEMICONDUCTOR INTEGRATED CIRCUIT ARRANGEMENT FABRICATION METHOD

FILING DATE: 11/10/1998
ISSUE DATE: 10/05/1999

015409/0602 PAGE 2

| | |
|--|-------------------------|
| SERIAL NUMBER: 09339041 | FILING DATE: 06/23/1999 |
| PATENT NUMBER: 6074958 | ISSUE DATE: 06/13/2000 |
| TITLE: SEMICONDUCTOR INTEGRATED CIRCUIT ARRANGEMENT FABRICATION METHOD | |

| | |
|--|-------------------------|
| SERIAL NUMBER: 09564754 | FILING DATE: 05/04/2000 |
| PATENT NUMBER: 6309980 | ISSUE DATE: 10/30/2001 |
| TITLE: SEMICONDUCTOR INTEGRATED CIRCUIT ARRANGEMENT FABRICATION METHOD | |

ALLYSON PURNELL, EXAMINER
ASSIGNMENT DIVISION
OFFICE OF PUBLIC RECORDS

MATTINGLY, STANGER & MALUR, P.C.

ATTORNEYS AT LAW
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PATENT, TRADEMARK
AND COPYRIGHT LAW
FACSIMILE: (703) 684-1157

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JEFFREY M. KETCHUM
Registered Patent Agent

* Bar Membership Other Than Virginia

(703) 684-1120

Date: December 8, 2004

FACSIMILE COVER LETTER

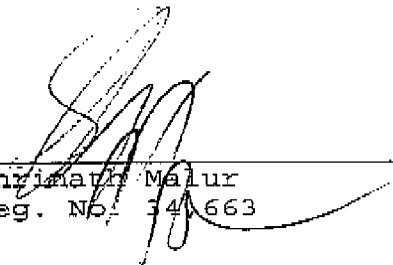
Facsimile Number: (703) 306-5995

To: Commissioner of Patents
From: Shrinath Malur
MATTINGLY, STANGER & MALUR, P.C.
Re: USSN 10/094,157
Attorney Docket No.: H-480-06

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following listed documents are being facsimile transmitted to the U.S. Patent and Trademark Office on the date shown below:

- Request to Correct Notice of Recordation of Assignment;
- Copy of Notice of Recordation of Assignment;
- Copy of Recordation Form Cover Sheet filed 11/30/04;
- Copy of Original Assignment document.



Shrinath Malur
Reg. No. 341663

December 8, 2004

Date

Total Number of Pages (including cover sheet): 7

If the facsimile you receive is incomplete or illegible, please CALL (703) 684-1120. Thank you.

IN THE UNITED STATES PATENT AND TRADEMARK

Appl. No. : 10/094,157 Confirmation No.
Applicant : T. TOKUNAGA et al
Filed : March 8, 2002
Titled : SEMICONDUCTOR INTEGRATED CIRCUIT
ARRANGEMENT FABRICATION METHOD
TC/A.U. : 1765
Examiner : R. Kunemund
Docket No.: H-480-06
Customer No.: 24,956

Commissioner for Patents
Mail Stop Assignments
PO Box 1450
Alexandria VA 22313-1450

REQUEST TO CORRECT NOTICE OF RECORDATION
OF ASSIGNMENT

Sir:

On November 30, 2004, Applicants filed an assignment in the above-identified application. A copy of the Notice of Recordation of Assignment Document received on December 3, 2004 is enclosed. Copies of the following papers were submitted on November 30, 2004: Recordation Form Cover Sheet and copy of original assignment document.

The patent number of the first listed patent (5,847,013) on the Notice of Recordation document is correct. The serial number, filing date of the application and issue date of the patent are incorrect. These should read as follows: Serial No. 08/857,167, Filing Date 03/17/1997 and Issue Date 2/23/99.

Appl. No. 10/094,157
Request to Correct Notice of Recordation
filed December 8, 2004

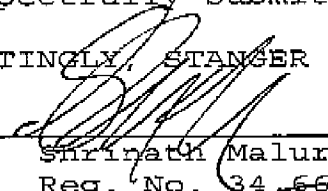
H-480-06

Correction is requested.

Respectfully submitted,

MATTINGLY, STANGER & MALUR

By



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