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HITA-0569

To the Honorable Commissioner of Patents and Trademarks. Please record the attached original documents or copy thereof.			
Additional names of 3. Nature of Con Security A Other:	•	2. Name and Address of Receiving Party(ies): (1) Name: Hitachi, Ltd. Address: 6, Kanda Surugadai 4-chome City: Chiyoda-ku, Tokyo, Japan And (2) Name: Hitachi ULSI Systems Co., Ltd. Address: 22-1, Josuihoncho 5-chome City: Kodaira-shi, Tokyo, Japan	
4. (a) Patent Application Number(s): If this document is being filed together with a new application, the execution date of the application is: Additional Numbers Attached.		4. (b) Patent Numbers:	
5. Name and Address of Party to whom Correspondence Concerning this Document Should be Mailed:		6. Total Number of Applications and Patents Involved: 1	
Name:	Stanley P. Fisher	7. Total Fee: \$40.00 (37 C.F.R. § 3.41)	
Address:	Reed Smith LLP 3110 Fairview Park Dr. Suite 1400 Falls Church, Va. 22042	Enclosed. Authorized to be charged to deposit account.	
DO NOT VICE TO	TIC OD LOS	8. Deposit Account Number: 08-1480 ATTACH DUPLICATE COPY OF THIS PAGE IF PAYING BY DEPOSIT ACCOUNT	
DO NOT USE THIS SPACE			
9. Statement and Signature: To the best of my knowledge and belief, the foregoing is true and correct and any attached copy is a true copy of the original document.			
VXVII	with	June 22, 2004	
Stanley P. Fisher, Registration No. 24, 344 Juan Carlos A. Marquez, Registration No. 34,072			
Total number of pages comprising cover sheet: 1			

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ASSIGNMENT (譲渡証)

As a below named inventor, I hereby declare that:

IN CONSIDERATION of the sum of One Dollar (\$1.00) or the equivalent thereof, and other good and valuable consideration paid to me citizen of Japan by HITACHI, LTD. and Hitachi ULSI Systems Co., Ltd., corporations organized under the laws of Japan, located at 6, Kanda Surugadai 4-chome, Chiyoda-ku, Tokyo, Japan, and 22-1, Josuihoncho 5-chome, Kodaira-shi, Tokyo, Japan, respectively, receipt of which is hereby acknowledged if do hereby sell and assign to said HITACHI, LTD. and Hitachi ULSI Systems Co., Ltd., their successors and assigns, all my right, title and interest, in and for the United States of America, in and to

LEVEL SHIFT CIRCUIT AND SEMICONDUCTOR INTEGRATED CIRCUIT

invented by me (if only one is named below) or us (if plural inventors are named below) and described in the application for United States Letters Patent therefor, executed on even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the entire owner ship of the said Letters Patent when granted, to be held and enjoyed by said HITACHI, LTD. and Hitachi ULSI Systems Co., Ltd., their successors, assigns or other legal representatives, to the full end of term for which said Letters Patent may be granted, as fully and entirely as the same would have been held and enjoyed by me or us if this assignment and sale had not been made:

And I hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of the above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And I do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said HITACHI, LTD. and Hitachi ULSI Systems Co., Ltd.

Signed on the date(s) indicated aside signatures:

RECORDED: 06/22/2004

	INVENTOR(S)	Date Signed
	(発明者フルネームサイン)	(署名日)
1) _	Hirokatsu Hayashi (Hirokatsu HAYASHI)	31/August / 2001
	Joshiro Jakahashi (Toshiro TAKAHASHI)	~
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4) _		
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6) _		
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PATENT REEL: 015497 FRAME: 0711