07-08-2004



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T Attorney Docket No. 501.43823X00

OMB No. 0651-0027 (exp. 6/30/2005)	U.S. DEPARTMENT OF COMMERCE U.S. Patent and Trademark Office		
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AMANO, KENJI	Name: 1) RENESAS TECHNOLOGY CORP.		
FUJISAWA, ATSUSHI HASEBE, HAJIME	Name. 1) RENESAS TECHNOLOGY CORP.		
	2) RENESAS NORTHERN JAPAN		
6-29-04	SEMICONDUCTOR, INC.		
, , , ,			
Additional name(s) of conveying party(ies) attached? Tyes No	Address 4) 4.4 Manusauchi 2 ahama		
3. Nature of conveyance:	Address: 1) 4-1, Marunouchi 2-chome, Chiyoda-ku, Tokyo, Japan		
	Oniyoda-ku, Tukju, dapan		
☐ Assignment ☐ Merger	2) 1007-39, Izumisawa, Chitose-shi,		
☐ Security Agreement ☐ Change of Name	Hokkaido, Japan		
Other			
Evacution Date: March 22, 2004	Additional name(s) & address(es) attached? ☐ Yes ☒ No		
Execution Date: March 23, 2004.			
Application number(s) or patent number(s):			
If this document is being filed together with a new application, the			
A. Patent Application No.(s)	B. Patent Registration No.(s)		
	10/878-226		
	10/8/8026		
Additional number(s) attached ☐ Yes ☒ No			
5. Name and address of party to whom correspondence	6. Total number of applications and patents involved		
concerning document should be mailed:			
	7. Total fee (37 CFR 3.41)		
Name: Antonelli, Terry, Stout & Kraus, LLP			
	Enclosed		
Internal Address:	☐ Authorized to be charged to deposit account		
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PATENT REEL: 015528 FRAME: 0223

## ASSIGNMENT

(譲渡証)

As a below named inventor, I hereby declare that:

IN CONSIDERATION of the sum of One Dollar (\$1.00) or the equivalent thereof, and other good and valuable consideration paid to me citizen of Japan by Renesas Technology Corp., and Renesas Northern Japan Semiconductor, Inc., corporations organized under the laws of Japan, located at 4-1, Marunouchi 2-chome, Chiyoda-ku, Tokyo, Japan, and 1007-39, Izumisawa, Chitose-shi, Hokkaido, Japan,

receipt of which is hereby acknowledged I do hereby sell and assign to said Renesas Technology Corp., and Renesas Northern Japan Semiconductor, Inc.,

their successors and assigns, all my right, title and interest, in and for the United States of America, in and to

## SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THEREOF

invented by me (if only one is named below) or us (if plural inventors are named below) and described in the application for United States Letters Patent therefor, executed on even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the entire ownership of the said Letters Patent when granted, to be held and enjoyed by said Renesas Technology Corp., and Renesas Northern Japan Semiconductor, Inc.,

their successors, assigns or other legal representatives, to the full end of term for which said Letters Patent may be granted as fully and entirely as the same would have been held and enjoyed by me or us if this assignment and sale had not been made;

And I hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of the above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And I do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said Renesas Technology Corp., and Renesas Northern Japan Semiconductor, Inc.,

Signed on the date(s) indicated aside our signatures:

RECORDED: 06/29/2004

inventor(5) (発明者フルネームサイン)			Date Signed (署名日)
1)_	Kenji Amano	Kenji AMANO	23 / March / 2004
	Atsushi tujisawa		23/March/2004
3)_	Hajime Hasebe	Hajime HASEBE	23/March/2004
5)			
6)_			
7)_			
8)_			
9)_			
10)			

PATENT REEL: 015528 FRAME: 0224