

07-08-2004

Form: PTO-1595

06/29/04



ET

DOCKET NUMBER: SC12283TS

102785808

To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

17497 U.S. PTO
10/879991
062904

1. Name of conveying party(ies):
Motorola, Inc.
Additional name(s) of conveying party(ies) attached? Yes No

2. Name and address of receiving party(ies)
Name: FREESCALE SEMICONDUCTOR, Inc.
Internal Address: Law Department
Street Address: 6501 William Cannon Drive West
City: Austin State: TX Zip: 78735
Additional name(s) & address(es) attached? Yes No

3 Name of conveyance:
 Assignment Merger
 Security Agreement Change of Name
 Other
Execution Date: _____

4. Application number(s) or patent number(s):
If this document is being filed together with a new application, the execution date of the application is _____
A. Patent Application No.(s)
10/879991
Additional numbers attached? Yes No

B. Patent No.(s)

5. Name and address of party to whom correspondence concerning document should be mailed:
Name: FREESCALE SEMICONDUCTOR, INC.
Internal Address: _____ MD: TX32/PL02
Street Address: 7700 W. Parmer Lane
City: Austin
State: Texas Zip: 78729

6. Total number of applications and patents involved: 1
7. Total fee (37 CFR 3.41) \$40.00
 Enclosed
 Authorized to be charged to deposit account
8. Deposit account number: 503079
(Attach duplicate copy of this page if paying by deposit account.)

DO NOT USE THIS SPACE

9 Statement and signature.
To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document
Susan C. Hill Susan C. Hill 6/29/04
Name of Person Signing Signature Date
Total number of pages including cover sheet, attachments, and documents: 5

Mail documents to be recorded with required cover sheet information to:
Commissioner of Patents & Trademarks, Box Assignments, Alexandria, VA 22313

07/07/2004 LHMILLER 00000144 503079 10879991
01 FC:8021 40.00 BA

PATENT ASSIGNMENT

This Patent Assignment is delivered pursuant to that certain Intellectual Property Assignment Agreement (the "Agreement") effective as of April 4, 2004, between Motorola, Inc. ("Assignor") and Freescale Semiconductor, Inc. ("Assignee"). Capitalized terms used in this Patent Assignment have the same meanings given to them in the Agreement.

The Assignor has delivered this instrument signed by the Assignor to enable the Assignee to file it with any appropriate governmental agency to indicate ownership of Registered Intellectual Property described below and for the other purposes set forth in this instrument. This instrument supplements and is in addition to all other rights of the Assignee under the Agreement and other instruments of transfer delivered in connection with the Agreement.

1. For good and valuable consideration, receipt of which the Assignor acknowledges, and by signing and delivering this instrument, the Assignor sells, assigns, transfers, conveys, and delivers to the Assignee all of the Assignor's right, title, and interest in and to:
 - (a) the patents, patent applications and invention disclosures specifically listed in Schedule 1 to this Patent Assignment; and
 - (b) the following properties and rights with respect to all patents and patent applications so listed in Schedule 1:
 - (i) the inventions claimed or described in the patents or applications,
 - (ii) any patents in the United States and anywhere else in the world and patent applications that have been or may be granted or filed, respectively, with respect to those inventions, including without limitation all foreign patents that may claim priority based on and correspond to the patents listed in Schedule 1,
 - (iii) all divisions, renewals, reissues, continuations, extensions, and (if filed by or for Assignee) continuations-in-part of the foregoing patents,
 - (iv) all income, royalties, damages, and payments due or payable to the Assignor with respect to the patents, including without limitation unpaid damages and payments for past, present, and future infringements of any patent and
 - (v) all rights to sue and recover damages and payments for past, present, and future infringements of any of the patents, including the right to fully and entirely replace the Assignor in all related matters.
2. The foregoing rights in and under the patents shall apply to the full end of their terms as fully as the Assignor would have held the same in the absence of this assignment. As of the date set forth below, the Assignee has succeeded to all right, title, and standing of the Assignor to (a) receive all rights and benefits pertaining to the patents described above,

and (b) commence, prosecute, defend and settle all claims and take all actions that the Assignee, in its sole discretion, may elect in relation to the patents and rights described above.

- 3. This Patent Assignment (a) is irrevocable and effective upon the Assignor's signature to and delivery of a manually signed copy of this instrument or facsimile or email transmission of the signature to this instrument in connection with the execution of the Agreement, if and only if the such execution occurs, (b) benefits and binds the parties to the Agreement and their respective successors and assignees, and (c) does not modify or affect, and is subject to, the provisions of the Agreement .

The undersigned has signed this Patent Assignment effective as of April 4, 2004.

Motorola, Inc.:

By: [Signature]
Name: Jonathan P. Meyer
Title: Senior Vice President and Assistant General Counsel

STATE OF Illinois
COUNTY OF COOK

On April 30, 2004 before me, Gabrielle Richards (the undersigned notary), personally appeared Jonathan P. Meyer personally known to me (or proved to me on the basis of satisfactory evidence) to be the person whose name is subscribed to the within instrument and acknowledged to me that he executed the same in his authorized capacity, and that by his signature on the instrument the person, or the entity upon behalf of which the person acted, executed the instrument.

WITNESS my hand and official seal this 30th day of April, 2004.

My Commission Expires:



[Signature]
Notary Public Signature
GABRIELLE RICHARDS
Printed Name of Notary Public

INTELLECTUAL PROPERTY ASSIGNMENT AGREEMENT
EXHIBIT A – LISTING OF PRINCIPAL ASSIGNED INTELLECTUAL PROPERTY

SCHEDULE 1: ASSIGNED PATENTS

A. Assigned SPS Patents

Status Description	Granted	Country Code	Docket Number	Patent Number	Grant Date	Title
		USA	AP01203	5173836	12/22/92	A HERMETICALLY SEALED INTERFACE
		USA	AP01288	5386341	01/31/95	SEMICONDUCTOR CARRIER ASSEMBLY
		USA	AP5036ES	6697956	02/24/04	METHOD AND APPARATUS FOR PHRASE SYNCHRONIZING A PLURALITY OF MICROCONTROLLERS OF A DISTRIBUTED MICROCONTROLLER NETWORK IN A BRAKE-BY-WIRE AUTOMOBILE BRAKING SYSTEM
		USA	BW00031	6301306	10/09/01	METHOD AND APPARATUS FOR GENERATING A SHORT-RANGE WIRELESS DATA COMMUNICATION LINK
		USA	CE30084P	5784427	07/21/98	FEEDBACK AND SHIFT UNIT
		USA	CE30085P	5781566	07/14/98	CYCLIC REDUNDANCY CODER
		USA	CM00120E	4639631	01/27/87	ELECTROSTATICALLY SEALED PIEZOELECTRIC DEVICE
		USA	CM00153F	4997812	03/05/91	SUPERCONDUCTING THICK FILM
		USA	CM00216FI	5120678	06/09/92	ELECTRICAL COMPONENT PACKAGE COMPRISING POLYMER-REINFORCED SOLDER BUMP INTERCONNECTIONS

PCT	SC12259ZP	PCT/US03/365564	11/14/03	MRAM AND METHODS FOR READING THE MRAM
PCT	SC12275MP	PCT/US03/30865	09/30/03	FLIP CHIP OPTICAL AND IMAGING SENSOR DEVICE
PCT	SC12278TC	PCT/US03/24462	08/05/03	CAPACITIVE CHARGE PUMP
PCT	SC12282ZK	PCT/US04/03100	02/04/04	ELECTRONIC COMPONENT AND METHOD OF MANUFACTURING SAME
PCT	SC12283TS	PCT/RU02/00469	10/29/02	METHOD AND APPARATUS FOR SELECTIVELY OPTIMIZING INTERPRETED LANGUAGE CODE
PCT	SC12287TS	PCT/US03/27430	08/22/03	METHOD AND APPARATUS FOR PROCESSING AN AMPLITUDE MODULATED (AM) SIGNAL
PCT	SC12303EM	PCT/EP03/11176	10/06/03	ARRANGEMENT SYSTEM AND METHOD FOR VECTOR PERMUTATION IN SINGLE-INSTRUCTION MULTIPLE-DATA MICROPROCESSORS.
PCT	SC12309HP	PCT/US03/30866	09/30/03	OPTICAL SENSOR PACKAGE
PCT	SC12310TK	PCT/US03/30593	09/23/03	PACKAGED IC USING INSULATED WIRE
PCT	SC12316TV	PCT/US04/03095	02/04/04	TIMING SYNCHRONIZATION FOR M-DPSK CHANNELS
PCT	SC12320TC	PCT/US03/22851	07/22/03	METHOD AND CIRCUITRY FOR IDENTIFYING WEAK BITS IN AN MRAM
PCT	SC12322TC	PCT/US03/33246	10/20/03	TECHNIQUE FOR SENSING THE STATE OF A MAGNETO-RESISTIVE RANDOM ACCESS MEMORY