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Form PTO-1595 (Rev. 10/02) OMB No. 0651-0027 (exp. 6/30/2005	T U.S. DEPARTMENT OF COMMERCE U.S. Patent and Trademark Office
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To the Honorable Commissioner of Patents and Trademarks:	
Name of conveying party(ies): MIYAMOTO, Nao SUKUTA, Toshiyuki	Name and address of receiving party(ies) Name: HITACHI, LTD.
	Street Address: 6, Kanda Surugadai 4-chome, Chiyoda-ku, Tokyo, Japan
Additional name(s) of conveying party(ies) attached? Yes No	
Nature of conveyance:	7
☐ Assignment ☐ Merger ☐ Change of Name ☐ Other	Additional name(s) & address(es) attached? ☐ Yes ☒ No
Execution Date: March 17, 23, 2004.	
4. Application number(s) or patent number(s): If this document is being filed together with a new application, the A. Patent Application No.(s)	execution date of the application is:, B. Patent Registration No.(s)
10/781,746 Filed February 20, 2004	
Additional number(s) attached ☐ Yes ☒ No	
Name and address of party to whom correspondence concerning document should be mailed:	6. Total number of applications and patents involved
Name: Antonelli, Terry, Stout & Kraus, LIP	7. Total fee (37 CFR 3.41) \$40.00
	⊠ Enclosed
Internal Address:	
1300 North Seventeenth Street Suite 1800 Arlington, VA 22209 USA	Deposit account number:
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9. Statement and signature. To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.	
Gregory E. Montone, Reg#28.141 Name of Person Signing Signature Date	
Total number of pages including cover sheet, attachments, and document: 2	

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ASSIGNMENT (譲渡証)

As a below named inventor, I hereby declare that:

IN CONSIDERATION of the sum of One Dollar (\$1.00) or the equivalent thereof, and other good and valuable consideration paid to me citizen of Japan by Hitachi, Ltd.,

a corporation organized under the laws of Japan,

located at 6, Kanda Surugadai 4-chome, Chiyoda-ku, Tokyo, Japan,

receipt of which is hereby acknowledged I do hereby sell and assign to said Hitachi, Ltd.,

its successors and assigns, all my right, title and interest, in and for the United States of America, in and to

SEMICONDUCTOR INTEGRATED-CIRCUIT DEVICE AND METHOD TO SPEED-UP CMOS CIRCUIT

invented by me (if only one is named below) or us (if plural inventors are named below) and described in the application for United States Letters Patent therefor, executed on even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the entire ownership of the said Letters Patent when granted,

to be held and enjoyed by said Hitachi, Ltd.,

its successors, assigns or other legal representatives, to the full end of term for which said Letters Patent may be granted as fully and entirely as the same would have been held and enjoyed by me or us if this assignment and sale had not been made;

And I hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of the above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And I do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said Hitachi, Ltd.,

Signed on the date(s) indicated aside our signatures:

RECORDED: 07/09/2004

INVENTOR(S) (発明者フルネームサイン)	Date Signed (署名日)
1) Nao Miyamato	3/17/2004
1) Nao Migamoto 2) Toshiyahu Sakuta	3/23/2004
3)	
4)	
5)	
6)	
7)	
8)	
9)	
10)	

PATENT REEL: 015554 FRAME: 0113