

Form PTO-1595
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08-17-2004



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U.S. DEPARTMENT OF COMMERCE
U.S. Patent and Trademark Office

To the Honorable Commissioner

Attached original documents or copy thereof.

1. Name of conveying party(ies):
Macronix International Co., Ltd.
and
Chien Pang Venture Capital Corp.

2. Name and address of receiving party(ies)

Name: Biomorphic VLSI, Inc.

Internal Address:

Additional name(s) of conveying party(ies) attached? ☐ Yes ☒ No

3. Nature of conveyance:

- ☐ Assignment ☐ Merger
☐ Security Agreement ☐ Change of Name
☒ Other Release of Security Interest

Street Address: 4615 E. Thousand Oaks Blvd.
Suite 180

City: Westlake Village State: CA Zip: 91360

Execution Date: September 19, 2003

Additional name(s) & address(es) attached? ☐ Yes ☒ No

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is:

A. Patent Application No.(s)

B. Patent No.(s)

Please see Exhibit A, attached hereto.

Additional numbers attached? ☒ Yes ☐ No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Louis L.S. Tao

Internal Address:

Street Address: Sonnenschein Nath & Rosenthal

1301 K Street, N.W., Suite 600, East Tower

City: Washington State: DC Zip: 20005

6. Total number of applications and patents involved: 8

7. Total fee (37 CFR 3.41).....\$ 320.00

- ☒ Enclosed
☐ Authorized to be charged to deposit account

8. Deposit account number:

01/15/2004 BYRME 00000099 09724424

01 FC:0031

320.00 00

DO NOT USE THIS SPACE

9. Signature.

Bimal P. Mathur

Name of Person Signing

Bimal Mathur

Signature

Dec 24/03 2003

Date

Total number of pages including cover sheet, attachments, and documents: 6

Mail documents to be recorded with required cover sheet information to:

Commissioner of Patents & Trademarks, Box Assignments
Washington, D.C. 20231

PATENT
REEL: 015674 FRAME: 0233

EXHIBIT A

Application Number(s) or Patent Number(s):

Title	Ser. No./Pat. No.
PIXEL READ-OUT ARCHITECTURE	09/274,424
COLOR FILTER PATTERN	09/274,413
COLOR-OPTIMIZED PIXEL ARRAY DESIGN	09/697,764
COLOR SELECTION FOR SPARSE COLOR IMAGE RECONSTRUCTION	09/482,270
PIXEL SELECTIVE WHITE BALANCING	09/552,217
SYSTEM AND METHOD OF COLOR INTERPOLATION	09/482,844
HIGH SENSITIVITY PHOTODIODE	09/690,424
HIGH ACCURACY SAMPLE AND HOLD CIRCUIT FOR ARRAY READ-OUT CIRCUIT	09/625,610

RELEASE OF SECURITY INTEREST

WHEREAS, on December 26, 2001, Biomorphic VLSI, Inc., a California corporation (the "Borrower"), and Macronix International Co., Ltd., a corporation organized under the laws of the Republic of China (the "Secured Party"), entered into a certain Credit Enhance and Security Agreement (the "Agreement");

WHEREAS, the Agreement granted the Secured Party a security interest in the Borrower's intellectual property, which is more specifically described in Exhibit A attached hereto and made a part hereof (the "Intellectual Property");

WHEREAS, the Agreement has been recorded by the Assignment Division of the United States Patent and Trademark Office at Reel 012566 / Frame 0456; and

WHEREAS, the Secured Party wishes to release its security interest in the Intellectual Property and to record said release with the United States Patent and Trademark Office.

NOW THEREFORE, for good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, the Secured Party hereby relinquishes all claims to, and any and all right, title, and interest it may have had in, the Intellectual Property.

MACRONIX INTERNATIONAL CO., LTD.

Date: Sept. 19, 2003

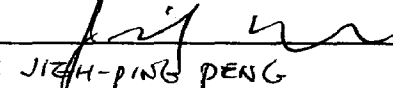
By: 
Name: JIZHI-PING PENG
Title: V.P. of Multimedia

EXHIBIT A

INTELLECTUAL PROPERTY

The intellectual property of the Borrower can be divided into the following four categories:

1. Products and/or designs for the digital still camera market listed below:

<u>Bi8602</u>	-- 1/3" VGA, 10-bit A/D, 5.0V power supply	Available: Now
<u>Bi8603</u>	-- 1/3" VGA, 10-bit A/D, 5.0V power supply	Available: Now
<u>Bi8615</u>	-- 1/4" VGA, 8-bit A/D, 3.3V device (0.35 um)	Available: 11, 2001
<u>Bi8801</u>	-- 1/2" 1 MP, 10-bit A/D, 3.3V device (0.25 um)	Design Completed
<u>Bi8831</u>	-- 1/2" 1.3 MP, 10-bit A/D, 3.3V device (0.25 um)	Design Completed
<u>Bi6810</u>	-- PC Centric Controller for Dual Mode Market	Design Completed

2. Products and/or designs for the integrated information appliance market listed below:

<u>Bi6612</u>	-- 1/5" CIF, 8-bit A/D, 5V device	Available: Now
<u>Bi6615</u>	-- 1/7" CIF, 8-bit A/D, 3.3V device (0.35 um)	Design in Progress
<u>Bi8636</u>	-- 1/4" VGA, Single Chip Camera with JPEG Output (0.25 u)	Design in Progress
Sensor core for optical mouse		Design in Progress
Firecam IEEE1394 PC Centric Camera Controller		Design in Progress

3. Patent applications delivered to the Borrower's patent counsel or filed with the U.S. Patent Office list below:

- PIXEL READ-OUT ARCHITECTURE -- filed 3/23/99
- COLOR FILTER PATTERN -- filed 3/23/99
- COLOR SELECTION FOR SPARSE COLOR IMAGE RECONSTRUCTION -- filed 1/13/00
- SYSTEM AND METHOD OF COLOR INTERPOLATION -- filed 1/13/00
- PIXEL SELECTIVE WHITE BALANCING -- filed 4/19/00
- HIGH ACCURACY SAMPLE AND HOLD CIRCUIT FOR ARRAY -- filed 7/26/00
- HIGH SENSITIVITY PHOTODIODE -- filed 10/17/00
- COLOR-OPTIMIZED PIXEL ARRAY DESIGN -- filed 10/26/00
- CMOS COMPATIBLE HIGH PERFORMANCE PHOTO DIODE -- in process

4. Patent applications / designs which are prepared by or on behalf of the Borrower during the period that the guarantee made by Secured Party on behalf of Borrower is outstanding.

RELEASE OF SECURITY INTEREST

WHEREAS, on November 14, 2001, Biomorphic VLSI, Inc., a California corporation (the "Borrower"), and Chien Pang Venture Capital Corp., a corporation organized under the laws of Taiwan (the "Secured Party"), entered into a certain Intellectual Property Security Agreement (the "Agreement");

WHEREAS, the Agreement granted the Secured Party a security interest in the Borrower's intellectual property, which is more specifically described in Exhibit A attached hereto and made a part hereof (the "Intellectual Property");

WHEREAS, the Agreement has been recorded by the Assignment Division of the United States Patent and Trademark Office at Reel 012566 / Frame 0456; and

WHEREAS, the Secured Party wishes to release its security interest in the Intellectual Property and to record said release with the United States Patent and Trademark Office.

NOW THEREFORE, for good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, the Secured Party hereby relinquishes all claims to, and any and all right, title, and interest it may have had in, the Intellectual Property.

CHIEN PANG VENTURE CAPITAL CORP.

Date: 09/19/03

By: 

Name: Ding-Hua Hu
Title: Chairman

EXHIBIT A

INTELLECTUAL PROPERTY

The intellectual property of the Borrower is divided into the following four categories:

1. Products/Designs for the digital still camera market:

<u>Bi8602</u> -- 1/3"VGA, 10-bit A/D, 5.0V power supply	Available: Now
<u>Bi8603</u> -- 1/3"VGA, 10-bit A/D, 5.0V power supply	Available: Now
<u>Bi8603A</u> -- VGA, 10-bit A/D, 5.0V power supply	Available: Now
<u>Bi8615</u> -- 1/4" VGA, 8-bit A/D, 3.3V device (0.35 um)	Available: 11, 2001
<u>Bi8801</u> -- 1/2" 1 MP, 10-bit A/D 3.3V device (0.25 um)	Design Completed
<u>Bi8831</u> -- 1/2" 1.3 MP, 10-bit A/D, 3.3 device (0.25 um)	Design Completed
<u>Bi6810</u> -- PC Centric Controller for Dual Mode Market	Design Completed

2. Products/Designs for the integrated information appliance market:

<u>Bi6612</u> -- 1/5" CIF, 8-bit A/D, 5V device	Available: Now
<u>Bi6615</u> -- 1/7" CIF, 8-bit A/D, 3.3V device (0.35 um)	Design in Progress
<u>Bi8636</u> -- 1/5" VGA, Single Chip Camera with JPEG output (0.25 u)	Design in Progress

3. Patent Applications:

• PIXEL READ-OUT ARCHITECTURE	Filed 3/23/99
• COLOR FILTER PATTERN	Filed 3/23/99
• COLOR SELECTION FOR SPARSE COLOR IMAGE RECONSTRUCTION	Filed 1/13/00
• SYSTEM AND METHOD OF COLOR INTERPOLATION	Filed 1/13/00
• PIXEL SELECTIVE WHITE BALANCING	Filed 4/19/00
• HIGH ACCURACY SAMPLE AND HOLD CIRCUIT FOR ARRAY	Filed 7/26/00
• HIGH SENSITIVITY PHOTODIODE	Filed 10/17/00
• COLOR-OPTIMIZED PIXEL ARRAY DESIGN	Filed 10/26/00
• CMOS COMPATIBLE HIGH PERFORMANCE PHOTO DIODE	In process of being completed

4. Patent applications and products designs which are prepared and developed by Borrower during the period in which the bridge loan made by Secured Party to Borrower is outstanding shall also be included on this exhibit.