

PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT								
NATURE OF CONVEYANCE:	ASSIGNMENT								
CONVEYING PARTY DATA									
<table border="1" style="width:100%; border-collapse: collapse;"> <thead> <tr> <th style="width:70%;">Name</th> <th>Execution Date</th> </tr> </thead> <tbody> <tr> <td>Joseph A. Yedinak</td> <td>12/05/2002</td> </tr> <tr> <td>Dwayne S. Reichl</td> <td>12/05/2002</td> </tr> <tr> <td>Douglas L. Lange</td> <td>12/05/2002</td> </tr> </tbody> </table>		Name	Execution Date	Joseph A. Yedinak	12/05/2002	Dwayne S. Reichl	12/05/2002	Douglas L. Lange	12/05/2002
Name	Execution Date								
Joseph A. Yedinak	12/05/2002								
Dwayne S. Reichl	12/05/2002								
Douglas L. Lange	12/05/2002								
RECEIVING PARTY DATA									
Name:	Fairchild Semiconductor Corporation								
Street Address:	82 Running Hill Road								
Internal Address:	MS 35-4E								
City:	South Portland								
State/Country:	MAINE								
Postal Code:	04106								
PROPERTY NUMBERS Total: 1									
<table border="1" style="width:100%; border-collapse: collapse;"> <thead> <tr> <th style="width:30%;">Property Type</th> <th>Number</th> </tr> </thead> <tbody> <tr> <td>Application Number:</td> <td>10315719</td> </tr> </tbody> </table>		Property Type	Number	Application Number:	10315719				
Property Type	Number								
Application Number:	10315719								
CORRESPONDENCE DATA									
Fax Number:	(585)454-6364								
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>									
Phone:	585.454.2250								
Email:	tom.fitzgerald@fitzpatent.com								
Correspondent Name:	Thomas R. FitzGerald, Esq.								
Address Line 1:	16 E. Main Street								
Address Line 2:	Suite 210								
Address Line 4:	Rochester, NEW YORK 14614-1808								
NAME OF SUBMITTER:	Thomas R. FitzGerald								
Total Attachments: 2 source=Assignment#page1.tif source=Assignment#page2.tif									

OP \$40.00 10315719

PATENT

FAIRCHILD SEMICONDUCTOR CORPORATION

Assignment

As a below named inventor or joint inventor of an invention or improvement entitled:

METHOD OF ISOLATING THE CURRENT SENSE ON POWER DEVICES WHILE MAINTAINING A CONTINUOUS STRIPE CELL

for which I have executed an application for Letters Patent of the United States of America, on an even date herewith; and

WHEREAS, FAIRCHILD SEMICONDUCTOR CORPORATION, a Corporation, organized and existing under the laws of the State of Delaware, having its principal office and place of business in the City of South Portland, State of Maine, is desirous of obtaining the entire right, title and interest in, to and under the said invention and the said application in the United States of America and in any and all countries foreign thereto;


NOW, THEREFORE, for good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, I have sold, assigned, transferred, and set over, and by these presents do hereby sell, assign, transfer and set over, unto said FAIRCHILD SEMICONDUCTOR CORPORATION, its successors, legal representatives, and assigns, my entire right, title and interest in, to, and under the said invention, and the said application, and all divisional, renewal, substitutional, and continuation applications thereof, and all Letters Patent of the United States of America which may be granted thereof and all reissues and extensions hereof, and all applications for Letters Patent which may be filed for said invention in any country or countries foreign to the United States of America, including all rights of priority, all rights to publish cautionary notices reserving ownership of said invention, all rights to register said invention in appropriate registries, and all Letters Patent which may be granted for said invention in any country or countries foreign to the United States of America, and all extensions, renewals, and reissues thereof, and I hereby authorize and request the Commissioner of Patents and Trademarks of the United States of America, whose duty it is to issue patents on applications as aforesaid, to issue all Letters Patent for said invention to said FAIRCHILD SEMICONDUCTOR CORPORATION, its successors, legal representatives, and assigns, in accordance with the terms of this instrument.

And I hereby covenant that I have full right to convey the entire interest herein assigned, and that I have not executed, and will not execute, any agreement in conflict herewith.

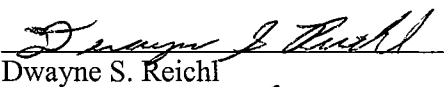
And I hereby further covenant and agree that I will communicate to said FAIRCHILD SEMICONDUCTOR CORPORATION, its successors, legal representatives, and assigns, any fact known to me respecting said invention, and testify in any legal proceeding, sign all lawful papers, execute all divisions, renewal, substitutional, continuing, and reissue applications, make all rightful declarations and/or oaths and generally do everything possible to aid said FAIRCHILD SEMICONDUCTOR CORPORATION, its successors, legal representatives, and assigns, to obtain and enforce proper patent protection for said invention in all countries.

IN TESTIMONY WHEREOF, I authorize and affirm said assignments with the signature(s) set forth below on the indicated date(s).

Inventors:



Joseph A. Yedinak Dec. 5, 2002
Date



Dwayne S. Reichl Dec 5, 2002
Date



Douglas J. Lange 12/5/02
Date

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