

08-25-2004

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PATENT

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8-18-04

**Conveyance Type**

- ☒ Assignment  
☐ License  
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☐ Change of Name  
☐ Other:

2386 U.S. PTO  
10/920360**Conveying Party(ies)**

1. Kiyoshi SHIBATA
- 2.
- 3.
- 4.
- 5.
- 6.

**Execution Date(s)**

August 2, 2004

☐ Mark if Additional Names of Conveying Parties Attached**Receiving Party**

Name Semiconductor Leading Edge Technologies, Inc.

Name

Address 16-1 Onogawa

Address

Address Tsukuba-shi

City

Ibaraki, Japan

State/Country

305-8569

Zip Code

☐ Mark if Additional Names of Receiving Parties Attached**Correspondent Name and Address**

Jeffrey A. Wyand  
 Leydig, Voit & Mayer  
 700 Thirteenth St., N.W. Suite 300  
 Washington, D.C. 20005-3960

Telephone: (202) 737-6770  
 Facsimile: (202) 737-6776  
 Attorney Docket No. 403181/TAKADA

**Pages** Enter the total number of pages of the attached conveyance document including any attachments: 1**Application Number(s) or Patent Number(s)**☐ Mark if additional numbers attached

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**Patent Application Numbers****Patent Numbers**

If this document is being filed together with a new Patent Application, enter the date the patent application was signed by the first named executing inventor.

Month Day Year  
August 2, 2004**Patent Cooperation Treaty (PCT)**

Enter PCT application number only if a U.S. Application Number has not been assigned.

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**Number of Properties**

Enter the total number of properties involved: 1

**Fee Amount**

Fee Amount for Properties Listed (37 CFR 3.41): 40.00

Method of Payment:

☐ Enclosed is a check in the amount of \$☒ Charge Deposit Account No. 12-1216Authorization to Charge Additional Fees to Deposit Account No. 12-1216: ☒ Yes ☐ No**Statement and Signature**

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document. Charges to deposit account are authorized, as indicated herein.

Jeffrey A. Wyand, Reg. No. 29,458

Name of Person Signing

Signature

Date

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Assignment Trm P1

01 FC:8021

40.00 DA

PATENT  
REEL: 015705 FRAME: 0767

ASSIGNMENT

In consideration of the premises and other good and valuable consideration in hand paid, the receipt and sufficiency of which is hereby acknowledged, the undersigned,

Kiyoshi SHIBATA

who has made a certain new and useful invention, hereby sells, assigns and transfers unto

Semiconductor Leading Edge Technologies, Inc.  
16-1 Onogawa, Tsukuba-shi  
Ibaraki, 305-8569  
JAPAN

its successors and assigns ( hereinafter designated "ASSIGNEE" ) the entire right, title and interest for the United States of America as defined in 35 U.S.C. 100 in the invention entitled

METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE

for which an application for United States Letters Patent was executed on even date herewith,

and the undersigned hereby authorizes and requests the United States Commissioner of Patents and Trademarks to issue any and all United States Letters Patent which may be granted therefor and any and all extensions, divisions, reissues, substitutes, renewals, continuations, or continuations-in-part thereof, and the right to all benefits under the international Convention for the Protection of Industrial Property to the said ASSIGNEE, for its interest as ASSIGNEE, its successors, assigns and legal representatives; the undersigned agrees that the attorneys of record in said application shall hereafter act on behalf of said ASSIGNEE;

AND the undersigned hereby agrees to transfer a like interest, and to render all necessary assistance in making application for and obtaining original, divisional, reissued or extended Letters Patent of the United States, upon request of the said ASSIGNEE, its successors, assigns and legal representatives, and without further remuneration, in and to any improvements, and applications for patent based thereon, growing out of or related to the said invention; and to execute any papers by the said ASSIGNEE, its successors, assigns and legal representatives, deemed essential to ASSIGNEE's full protection and title in and to the invention hereby transferred.

SIGNED on the dates indicated aside our signatures:

INVENTOR

DATE SIGNED

*Kiyoshi Shibata*

*August 2, 2004*

Name: Kiyoshi SHIBATA