

09-08-2004



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To the Director, U.S. Patent and Traden

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original documents or copy thereof.

1. Name of conveying party(ies): (List using letters or numbers for multiple parties) CONEXANT SYSTEMS, INC. Additional name(s) of conveying party(ies) attached? () Yes (X) No	2. Name and address of receiving party(ies): Name: WASHINGTON SUB, INC. Internal Address: Street Address: 4311 Jamboree Road City: Newport Beach State: CA ZIP: 92660 Additional name(s) of receiving party(ies) attached? () Yes (X) No
3. Nature of conveyance: (X) Assignment () Security Agreement () Merger () Change of Name () Other: Execution Date: (List as in section 1 if multiple signatures) June 25, 2002	4. US or PCT Application number(s) or US Patent number(s): (X) Patent Application No.: 10/261,896 Filing Date: October 1, 2002 Additional numbers attached? () Yes (X) No
5. Party to whom correspondence concerning document should be mailed: Customer No. 20,995 Return Fax: (949) 760-9502 Attorney's Docket No.: ROKWELL.050C1	6. Total number of applications and patents involved: 1
7. Total fee (37 CFR 1.21(h)): \$40 (X) Enclosed	8. Deposit account number: 11-1410 Please charge this account for any additional fees which may be required, or credit any overpayment to this account.
9. Statement and signature. To the best of my knowledge and belief, the foregoing information is true and correct, and any attached copy is a true copy of the original document. John R. King Name of Person Signing <u>34,362</u> Registration No. <u>John R. King</u> Signature <u>9/3/04</u> Date Total number of pages including cover sheet, attachments and document: 34	

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**ASSIGNMENT OF INVENTIONS, PATENTS, PATENT APPLICATIONS
AND TRADEMARKS**

WHEREAS, CONEXANT SYSTEMS, INC. ("Assignor"), a Delaware corporation with an office at 4311 Jamboree Road, Newport Beach, California 92660-3095, owns or has rights to certain inventions, patents, patent applications and trademarks identified in the attached Schedule entitled "IP SCHEDULE A";

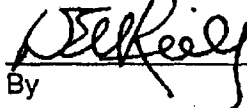
WHEREAS, WASHINGTON SUB, INC. ("Assignee"), a Delaware corporation with an office at 4311 Jamboree Road, Newport Beach, California 92660-3095, wishes to acquire full rights and ownership of said inventions, patents, patent applications and trademarks.

NOW THEREFORE, for good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, Assignor grants, conveys, assigns and transfers to the Assignee and the Assignee's successors and assigns, Assignee's entire right, title and interest in and to said inventions, patents, patent applications and trademarks listed on the attached Schedule identified as "IP SCHEDULE A", including all corresponding applications such as continuations, continuations-in-part, divisionals, provisionals, reissues, reexaminations, and foreign counterparts thereof, along with the subject matter of any and all claims which may be obtained in the aforementioned, in the United States and every foreign country, including all rights to profits and damages by reason of past infringement by any party or parties, with the right to sue and collect same for Assignee's, and Assignee's successors and assigns own use and benefit.

UPON SAID CONSIDERATION, Assignor appoints Assignee and Assignee's successors and assigns as its attorney-in-fact to act in Assignor's name and place to execute, deliver and record any document or instrument of assignment or conveyance necessary to perfect, grant, and confirm the rights granted herein, and Assignor conveys to the Assignee the right to make application, prosecute, receive and enforce in its own behalf and name the inventions, patents, patent applications and trademarks of "IP SCHEDULE A" in the United States and all foreign countries and to claim priority therefrom under the Patent Cooperation Treaty, the International Convention and/or any other international arrangement.

IN WITNESS WHEREOF, Assignor has caused this Assignment to be duly executed by one of its officers on the date shown below.

CONEXANT SYSTEMS, INC.

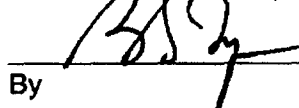
By 

Dennis O'Reilly
Name

General Counsel & Secretary
Title

Date: June 25, 2002

WASHINGTON SUB, INC.

By 

Balakrishnan S. Iyer
Name

Title

IP SCHEDULE A
16/7/02

Granted US Patents

ACTIVE US PATENTS/APPLICATIONS & DISCLOSURES

Pat. No.	Case Number	App. Title	Status	App. Number	File Date	Pat. Number	Issue Date	Comment
1	00CXT0174W	Synthetic Quadrature Generating Apparatus	Granted	776,181	13-Sep-1985	4,787,058	22-Nov-1988	
2	00CXT0185D	Matched Filters for Processing Related Signal Components	Granted	571,596	13-Dec-1995	5,617,063	01-Apr-1997	
3	80E131	Shielded Amplifier	Granted	354,840	04-Mar-1982	4,454,481	12-Jun-1984	
4	82E035	Parameter-Setting Approach to Obtain HiPerformance CTD Transversal Filters for Devices with...	Granted	441,195	12-Nov-1982	4,539,536	03-Sep-1985	
5	85E018	Uncompensated and Compensated GaAs Input Receivers	Granted	759,193	26-Jul-1985	4,703,205	27-Oct-1987	
6	86E006	Third Mode Torsional E-M Resonator	Granted	849,093	07-Apr-1986	5,006,824	08-Apr-1991	
7	90E052	Receiver Designed with Large Output Drive and Having Unique Input Protection Circuit	Granted	591,164	01-Oct-1990	5,124,578	23-Jun-1992	
8	92E030	Symmetrical Clock Crystal Oscillator Circuit	Granted	108/155,500	22-Nov-1993	5,455,542	03-Oct-1995	
9	93E032	Direct Connect Radio and Antenna Assembly	Granted	233,289	26-Apr-1994	5,606,732	25-Feb-1997	
10	93E067	Variable Multi-Threshold Detection for 0.3-GMSK	Granted	307,202	16-Sep-1994	5,459,762	17-Oct-1995	
11	94E005	Component Insensitive, Analog Bandpass Filter	Granted	198,328	15-Feb-1994	5,523,719	04-Jun-1996	
12	94E059	Method and Apparatus for Controlling the Wakeup Logic of a Radio Receiver in Sleep Mode	Granted	108/514,509	11-Aug-1995	5,845,204	01-Dec-1998	
13	95E002	An Iterative Filtering Frequency Estimator	Granted	515,403	15-Aug-1995	5,761,250	02-Jun-1998	
14	95E003	An Adaptive Frequency Correction Burst Detector for the GSM Handset System	Granted	108/537,376	29-Sep-1995	5,724,657	03-Mar-1998	
15	95E056	Jitter Circuit for Reduced Switching Noise	Granted	644,762	10-May-1996	5,754,601	19-May-1998	

IP SCHEDULE A

Pat. No.	Case Number	App. Title	Status	App. Number	Filing Date	Pub. Number	Pub. Date	Comments
16	95E071	Extended Time Tracking and Peak Energy In-Window Demodulation for Use in a Direct Sequence Spread Spectrum System	Granted	08/568,053	08-Dec-1995	5,778,022	07-Jul-1998	
17	95E072	Variable Digital Automatic Gain Control in a Cordless Direct Sequence Spread Spectrum Telephone	Granted	08/568,161	08-Dec-1995	5,764,689	09-Jul-1998	
18	95E080	Method and Apparatus for Signal Quality Estimation in a Direct Sequence Spread Spectrum	Granted	09/348,491	08-Jan-2000	6,301,287	09-Oct-2001	
19	96E006	Ultra-Clean Voltage Power Supply Generator	Granted	08/892,444	14-Jul-1997	6,049,724	11-Apr-2000	
20	97RSS036	An LC-VCO Charge-Pump and Loop-Filter Architecture for Improved Noise-Immunity in Integrated Phase-Locked Loops	Granted	09/409,509	30-Sep-1999	6,281,758	28-Aug-2001	
21	97RSS053	Timing Estimation in Mobile Communication Systems Using Parabolic Interpolator	Granted	09/163,640	30-Sep-1998	6,219,345	17-Apr-2001	
22	97RSS080	A Method and an Apparatus for Positioning System Assisted Cellular Radiotelephone Handoff and Dropoff	Granted	09/025,962	19-Feb-1998	6,327,471	04-Dec-2001	
23	97RSS081	Cellular Radiotelephone Having Answering Machine/Voice Memo Capability with Parameter-Based Speech Compression and Decompression	Granted	09/026,619	20-Feb-1998	6,240,299	29-May-2001	
24	97RSS084	A Transistor Having a Novel Layout and an Emitter Having More Than One Feed Point	Granted	08/126,301	30-Jul-1998	6,236,071	22-May-2001	
25	97RSS086	Voltage Upconverter for Portable Time Divisional Multiple Access Radio	Granted	09/047,242	24-Mar-1998	6,078,167	20-Jun-2000	
26	97RSS100	Method and Apparatus for Automatic Gain Control With Improved Response Time and Stability	Granted	09/036,397	08-Mar-1998	6,037,834	14-Mar-2000	
27	97RSS105	Universal Radio Architecture for Low-Tier Personal Communication System	Granted	08/348,359	30-Nov-1994	5,848,985	15-Jul-1997	PCSI P035US Exhibit 2

Patent Number	App. Title	Status	App. Number	Filing Date	Pub. Number	Exam. Date	Comments
28	97RSS107	Granted	08/9/72,311	18-Nov-1997	6,163,706	19-Dec-2000	
29	97RSS113	Granted	08/9/41,488	30-Sep-1997	8,236,271	22-May-2001	
30	97RSS116	Granted	09/0/44,261	19-Mar-1998	8,005,443	21-Dec-1999	
31	97RSS118	Granted	09/0/57,124	08-Apr-1998	8,208,875	27-Mar-2001	
32	97RSS120	Granted	09/0/36,258	06-Mar-1998	8,125,271	26-Sep-2000	
33	97RSS122	Granted	08/9/40,630	30-Sep-1997	8,031,302	29-Feb-2000	
34	97RSS126	Granted	08/9/65,843	07-Nov-1997	6,216,012	10-Apr-2001	
35	97RSS128	Granted	09/0/64,114	21-Apr-1998	5,966,051	12-Oct-1999	
36	97RSS129	Granted	09/0/40,225	13-Mar-1998	6,078,628	20-Jun-2000	
37	97RSS151CON	Granted	08/3/30,577	27-Oct-1994	5,600,678	04-Feb-1997	PCSI P006US Exhibit 2
38	97RSS151	Granted	07/8/58,397	26-Mar-1992	5,420,867	30-May-1995	
39	97RSS154	Granted	918,941	25-Aug-1997	5,937,010	10-Aug-1999	Continuation of Abandoned PCSI Exhibit 2
40	97RSS158	Granted	08/5/21,568	30-Aug-1995	5,828,955	27-Oct-1998	PCSI P058US Exhibit 2

Serial Number	App. No.	Class.	Int. Class.	Pub. No.	Pub. Date	Pub. No.	Pub. Date	Comments
41	97RSS160CON	Frame Structure Using Consecutive Slot Assignments for Mobile Communications	Granted	08/468,374	08-Jun-1995	5,710,762	20-Jan-1996	PCSI P020US1 Exhibit 2
42	97RSS160	Frame Structure Using Consecutive Slot Assignments for Mobile Communications	Granted	08/145,745	29-Oct-1993	5,444,696	22-Aug-1995	PCSI P020US Exhibit 2
43	97RSS162	Receiver With Filter Offset Correction	Granted	08/516,197	17-Aug-1995	5,757,864	28-May-1996	PCSI P046US Exhibit 2
44	97RSS163	Method and Apparatus for Minimizing Perceptible Impact on an Interrupted Call Prior to Hand-Off	Granted	08/692,552	06-Aug-1996	6,259,916	10-Jul-2001	PCSI P068US Exhibit 2
45	97RSS167	Bipolar Analog Multipliers for Low Voltage Applications	Granted	08/485,088	07-Jun-1996	5,570,056	29-Oct-1996	PCSI P026US Exhibit 2
46	97RSS168	Dual Frequency Synthesis System	Granted	09/163,735	30-Sep-1998	6,127,900	03-Oct-2000	
47	97RSS179	Retransmission Packet Capture System Within a Wireless Multiservice Communications Environment	Granted	08/923,722	04-Sep-1997	6,145,108	07-Nov-2000	
48	97RSS179	Retransmission Packet Capture System Within a Wireless Multiservice Communications Environment	Granted	09/038,724	10-Mar-1998	6,138,260	24-Oct-2000	
49	97RSS194	Using a Single Low-Noise Amplifier in a Multi-Band Wireless Station	Granted	09/164,166	30-Sep-1998	6,134,427	17-Oct-2000	
50	97RSS200	Digital Phase Lock Loop Divider Cycling Method, Apparatus, and Communication System Incorporating the Same	Granted	09/160,884	25-Sep-1998	6,114,888	05-Sep-2000	
51	97RSS201	Dual Band Transmitter for a Cellular Phone Comprising a PLL	Granted	09/163,798	30-Sep-1998	6,150,890	21-Nov-2000	
52	97RSS202	Transmitter Circuitry for a Cellular Phone	Granted	09/158,876	23-Sep-1998	6,304,748	16-Oct-2001	
53	97RSS209	Stability and Enhanced Gain of Amplifiers Using Inductive Coupling	Granted	09/016,195	30-Jan-1998	6,211,738	03-Apr-2001	
54	97RSS242	Frame Structure Using Consecutive Slot Assignments for Mobile Communications	Granted	145,745	29-Oct-1993	5,444,696	22-Aug-1995	

	App. Title	Status	Pub. No.	Pub. Date	Pub. No.	Pub. Date	Comments
55	97RSS291	Granted	08/110,611	24-Aug-1993	5,542,095	30-Jul-1996	PCSI P009US Exhibit 2
56	97RSS311	Granted	09/161,154	25-Sep-1998	6,263,027	17-Jul-2001	
57	97RSS312	Granted	09/103,418	24-Jun-1998	6,268,818	31-Jul-2001	
58	97RSS323	Granted	09/103,897	24-Jun-1998	6,154,664	28-Nov-2000	
59	97RSS324	Granted	09/103,899	24-Jun-1998	6,163,709	19-Dec-2000	
60	97RSS325	Granted	09/103,898	24-Jun-1998	6,151,509	21-Nov-2000	
61	97RSS343	Granted	09/104,732	25-Jun-1998	6,256,476	03-Jul-2001	
62	97RSS347 (WCD)	Granted	09/109,987	30-Jun-1998	6,327,476	04-Dec-2001	
63	97RSS348	Granted	09/060,425	14-Apr-1998	6,100,670	08-Aug-2000	
64	97RSS361	Granted	09/163,885	30-Sep-1998	6,124,761	26-Sep-2000	
65	97RSS393	Granted	09/081,542	19-May-1998	6,181,218	30-Jan-2001	
66	97RSS450	Granted	08/959,588	29-Oct-1997	6,044,069	28-Mar-2000	
67	97RSS451	Granted	09/056,572	07-Apr-1998	6,240,288	29-May-2001	
68	97RSS458	Granted	086,535	14-Apr-1998	D408,398	20-Apr-1999	

IP SCHEDULE A

Patent Number	App Title	Status	App Number	File Date	Pub Number	Issue Date	Comments
69	97RSS462	Granted	09/087,315	29-May-1998	6,057,714	02-May-2000	
70	97RSS464	Granted	09/021,898	11-Feb-1998	6,192,500	20-Feb-2001	
71	97RSS474	Granted	09/016,242	30-Jan-1998	6,075,995	13-Jun-2000	
72	97RSS479	Granted	09/153,008	11-Sep-1998	6,192,228	20-Feb-2001	
73	97RSS480	Granted	09/073,725	07-May-1998	6,219,257	17-Apr-2001	
74	97RSS495	Granted	09/201,643	30-Nov-1998	6,275,687	14-Aug-2001	
75	97RSS497	Granted	09/203,001	30-Nov-1998	6,298,226	02-Oct-2001	
76	97RSS526	Granted	09/164,428	30-Sep-1998	6,181,674	30-Jan-2001	
77	98RSS009	Granted	09/108,947	01-Jul-1998	6,064,272	16-May-2000	
78	98RSS014	Granted	09/148,568	04-Sep-1998	6,160,429	12-Dec-2000	
79	98RSS015	Granted	09/144,404	31-Aug-1998	6,321,072	20-Nov-2001	
80	98RSS058	Granted	09/160,613	24-Sep-1998	6,311,048	30-Oct-2001	
81	98RSS134	Granted	09/134,177	14-Aug-1998	6,223,155	24-Apr-2001	
82	98RSS178	Granted	09/222,686	29-Dec-1998	6,327,462	04-Dec-2001	
83	98RSS202	Granted	09/170,029	13-Oct-1998	6,134,110	17-Oct-2000	

IP SCHEDULE A

Pat. No.	App. Title	Status	App. Number	Pub. Date	Pub. Number	Issue Date	Comments
84	98RSS238	Granted	09/363,779	29-Jul-1998	6,147,561	14-Nov-2000	
	Phase/Frequency Detector with Time-Delayed Inputs in a Charge Pump Based Phase Locked Loop and a Method for Enhancing the Phase Locked Loop Gain						
85	98RSS255	Granted	09/405,749	30-Sep-1998	6,255,912	03-Jul-2001	
	Phase Lock Loop Used as Up Converter and for Reducing Phase Noise of an Output Signal						
86	98RSS259	Granted	09/302,755	30-Apr-1999	6,208,183	27-Mar-2001	
	Gated delay-locked loop for clock generation applications						
87	98RSS261	Granted	09/302,666	30-Apr-1999	6,160,432	12-Dec-2000	
	source-switched or gate-switched charge pump having cascoded output						
88	98RSS267	Granted	09/405,766	27-Sep-1999	6,265,944	24-Jul-2001	
	Fully Integrated Broadband RF Voltage Amplifier with Enhanced Voltage Gain and Method						
89	98RSS268	Granted	09/405,752	27-Sep-1999	6,215,363	10-Apr-2001	
	Low Noise Low Power Charge Pump System for Phase Lock Loop						
90	98RSS271	Granted	09/256,450	23-Feb-1999	6,208,844	27-Mar-2001	
	System and Process for Shared Functional Block Communication Transceivers With GPS Capability						
91	98RSS280	Granted	09/328,927	09-Jun-1999	6,339,361	15-Jan-2002	
	Power Amplifier Driver System for Wireless Handset						
92	98RSS285	Granted	09/318,462	25-May-1999	6,307,894	23-Oct-2001	
	Power Amplification Using A Direct-Upconverting Quadrature Mixer Topology						
93	98RSS286	Granted	09/318,529	25-May-1999	6,242,975	05-Jun-2001	
	Envelope Peak and Trough Limiting to Improve Amplifier Efficiency and Distortion Characteristics						
94	98RSS304	Granted	09/314,898	19-May-1999	6,211,743	03-Apr-2001	
	Phase-Locked Loop Having Temperature-Compensated Bandwidth Control						
95	98RSS307	Granted	09/322,401	28-May-1999	6,262,623	17-Jul-2001	
	Log-Domain Filter Having a Variable Dynamic Range Window						

IP SCHEDULE A

App. No.	App. Title	Status	Pub. No.	Grant Date	Pub. No.	Pub. Date	Granted
96	99RSS319 Six Inverting Amplifier Transconductance Stage and Methods for its Use	Granted	09/355,225	27-Aug-1998	6,191,655	20-Feb-2001	
97	99RSS336 Method and Apparatus for Extending a VCO Tuning Range	Granted	09/498,378	03-Feb-2000	6,204,734	20-Mar-2001	
98	99RSS005 A Method and Apparatus for Improving RF Contact Positioning in as RF Test Socket	Granted	09/360,586	28-Jul-1998	6,227,878	08-May-2001	
99	99RSS015 Power Amplifier Operated as an Envelope Digital to Analog Converter with Digital Pre-Distortion	Granted	09/410,216	30-Sep-1999	6,255,906	03-Jul-2001	
100	99RSS025 System and Method For Variable Gain Coder-Decoder	Granted	09/378,325	20-Aug-1999	6,252,528	26-Jun-2001	
101	99RSS042 Differential Oscillator	Granted	09/386,957	25-Aug-1999	6,249,190	19-Jun-2001	
102	99RSS068 Peer-To-Peer Data Transfer Using Pre-Existing Caller ID Class FSK Signaling Infrastructure	Granted	09/328,048	08-Jun-1999	6,304,642	16-Oct-2001	
103	99RSS220 Method and Apparatus for Code Error Correction	Granted	08/131,078	01-Oct-1993	5,491,700	13-Feb-1996	PCSI P013US Exhibit 2
104	99RSS354 Apparatus and Method for Increasing Data Transmission Rate Over Wireless Communication Systems Using Spectral Shaping	Granted	099,080	28-Jul-1993	5,386,590	31-Jan-1995	
105	99RSS355 Apparatus and Method For Increasing Data Transmission Rate Over Wireless Communication Systems Using Spectral Shaping	Granted	147,148	03-Nov-1993	5,507,033	09-Apr-1996	
106	99RSS356 Apparatus and Method for Compensating for Limiter Induced Non-Linear Distortion in a Wireless Data Communication System	Granted	198,085	17-Feb-1994	5,533,048	02-Jul-1996	
107	99RSS357 Method of Internal Interference Cancellation in TDMA Receiver	Granted	09/495,993	01-Feb-2000	6,259,752	10-Jul-2001	

Patent No.	Patent Title	Status	Pub. No.	Pub. Date	Pub. No.	Pub. Date	Comments
108	97RSS372 (PTD)	Granted	09/163,789	30-Sep-1998	6,087,896	11-Jul-2000	
109	97RSS088 (PTD)	Granted	09/163,675	30-Sep-1998	6,292,046	18-Sep-2001	
110	95E117	Granted	08/752,016	19-Nov-1996	5,945,848	31-Aug-1999	
111	97RSS005	Granted	08/974,874	20-Nov-1997	6,192,134	20-Feb-2001	
112	97RSS006	Granted	08/920,846	29-Aug-1997	6,072,882	06-Jun-2000	
113	97RSS103	Granted	09/347,889	06-Jul-1999	6,104,304	15-Aug-2000	
114	97RSS460	Granted	09/021,896	11-Feb-1998	6,199,190	06-Mar-2001	
115	96RSS114	Granted	09/163,862	30-Sep-1998	6,157,693	05-Dec-2000	
116	96RSS258	Granted	09/405,964	30-Sep-1999	6,191,629	20-Feb-2001	
117	00CXT0105T	Granted	949,901	23-Sep-1992	5,250,826	05-Oct-1993	
118	83E050	Granted	727,958	26-Apr-1985	4,707,808	17-Nov-1987	
119	00CXT0191W	Granted	494,712	26-Jun-1995	5,517,530	14-May-1996	
120	95E082	Granted	08/568,843	07-Dec-1995	5,933,494	03-Aug-1999	
121	97RSS161	Granted	565,424	30-Nov-1995	5,774,013	30-Jun-1998	PCSI P046US Exhibit 2
122	95E062	Granted	08/568,095	06-Dec-1995	5,732,111	24-Mar-1998	

Patent Number	App Title	Status	App Number	File Date	Pat Number	Issue Date	Comment
123	95E063	Granted	09/447,536	10-Nov-1998	6,222,874	24-Apr-2001	
124	95E063	Granted	08/566,056	06-Dec-1995	5,799,034	25-Aug-1998	
125	95E089	Granted	08/568,210	06-Dec-1995	5,930,286	27-Jul-1999	
126	95E089	Granted	09/325,109	03-Jun-1999	6,307,902	23-Oct-2001	
127	97RSS297DIV	Granted	08/487,455	07-Jun-1995	5,594,758	14-Jan-1997	PCSI P008US3 Exhibit 2
128	98RSS178	Granted	09/222,866	29-12-1998	6,327,462	04-Dec-2001	
129	97RSS156DIV	Granted	08/472,125	07-Jun-1995	5,625,652	29-Apr-1997	PCSI P008US2 Exhibit 2
130	97RSS220	Granted	07/999,210	31-Dec-1992	5,376,894	27-Dec-1994	PCSI P008US Exhibit 2
131	98E005 (PTD)	Granted	09/006,972	14-Jan-1998	6,275,527	14-Aug-2001	
132	97RSS010 (PTD)	Granted	09/046,011	23-Mar-1998	6,331,782	18-Dec-2001	
133	95E074	Granted	08/569,489	07-Dec-1995	5,828,708	27-Oct-1998	
134	97RSS091	Granted	08/573,128	15-Dec-1995	5,661,762	28-Aug-1997	PHS
135	97RSS104	Granted	08/699,217	19-Aug-1996	5,848,992	15-Jul-1997	PHS

Patent Number	App Title	Status	App Number	Filing Date	Pat. Number	Issue Date	CONFIDENTIAL
136 97RSS155	Unique Word Detector and Method for Detecting a Unique Word with one of Several Windows Offset Time	Granted	08/472,121		5,524,127	04-Jun-1996	PHS
137 97RSS155	Phase detector and a method determining the phase of received PSK symbols	Granted	08/289,061	11-Aug-1994	5,610,949	11-Mar-1997	PHS
138 97RSS157	Method and Apparatus of Frequency Generation for use With Digital Cordless Telephone	Granted	08/013,625	04-Feb-1993	5,722,040	24-Feb-1998	PHS
139 97RSS159	ADPCM Coding and Decoding Techniques for Personal Communication Systems	Granted	08/191,609	04-Feb-1994	5,615,222	25-Mar-1997	PHS
140 97RSS165	Multiple Antenna Home Base for Digital Cordless Telephones	Granted	08/573,287	15-Dec-1995	5,696,798	09-Dec-1997	PHS
141 97RSS295	Method and Apparatus for Frequency Synthesization in Digital Cordless Telephones	Granted	08/131,210	01-Oct-1993	5,528,527	11-Jun-1996	PHS
142 97RSS454	Avoiding Interference From a Potentially Interfering Transmitter in a Wireless Communication System	Granted	09/163,972	30-Sep-1998	6,256,477	03-Jul-2001	PHS

DISPUTED WITH BOEING

00CXT0143W	Soft-Decision Classifier	Granted	988,384	09-Dec-1992	BOE	5,361,379	01-Nov-1994
00CXT0095W	Resistive Planar Ring Double-Balanced Mixer	Granted	576,755	04-Sep-1990	BOE	5,125,111	23-Jun-1992
00CXT0096N (PTD)	Large Array MMIC Feedthrough	Granted	540,964	08-Jun-1990	BOE	5,132,648	21-Jul-1992
00CXT0098T	Stress-Free Chemo-Mechanical Polishing Agent for II-VI Compound Semiconductor Single Crystals and Method of Polishing	Granted	787,154	04-Nov-1991	BOE	5,157,876	27-Oct-1992
00CXT0101T	Technique for Doping MOCVD Grown Crystalline Materials Using Free Radical Transport of the Copant Species	Granted	656,940	19-Feb-1991	BOE	5,202,283	13-Apr-1993
00CXT0108T	Technique for Doping Mercury Cadmium Telluride MOCVD Grown Crystalline Materials Using Free Radical Transport of Elemental Indium and Apparatus Therefor	Granted	657,692	19-Feb-1991	BOE	5,306,660	26-Apr-1994
00CXT0113W	Moving Vehicle Classifier with Nonlinear Mapper	Granted			BOE	5,337,257	09-Aug-1994
00CXT0084T	Isolated Low Power Thyristor Gate Drive Circuit	Granted	740,465	03-Jun-1985	BOE	4,631,472	23-Dec-1986
00CXT0113W	Large Time-Bandwidth Chirp Pulse Generator	Granted	102,924	06-Aug-1993	BOE	5,428,361	27-Jun-1995
00CXT0140N	Moving Vehicle Classifier With Iterative Deconvolver Estimator	Granted	76,077	11-Jun-1993	BOE	5,367,475	22-Nov-1994

IP SCHEDULE A

PATENT

REEL: 015755 FRAME: 0181

US Pending Patent Applications filed with USPTO

DATE NO	FILE	STATUS	APP NO	FILE DATE	COMMENT
00CXT0018W	VCO Tuning Curve Compensated Charge Pump Current Synthesizer	Pending	09/675,889	29-Sep-2000	
00CXT0040W	Power amplifier with provisions for varying operating voltage based upon power amplifier output power	Pending	09/792,860	23-Feb-2001	
00CXT0045W	Multi-Level Power Amplifier	Pending	09/880,286	13-Jun-2001	
00CXT0070W	IC Capacitor Trimming Using Fuses	Pending	10/121,366	11-Apr-2002	
00CXT0072W	Longitudinally-Coupled Saw Filter Enhanced Frequency Selectivity	Pending	09/545,232	07-Apr-2000	
00CXT0263W	System for Closed Loop Power Control Using a Linear or a Non-Linear Power Amplifier	Pending	09/650,987	30-Aug-2000	
00CXT0308W	Transmitter Architecture having a Secondary Phase- Error Correction Loop including an Amplitude Reconstruction System	Pending	09/650,831	30-Aug-2000	
00CXT0310W	System for Allowing a TDMA/CDMA Portable Transceiver to Operate with Closed Loop Power Control	Pending	09/665,820	20-Sep-2000	
00CXT0323W	Multiple Step Switched Translation Loop for Power Amplifier Feedback Control	Pending	09/666,698	21-Sep-2000	
00CXT0325W	Dual-Feedback Translation Loop	Pending	09/666,577	21-Sep-2000	
00CXT0350W	Dynamically Biased Power Amplifiers	Pending	09/818,285	27-Mar-2001	
00CXT0377W	Digitally Controlled Bias Circuit for Amplifiers	Pending	09/817,668	26-Mar-2001	
00CXT0464W	Amplification Control Scheme for a Receiver	Pending	09/823,488	30-Mar-2001	
00CXT0517W	Terminal Arrangement for an Electrical Device	Pending	09/677,162	02-Oct-2000	
00CXT0521W	A Charge Pump Having Reduced Switching Noise	Pending	09/718,963	21-Nov-2000	
00CXT0523W	Fast Settling Charge Pump	Pending	09/718,615	21-Nov-2000	
00CXT0526W	Fast-Acquisition Phase-Locked Loop	Pending		01-Nov-2001	
00CXT0608W	Power Amplifier Circuit for Providing Constant Bias Current Over a Wide Temperature Range	Pending	09/722,772	27-Nov-2000	
00CXT0618W	Current Mirror Compensation System for Power Amplifiers	Pending	09/772,717	30-Jan-2001	

00CXT0641W	Integrated on Chip VCO Calibration Algorithm to Extend VCO Tuning Range	Pending		02-Apr-2002		
00CXT0656W	Constant Current Biasing Circuit for Linear Power Amplifiers	Pending	09/693,398	21-Oct-2000		
00CXT0656W	Configurable Power Amplifier and Bias Control	Pending		05-Oct-2001		
00CXT0682W	Harmonic Cancellation Mixers	Pending	09/823,314	30-Mar-2001		
00CXT0718W	Bias Enhancement Circuit for Linear Amplifiers	Pending	09/715,871	15-Nov-2000		
00CXT0733W	Continuous Closed-Loop Power Control System Including Modulation Injection in a Wireless Transceiver Power Amplifier	Pending	09/704,930	02-Nov-2000		
00CXT0816W	Frequency Plan	Pending	09/823,680	30-Mar-2001		
01CXT0011W	Low Voltage Integrated Bandgap Reference for an HBT Linear Power Amplifier	Pending		30-Apr-2002		
01CXT0014W	Closed Loop Power Amplifier Control	Pending	60/280,709	30-Mar-2001		
01CXT0018W	Doherty Bias Circuit for Dynamically Compensating for Environmental Variations	Pending	09/951,788	10-Sep-2001		
01CXT0019W	Dual Mode PA Having a Common Controller	Pending	10/137,718	30-Apr-2002		
01CXT0149W	Systems for Controlling the Frequency of an Oscillator	Pending	09/823,316	30-Mar-2001		
01CXT0176W	System for Controlling the Amplitude of an Oscillator	Pending	09/823,285	30-Mar-2001		
01CXT0179W	Low Voltage Interface	Pending	09/823,681	30-Mar-2001		
01CXT0180W	Interference Reduction for Direct Conversion Receivers	Pending	09/823,313	30-Mar-2001		
01CXT0181W	Switched Capacitor for Analog Integrated Circuits	Pending	09/823,679	30-Mar-2001		
01CXT0201W	Method & Apparatus Multipath Signal Detection, Identification, and Monitoring for W-CDMA	Pending	60/275,032	12-Mar-2001		
01CXT0202W	Method & Apparatus for Spread Spectrum Radio Signal Recovery in Wideband Spread Communication Systems	Pending	60/275,192	12-Mar-2001		

01CXT0235W	A Stable Epitaxial Emitter Ballast for Heterojunction Bipolar Transistors (HBTs) by Modulated Emitter Cap Doping Profile	Pending	10/115,317	03-Apr-2002		
01CXT0341T (PTD)	A Low Current and High Performance BiFET Low Noise Amplifier (LNA)	Pending	10/057,098	23-Jan-2002		
95E080	Method and Apparatus for Signal Quality Estimation in a Direct Sequence Spread Spectrum...	Pending	568,330	06-Dec-1995		
95E082	Echo Cancelling Method and Apparatus in a Communication Device	Pending	09/220,123	23-Dec-1998		
95E088	12 Chip Coded Spread Spectrum Modulation for Direct Conversion Radio Architectures in a Digital Cordless Telephone	Pending				
96E085	Intermediate Frequency Amplifier Circuit for Use in Receive and Transmit Modes	Pending	08/752,767	20-Nov-1996		
97RSS006	Method and Apparatus for Sensing an Audio Signal That is Sensitive to the Audio Signal and Insensitive to Background Noise	Pending	09/491,726	27-Jan-2000		
97RSS057	Multiple Half-Burst Phase Rotation for High Frequency Demodulation	Pending	09/164,534	30-Sep-1998		
97RSS063	Power Amplifier Saturation Prevention Method, Apparatus and Communication System Incorporating the Same	Pending	09/152,439	14-Sep-1998		
97RSS068	Device and Process for Coupling Multi-Band Transmitters and Receivers and Communication System Employing Same	Pending	09/160,677	11-Oct-2000		
97RSS069	Using a Low Frequency Timer to Restore Timing to a High Frequency Timer	Pending	09/164,432	30-Sep-1998		
97RSS073	A System and Method for Extending the Range of a Base Unit	Pending	09/072,198	31-Jul-2000		
97RSS101	Automatic GSM Mobile Power	Pending	09/161,611	25-Sep-1998		
97RSS107	Apparatus for and Method of Improving Efficiency of Transceivers in Radio Products	Pending	09/672,235	27-Sep-2000		

97RSS108	Cellular Telephone Keypad/Acoustic Isolation Device/LCD	Pending	09/408,986	29-Sep-1999		
97RSS115	Dual Mode Phone Architecture Utilizing a Single Transmit-Receive Switch	Pending	09/013,235	26-Jan-1998		
97RSS118CIP (MCD)	RF Architecture for Cellular Dual Band Telephones	Pending	09/386,257	31-Aug-1999		
97RSS124	Method and System for Efficiently Transmitting Energy from an RF Device	Pending	09/381,865	27-Jul-1999		
97RSS125	Wide Frequency Range Couplers and Detectors for Power Detection in Multiple Frequency Band Systems	Pending	09/081,702	20-May-1998		
97RSS127	Integrated Power Management Module	Pending	09/060,426	14-Apr-1998		
97RSS178	System and Method for Receiving A Signal	Pending	09/409,484	30-Sep-1998		
97RSS178/1	System and Method for Receiving A Signal	Pending	10/033,203	26-Dec-2001		
97RSS203	Device, System and Method for Low Noise Radio Frequency Transmission	Pending	09/160,690	25-Sep-1998		
97RSS205	Device and Process for Coupling Multi-Band Transmitters and Receivers and Communication System Employing Same	Pending	09/160,646	25-Sep-1998		
97RSS206	System and Process for Shared Frequency Source Multi-Band Transmitters and Receivers	Pending	09/203,645	01-Dec-1998		
97RSS329	Switchable High-Low Side Mixer for the Translational IC for Dual Mode Application	Pending	09/295,036	20-Apr-1999		
97RSS331	First Party Signalling for Call Waiting Calling Number Identification (CIVCID) System	Pending	08/923,447	04-Sep-1997		
97RSS336	Direct Conversion Time Division Duplex, Radio, Direct Sequence Spread Spectrum Cordless Telephone	Pending	09/107,733	30-Jun-1998		
97RSS338	Techniques to Increase Data Transmission Rate of Spread Spectrum Communications Systems	Pending	09/325,107	03-Jun-1999		
97RSS347	System and Method for Wireless Voice and Computer Communications	Pending	09/300,582	27-Apr-1999		
97RSS347	System and Method for Wireless Voice and Computer Communications	Pending	09/974,769	08-Oct-2001		

97RSS362	Variable Gain Amplifier with High Linearity and Low Noise	Pending	09/163,892	30-Sep-1998		
97RSS371	Method for Programming a Radio Transceiver	Abandoned	09/410,217	30-Sep-1999		
97RSS392	Adaptive Wireless Communication Receiver	Pending	09/163,797	30-Sep-1998		
97RSS449	Traffic Channel Quality Estimation from a Digital Control Channel	Pending	09/046,060	23-Mar-1998		
97RSS450	Power Management System for a Mobile Station	Pending	09/489,911	08-Feb-2000		
97RSS460	Convolution Decoding Terminated by an Error Detection Block Code with Distributed Parity Bits	Published	09/761,259	16-Jan-2001		
97RSS481	Enhanced Keypad Control for Portable Communication Device	Pending	09/073,724	07-May-1998		
CASE ABANDONED						
97RSS483	External Connector and Battery Extension Pack for a Portable Communication Device	Pending	09/137,194	20-Aug-1998		
97RSS494	Power Management System for a Mobile Unit by Reduced Neighbor Cell Scanning	Pending	09/084,534	26-May-1998		
97RSS503	Full-Duplex Speakerphone With Wireless Microphone	Pending	09/144,842	31-Aug-1998		
97RSS518	Turbo Decoding of Trellis-Coded Modulations	Pending	09/410,220	30-Sep-1999		
98RSS014	A Compact and Fast Cubic Function Generator	Pending	09/706,483	03-Nov-2000		
98RSS024	Method of and Apparatus for Adaptation of an Echo Canceller in a System With Multitasking	Pending	09/128,362	03-Aug-1998		
98RSS031	A Method and Apparatus for Dynamically Switching Between Speech Coding Techniques	Pending	09/153,631	15-Sep-1998		
98RSS064	System and Method for Providing a Trap and Patch Function to Low Power, Cost Conscious, and Space Constrained Applications	Pending	09/232,881	15-Jan-1999		
98RSS069	System and Method for Communication Parameter Determination	Pending	09/157,209	18-Sep-1998		
98RSS072	Apparatus and Method for Improving Power Control Loop Linearity	Pending	09/161,014	25-Sep-1998		

98RSS088	Multi-Line Recording Device Having Reduced Processing and Storage Requirements	Pending	09/163,263	29-Sep-1998		
98RSS097	Wireless Communications Device Allowing a Soft Handoff Procedure in a Mobile Communications System	Pending	09/342,165	28-Jun-1999		
98RSS112	Dynamic Range Reduction Circuitry for a Digital Communications Receiver	Pending	09/159,328	23-Sep-1998		
98RSS121	Frequency Hopping Spread Spectrum Modulation and Direct Sequence Spread Spectrum Modulation Cordless Telephone	Pending	09/137,198	20-Aug-1998		
98RSS165	A Method to Generate a Secure Wireless Link Between a Handset and Basestation in a Cordless Phone System Without Any Wired Connection ...	Pending	09/216,380	18-Dec-1998		
98RSS177	Method and Apparatus for Saving Power During Punctured Transmission of Mobile Communications	Pending	09/205,523	03-Dec-1998		
98RSS179	System and Method for Selecting Amplifiers in a Communications Device	Pending	09/222,063	29-Dec-1998		
98RSS180	System and Method for Selectively Interconnecting Amplifiers in a Communications Device	Pending	09/222,672	29-Dec-1998		
98RSS180	System and Method for Selectively Interconnecting Amplifiers in a Communications Device	Pending		23-Oct-2001		
98RSS189	Secondary Automatic Gain Control Loops for Direct Conversion CDMA Receivers	Pending	09/382,862	25-Aug-1999		
98RSS201	System and Process for Shared Functional Block CDMA and GSM Communication Transceivers	Pending	09/298,315	23-Apr-1999		
98RSS207	Method and System Which Uses Sound Wave Based Communication to Generate A Secure Wireless Link Between A Handset and Base Station	Pending	09/216,086	18-Dec-1998		
98RSS257	Programmable relaxation oscillator	Pending	09/302,754	30-Apr-1999		
98RSS276	System and Method for Amplifying a Cellular Radio Signal	Pending	09/408,754	30-Sep-1999		
98RSS280	Power Amplifier Driver System for Wireless Handset	Pending	09/328,927	09-Jun-1999		
98RSS283	Method and Apparatus for Filter Selection From Frequency Synthesizer	Pending	09/392,825	09-Sep-1999		

99RSS291	Mixer Circuit with On-Chip Transformer	Pending	09/371,311	10-Aug-1999		
99RSS321	Dual Tune Input-Integrated VCO on a Chip	Pending	09/706,921	08-Nov-2000		
99RSS347	Performance Improvement of Internet Protocols Over Wireless Connections	Pending	09/375,607	17-Aug-1999		
99RSS350	Programmable Frequency Divider	Pending	09/370,089	08-Aug-1999		
99RSS350	Frequency Divider with Low Harmonics	Pending	09/821,833	30-Mar-2001		
99RSS351	Packaging of Surface Acoustic Wave (SAW) Filters on a Multi Chip Module	Pending	09/455,108	08-Dec-1999		
99RSS396	Multi-Band Transceiver Having Multi-Slot Capability	Pending	09/387,038	31-Aug-1999		
99RSS012	System and Method for Cordless Telephonic Communication	Pending	09/756,039	05-Jan-2001		
99RSS025	System and Method For Variable Gain Coder-Decoder	Pending	09/845,840	30-Apr-2001		
99RSS030	Multi-Band Filter System For Wireless Communication Receiver	Pending	09/330,846	11-Jun-1999		
99RSS032	Differential Oscillator	Pending	09/675,239	29-Sep-2000		
99RSS037	System and Method for Receiving Analog and Digital Signals	Pending	09/383,122	25-Aug-1999		
99RSS057	A Method to Combine the Detection of Stutter Dial Tone and VMW FSK Signals Integrated Within a Telephone	Pending	09/299,202	23-Apr-1999		
99RSS065	Direct Conversion Receiver	Pending	09/260,919	02-Mar-1999		
99RSS065	Direct Conversion Receiver	Pending	09/386,865	31-Aug-1999		
99RSS069	Peer-to-Peer Data Transfer Using Pre-existing Caller ID Class FSK Signaling Infrastructures	Pending	09/363,989	16-Jul-1999		
99RSS077	Local Area Internet Radio Receiver/Transmitter	Pending	09/407,149	28-Sep-1999		
99RSS078	System for Controlling a Class D Amplifier	Pending	09/814,196	21-Mar-2001		
99RSS082	System and Process for Supporting Multiple Wireless Standards With a Single Circuit Architecture	Pending	09/410,136	30-Sep-1999		
99RSS083	System for Using Adaptive Circuitry To Improve Performance and Provide Linearity and Dynamic Range on Demand	Pending	09/881,932	15-Jun-2001		

99RSS085	Avoiding Interference from a Potentially Interfering Transmitter in a Wireless Communication	Pending	09/394,189	13-Sep-1999		
99RSS085	Wireless Communications System Utilizing Directional Wireless Communication Device	Pending	09/821,110	29-Mar-2001		
99RSS086	Cellular Handset with Adjustable Analog to Digital Conversion	Pending	09/410,205	30-Sep-1999		
99RSS086	Preprocessor and related frequency translator	Pending	09/261,056	02-Mar-1999		
99RSS086	Preprocessor and related frequency translator	Pending	09/386,956	27-Aug-1999		
99RSS102	Direct Sequence Spread Spectrum System with Enhanced Forward Error Correction Code Feature	Pending	09/427,041	19-Oct-1999		
99RSS116	System and Method for Achieving Wireless Communications Coverage in a Local Area	Pending	09/409,297	29-Sep-1999		
99RSS130	Hands-Free Activation of a Personal Communication Device	Pending	09/398,909	15-Sep-1999		
99RSS136	Simple Dual Band Multi Time Slot Receiver Architecture	Pending	09/392,355	08-Sep-1999		
99RSS138	System of and Method for Compensating a Baseband Signal to Reduce Third Order Modulation Distortion	Pending	09/515,538	29-Feb-2000		
99RSS161	A Novel Technique for Generation of Phase/Frequency Modulated Signals	Pending	09/398,911	14-Sep-1999		
99RSS172	A Balun Circuit for Combining Differential Power Amplifier Outputs	Pending	09/386,863	31-Aug-1999		
99RSS174	A GmC Filter and Method for Suppressing Unwanted Signals Introduced by the Filter	Pending	09/663,848	18-Sep-2000		
99RSS199	Method and Apparatus for Multiple Phase Splitting for Dual Band IQ Subharmonic Mixer	Pending	09/506,302	17-Feb-2000		
99RSS213	Dynamically Varying Linearity System for an RF Front-End of a Communication Device	Pending	09/811,082	19-Mar-2001		
99RSS232	Smart Current System for Dynamically Varying the Operating current of a Frequency Source in a Receiver	Pending	09/793,744	26-Feb-2001		
99RSS297	System of and Method for Reducing or Eliminating the Unwanted Sideband in the Output of a Transmitter Comprising a Quadrature Modulator Followed by a	Pending	09/514,501	29-Feb-2000		

	Translational Loop						
99RSS301	DMA Channel for High-Speed Asynchronous Data Transfer	Pending	09/550,852	17-Apr-2000			
99RSS305	Doherty Power Amplifier With Integrated Quarter Wave Transformer/Combiner Circuit	Pending	09/761,100	16-Jan-2001			
99RSS310	System of and Method for Reducing or Eliminating the Unwanted Sideband in a Signal Derived from the Output of a Quadrature Modulator	Pending	09/515,633	29-Feb-2000			
99RSS313	System for Associating User Selectable Information in Wireless Devices	Pending	09/842,601	26-Apr-2001			
99RSS316	Power Amplifier Saturation Detection and Compensation	Pending	09/651,801	30-Aug-2000			
99RSS358	Low-Power Method of Interference Cancellation in GSM Receiver	Pending	09/634,081	08-Aug-2000			
99RSS359	Signal Detector With Echo Cancellation	Pending	09/792,816	23-Feb-2001			
99RSS386	High Efficiency Multiple Power Level Amplifier	Pending	09/686,440	10-Oct-2000			
99RSS397	Non-Linear Transistor Circuits with Thermal Stability	Pending	09/871,517	31-May-2001			
99RSS404	System for Using a Local Wireless Network to Control a Device Within Range of the Network	Pending	09/658,729	11-Sep-2000			
99RSS405	System For Eight Phase 45 Degree Polyphase Filter With Amplitude Matching	Pending	09/666,501	18-Sep-2000			
99RSS408	Polyphase Tuning for Phase Calibration	Pending	09/823,299	30-Mar-2001			
99RSS410	Stacked Core Half-LO Mixer	Pending	09/668,879	25-Sep-2000			
99RSS422	Temperature Compensation Module	Pending	09/712,771	13-Nov-2000			
99RSS426	Trap and Patch System for Virtual Replacement of Defective Volatile Memory Cells	Pending	09/671,401	27-Sep-2000			
99RSS429	Zero-Overhead Symbol Rate Adaptation System for OVSF Code	Pending	09/718,985	22-Nov-2000			
99RSS461	Programmable Silent Ringing System for Communication Systems	Pending	09/650,543	30-Aug-2000			
99RSS468	System and Apparatus for a Direct Conversion Receiver and Transmitter	Pending	09/621,407	21-Jul-2000			

99RSS479	Even Order Non-Linearity Correction Feedback for Gilbert Style Mixers	Pending	09/811,133	16-Mar-2001		
99RSS497	Adaptive Configurable Demodulation System With Multiple Operating Modes	Pending	09/710,764	08-Nov-2000		
99RSS498	Digital Phase/Frequency Detector	Pending	09/799,822	05-Mar-2001		
99RSS500	Burst Detector	Pending	09/735,369	12-Dec-2000		
99RSS504	Variable-Slope Variable-Gain Amplifier	Pending	09/686,669	11-Oct-2000		
97RSS010	Methods for Wireless Testing of Integrated Circuits (PTD)	Pending	09/784,689	16-Jan-2001		
98RSS266	Electrostatic Discharge Protection Circuit (PTD)	Pending	09/738,127	15-Dec-2000		
00CXT0290T	Improved Transformer Comprising Stacked Inductors (PTD)	Pending	09/797,307	01-Mar-2001		
00CXT0335T	On-Chip Transformers (PTD)	Pending	09/779,402	08-Feb-2001		
00CXT0622W	Systems for Testing Integrated Circuits	Pending	09/622,920	30-Mar-2001		
97RSS064	A Transistor Having a Novel Layout and an Emitter Having More Than One Feed Point	Pending	09/687,381	13-Oct-2000		
97RSS064	Transistor Layout CIP	Pending	09/661,921	21-May-2001		
97RSS313	Method and Apparatus for Detecting and Correcting Errors Using Cyclic Redundancy Check	Pending	09/103,421	24-Jun-1998		
97RSS465	Envelope Feedforward Technique with Power Control for Efficient Linear RF Power Amplification	Pending	09/108,628	01-Jul-1998		
98RSS275	RF Transmitter with Extended Efficient Power Control Range	Pending	09/482,121	11-Jan-2000		
99RSS013	RF Transmitter with Extended Efficient Power Control Range	Pending	09/481,094	11-Jan-2000		

99RSS133	Apparatus and Method for Connecting a Cellular Telephone to a Universal Serial Bus	Pending	09/312,064	13-May-1999		
99RSS339	Electrical Contact for Compound Semiconductor Device and Method For Forming Same	Pending	09/675,023	28-Sept-2000		
99RSS385	Electrical Contact for Compound Semiconductor Device & Method for Forming Same	Pending	09/580,323	26-May-2000		
00CXT0289T	Modified HBT Collector Design for Increased Robustness	Pending	10/034,880	26-Dec-2001		
99RSS143	Electrostatic Discharge Protection Circuit	Pending	09/519,214	3-Mar-2001		
99RSS081	Critical Path Adaptive Power Control	Pending	09/814,921	22-Mar-2001		
97RSS475	Polarization-Adaptive Antenna Transmit Diversity System	Pending	09/103,417	24-Jun-1998		
97RSS477	Using Channel Loading Statistics to Determine Whether to Search for a New Channel in a Wireless Communication System	Pending	09/201,076	30-Nov-1998		
98RSS173	Muting Method for ADPCM Coded Speech Signal Without Performance Critical Threshold Comparisons	Pending	09/234,243	20-Jan-1999		
98RSS284	Efficient Accurate Controller for Envelope Feedforward Power Amplifiers	Pending	09/409,818	30-Sep-1999		
99RSS133	Apparatus and Method for Connecting a Cellular Telephone to a Universal Serial Bus	Pending	09/312,064	13-May-1999		
99RSS292	Device-Independent Interface for Embedded Telephone Applications to a Wireless Spread-Spectrum Link and Audio Processor	Pending	09/479,127	07-Jan-2000		
00CXT0430IW	System and Method for Processing Audio and Video Data in a Wireless Handset	Pending	09/631,511	03-Aug-2000		
00CXT0431IW	System and Method for Processing Audio and Video Data in a Wireless Handset	Pending	09/631,508	03-Aug-2000		
96E005/1	Pre-Quantization in Motion Compensated Videocoding	Pending	09/828,535	06-Apr-2001		

Innovation Disclosures

Innovation Disclosures Assigned to Outside Counsel in Progress

Innovation ID	Innovation Title	Client	Disclosures	Comments
00CXT0070W	IC Capacitor Trimming Using Fuses	WCD	Assigned	
00CXT0284W	Integrated Auto-Calibration of On Chip VCOs by Winner Selection Algorithm, and Sensitivity Adjustment by Coarse and Fine Varactor Tuning	WCD	Assigned-FBD	
00CXT0522W	Differential-Phase-Locked-Loop Method of Non-Constant Envelope Modulation	WCD	Assigned	
00CXT0616W	Adaptive Matching System	WCD	Assigned	
00CXT0617W	Desensitizing Bias Circuit to Prevent RF Interaction	WCD	Assigned	
00CXT0620W	Method for Determining Quality of Dielectric Laminated Substrate	WCD	Assigned	
00CXT0641W	Integrated on Chip VCO Calibration Algorithm to Extend VCO Tuning Range	WCD	Assigned	
01CXT0001W	Using Trimming Hooks to Integrate a Circuit that can Calibrate the VCO Independently Each Time at Circuit Power-up	WCD	Assigned	
01CXT0002W	Monitor the Varactor Capacitance of the Process Thru Process Control Monitor	WCD	Assigned	
01CXT0164W	Supply & Bias Enhancements for Conexant Power Amplifiers. Second Generation PMIC/PA	WCD	Assigned-FBD	
97RSS398	An Current Compensation Method in Replica Circuit Where the Product of the Transconductance and Resistance is Kept Constant	WCD	Assigned	
99RSS079	Method for Implementing a Radio Transceiver with Digital Signal Processing	WCD	Assigned	
99RSS128	Capacitor Calibration for Current-Tuned Filters	WCD	Assigned	
99RSS200	A Multiple Cascaded DS Offset Correction Scheme is Proposed For Use in Connecting Offsets Caused By Multiple Cascaded High Gain Baseband Stages in Transceivers.	WCD	Assigned	
99RSS246	Switchable Gain Low Noise Amplifier for DCMA/AMPS Applications	WCD	Assigned	
99RSS409	Gain Calibration for Linearity-On-Demand Systems	WCD	Assigned	
00CXT0254W	Power Amp Control Driver Providing Over-Current Protection and	WCD	Assigned-FBD	

INVENTOR		TITLE		CLASS	DESCRIPTION	COMMENTS
00CXT0524W	Control Linearization			WCD	Assigned-FBD	
00CXT0524W	Feedback and Feedforward Closed-Loop Power Control for Amplitude-Modulated Signals			WCD	Assigned	
02CXT0004W	Software Defined Transmitter for WCDMA, GSM, EDGE, TDMA			WCD	Assigned	
01CXT0288W	GSM/GPRS Transmitter with Synchronous Oscillator Feed			WCD	Assigned	
01CXT0294W	CMOS/BiCMOS Subharmonic Mixer for Improved Noise, Power Consumption and Headroom			WCD	Assigned	
01CXT0355W	Use of NiV as Seed Layer for Electroplated Backside Metalization of III-V Semiconductor Circuits			WCD	Assigned	

IP SCHEDULE A

Innovation Disclosures on Hold Status

INVENTION TITLE	CLASS	DISCLOSURE STATUS	COMMENTS
00CXT0052W High Performance Dual Tone Detections in Presence of Speech Interference	WCD	Hold/Assigned	
00CXT0054W Gated Li-Ion Battery Charger	WCD	Hold-FBD/Assigned	
00CXT0262W Power Amplifier Saturation Control Scheme	WCD	Hold-FBD/Assigned	
00CXT0463W A GFSK/GMSK Detector with Enhanced Reception Performance in Cochannel Interference and A WGN Channels	WCD	Hold-FBD/Assigned	
00CXT0471W Voltage Regulator Based on Switchable Capacitor Array or Simply "Digital LDO"	WCD	Hold-FBD/Assigned	
00CXT0535W Variable RF Gain Power Amplifier Driver	WCD	Hold-FBD/Assigned	
00CXT0538W Programmable Dual Power Amplifier Drivers for CDMA Applications	WCD	Hold-FBD/Assigned	
00CXT0685W A FEET/HBT Bias Circuit for Low Voltage Wireless Handsets	WCD	Hold-FBD	
00CXT0699W High Efficiency Adaptable Power Amplifier	WCD	Hold-FBD	
00CXT0767W DC Offset Correction Technique for a Direct Conversion Bluetooth Receiver	WCD	Hold-FBD	
00CXT0773W A Design Technique for the Thermal Stabilization of Wireless Power Amplifiers	WCD	Hold-FBD	
00CXT0796W Phase Detector Based Upon Look-Up Table	WCD	Hold-FBD	
00CXT0798W Fast Response Low if Filter Using Differential Peak Detectors	WCD	Hold-FBD	
00CXT0805W Acquisition Speed Up for Fractional-N Synthesizers	WCD	Hold-FBD	
00CXT0807W Resistor Calibration Algorithm	WCD	Hold-FBD	
01CXT0015W Calibration of Device Using a Linear PA	WCD	Hold/Assigned	
01CXT0017W Optimum Doherty Topology for Broad Bandwidth	WCD	Hold/Assigned	
01CXT0063W New Self Oscillating AM Modulator	WCD	Hold-FBD	
01CXT0100W Linearized Split-Balasting Technique for Power Amplifiers	WCD	Hold-FBD	
01CXT0123W Dynamically Tuned Embedded Antenna	WCD	Hold-FBD	
01CXT0156W An Algorithm for Removing Unwanted DC Offset from the Desired Signal at the GSM Receiver	WCD	Hold-FBD	
01CXT0167W Detection of Strong Unwanted Interfering Signals in Direct	WCD	Hold-FBD	

IP SCHEDULE A

Patent No.	Abstract	Class	IPC Class	IPC Class	IPC Class	IPC Class	IPC Class
01CXT0188W	Conversion Receiver for GSM RF Power Splitter/Quadrature Phase Shifter Circuit	WCD	Hold-FBD				
01CXT0199W	Dynamic Parameter Setting Based on Surrounding Environment for Low-Complexity Speech Recognition	WCD	Hold-FBD				
01CXT0233W	Technique to Reduce Dynamic Range Requirement in a DCR	WCD	Hold-FBD/Assigned			Combined w/01CXT0236W	
01CXT0247W	A New Method and Architecture on the Efficient Hardware Implementation of Forward and Backward Recursive Computation	WCD	Hold-FBD				
01CXT0249W	A Multiplier-less Joint Frequency and Code Acquisition for a WCDMA System	WCD	Hold-FBD				
01CXT0250W	An Efficient Interleaver/Deinterleaver Design for the Turbo Decoder in a 3G WCDMA System	WCD	Hold-FBD				
01CXT0251W	A Method for Modifying the Saturation Point of a Power Amplifier for Improved Linearity	WCD	Hold-FBD				
01CXT0274W	Channel Estimator for WCDMA	WCD	Hold-FBD				
01CXT0295W	Ripple Effects Reduction Techniques for Use in Switching Regulator for Cellular Phone PA Power Supply	WCD	Hold-FBD				
01CXT0308W	Adaptive Linearity Control for 3G DCR Handset Radio	WCD	Hold-FBD				
01CXT0311W	Method for Linearizing VCO Gain	WCD	Hold-FBD				
01CXT0315W	VSWMR Protected Bias Circuit for Power Amplifiers in Handset Applications	WCD	Hold-FBD				
01CXT0317W	A Novel Modular Solution to Connected Word Recognition with Reduced Computational and Memory Requirements Suitable for Hand-Held Devices	WCD	Hold-FBD				
01CXT0329W	Improving Wafer Yield During Wafer Scribe and Break by Adding a Protective Film at Die Corners	WCD	Hold-FBD				
01CXT0344W	An Improved Output Matching and Combining Circuit for Doherty Power Amplifiers	WCD	Hold-FBD				

01CXT0353W	Highly Accurate 45 Signal Generation with Application to Sub-Harmonic Mixers	WCD	Hold-FBD		
02CXT0003W	Switchable VCO Performance in a Time-Division Duplex Communication Device	WCD	Hold-FBD		
02CXT0005W	Varying VCO Performance as Receiver Signal Level Changes	WCD	Hold-FBD		
99RSS387	System for Manufacturing a Stacked Plate Capacitor Having an Interconnect Metal Plate	WCD	Hold-FBD/Assigned		
99RSS423	Improvements to Linearization and High Efficiency in Power Amplifiers	WCD	Hold-FBD/Assigned		
99RSS424	Linearization Improvement in Pas using Split Stage Bias	WCD	Hold-FBD/Assigned		
99RSS486	Blue Tooth I/O Saving Front End Architecture and Device	WCD	Hold-FBD/Assigned		
01CXT0329W	Improving Wafer Yield During Wafer Scribe and Break by Adding a Protective Film at Die Corners	WCD	Hold-FBD		

IP SCHEDULE A

Innovation Disclosures/CLOSED Status

		CLASS	DISCLOSURE	COMMENT
99RSS494	Bandgap Regulator Using Capacitive Charge Pumping	MCD	CLOSED	
00CXT0251W	Symmetrically-Driven Oscillator with Vertical Structure	MCD	CLOSED	
00CXT0623W	Power Amplifier Having Tunable Bias Ports	MCD	CLOSED	
00CXT0633W	Noise Coupling Reduction Scheme in Dual Fractional-N/Integer-N Frequency Synthesizer	MCD	CLOSED	
00CXT0635W	Quantization Noise Reduction Delta-Sigma Fractional-N Frequency Synthesizer with Digital Frequency Doubler	MCD	CLOSED	
00CXT0636W	Improved Modulus Control Method in Frequency Synthesizers with an Embedded D Flip-Flop	MCD	CLOSED	
00CXT0799W	Feedforward LNA/Mixer Switched Gain Block Architecture	MCD	CLOSED	
00CXT0809W	High Q Digital Varactor	MCD	CLOSED	
01CXT0038W	A CMOS RF Programmable Driver for Bluetooth Applications	MCD	CLOSED	
01CXT0154W	Dynamic Calibration of a Block in a GSM Power Amplifier Control (PAC) Loop with Application to a Block that Converts the Linear Scale to Log Scale	MCD	CLOSED	
01CXT0299W	Active Temperature Compensated RF Power Detector	MCD	CLOSED	
01CXT0306W	New Phase Detection and Lock Circuit for Clock/Freq Synthesis	MCD	CLOSED	
01CXT0310W	A PLL and Clock Independent VCO Tuning Calibration Algorithm	MCD	CLOSED	
01CXT0342W	An Algorithm for Automated Receiver Static Sensitivity Measurement	MCD	CLOSED	

Innovation Disclosures not yet ranked

CaseNumber	InvTitle	DisclosureStatus	Inventor
96E042	Current Limiting Low Impedance Active Bias Circuit for Power Amplifiers	No Rank	P Lau
01CXT0256W (PTD)	Optimized Layout for Power Amplifier Arrays to Reduce Peak Junction Temperature	No Rank	
01CXT0354W (PTD)	MCM Construction Using Capped Buried Vias for Heat Transfer and Lower Inductance	No Rank	
02CXT0007W (PTD)	A New BIFET Gilbert-cell Mixer	No Rank	
02CXT0011W	Audio-Video Synchronization to Support Streaming in UMTS Multimedia Messaging Service (MMS)	No Rank	
02CXT0020W	Integration of an Implanted Lateral PNP with a Double Hetero-Junction NPN in an Existing InGaP/GaAs HBT Process Technology	No Rank	
02CXT0022W	Method for Forming a Compact R-C-R Bypass Network for Device Ballasting	No Rank	

IP SCHEDULE A

02CXT0025W
 Programmable 8/9 and 8/17 High Frequency Prescaler with a Optimized Supply Current
 No Rank

02CXT0026W
 A Layout Technique Reducing Bipolar Transistor External Base Resistance
 No Rank

02CXT0030T
 Design of a Lateral Metal Insulator Metal Capacitor in a Semiconductor Metal Process to Meet Density and Slotting Rules
 No Rank

02CXT0040W
 Optimized Double Heterostructure Bipolar Transistor Design
 No Rank

02CXT0041W
 A Technique to Efficiently Allocate Computational Resources in a Digital Communication System
 No Rank

Trademarks

Trademark Name	Country/Name	Trademark Status	Client	Applicant	Next Steps/Comments
SYSTEM SMART	United States of America	Published	WCD		
SYSTEM SMART	European Community	Pending	WCD		
SYSTEM SMART	Japan	Pending	WCD		