

PATENT ASSIGNMENT

Electronic Version v1.1

Stylesheet Version v1.1

SUBMISSION TYPE:

NEW ASSIGNMENT

NATURE OF CONVEYANCE:

Termination and Release of Intellectual Property Security

CONVEYING PARTY DATA

Name	Execution Date
WK Technology Fund	03/17/2005
WK Technology Fund IV	03/17/2005
WK Technology Fund V	03/17/2005
WK Technology Fund VI	03/17/2005
WK Technology Fund VII	03/17/2005
WK Technology Fund VIII	03/17/2005
WK Global Investment Limited	03/17/2005
WK Global Investment II Limited	03/17/2005
WK Global Investment III Limited	03/17/2005
Sequoia Capital IX	03/17/2005
Sequoia Capital IX Principal's Fund	03/17/2005
Sequoia Capital Franchise Fund	03/17/2005
Sequoia Capital Franchise Partners	03/17/2005
Focus Ventures II, L.P.	03/17/2005
Focus Ventures II, A, L.P.	03/17/2005
Focus Ventures II, QP, L.P.	03/17/2005
Saiyed Atig Raza and Noreen Tirmizi Raza, Trustees N&A Raza Revocable Trust	03/17/2005
Sequoia Capital Entrepreneurs Annex Fund as Successor in Interest to Sequoia Capital Entrepreneurs Fund	03/17/2005

RECEIVING PARTY DATA

Name:	Zeevo, Inc.
Street Address:	2500 Condensa Street
City:	Santa Clara
State/Country:	CALIFORNIA
Postal Code:	95051

PROPERTY NUMBERS Total: 26

Property Type	Number
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PATENT

500023811

REEL: 015788 FRAME: 0327

CH \$1040.00 6420934

Patent Number:	6420934
Patent Number:	6445258
Patent Number:	6489688
Patent Number:	6570450
Patent Number:	6686665
Patent Number:	6710425
Patent Number:	6747521
Patent Number:	6750715
Patent Number:	6781445
Patent Number:	6782247
Patent Number:	6791374
Patent Number:	6844792
Patent Number:	6492716
Application Number:	10101676
Application Number:	09837897
Application Number:	09813420
Application Number:	09858812
Application Number:	10101405
Application Number:	10101396
Application Number:	09618971
Application Number:	10233237
Application Number:	10262336
Application Number:	10623084
Application Number:	10613346
Application Number:	10826460
Application Number:	10826464

#### CORRESPONDENCE DATA

Fax Number: (310)203-7199  
*Correspondence will be sent via US Mail when the fax attempt is unsuccessful.*  
 Phone: 3102771010  
 Email: rwitter@irell.com  
 Correspondent Name: Irell & Manella LLP  
 Address Line 1: 1800 Avenue of the Stars  
 Address Line 2: Suite 900  
 Address Line 4: Los Angeles, CALIFORNIA 90067

NAME OF SUBMITTER:

Rachele Wittwer

**PATENT**

**REEL: 015788 FRAME: 0328**

**Total Attachments: 9**

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**TERMINATION AND RELEASE**  
**OF**  
**INTELLECTUAL PROPERTY SECURITY**

This Termination and Release of Intellectual Property Security (this "Termination") is executed by **the secured parties on the signature page hereto** (the "Secured Parties") in favor of **Zeevo, Inc.**, a Delaware corporation (the "Debtor"), and is effective as of March 17, 2005.

**RECITALS**

A. WHEREAS, Debtor and Comdisco, Inc. entered into that certain Reimbursement and Security Agreement dated as of November 16, 2000, as amended by that certain Amendment No. 1 to Reimbursement and Security Agreement dated as of July 2, 2001 and that certain Letter Agreement dated as of February 24, 2003 (the "Reimbursement Agreement").

B. WHEREAS, Comdisco Ventures, Inc. ("Comdisco"), is the successor in interest to Comdisco, Inc.

C. WHEREAS, Debtor and Comdisco entered into that certain Restructuring Agreement dated as of July 16, 2003 (the "Restructuring Agreement"), that restructured certain payments due under the Reimbursement Agreement.

D. WHEREAS, in connection with the Restructuring Agreement, Debtor and Comdisco entered into a Reaffirmation and Grant of Security Interest in Patents and Trademarks, dated as of July 16, 2003 (the "Security Agreement"), which Security Agreement was recorded with the United States Patent and Trademark Office at Reel 014462, Frame 0200 with respect to patents, and Reel 2805, Frame 0759 with respect to trademarks, for the purpose of securing payment and performance of Debtor's obligations under the Restructuring Agreement.

E. WHEREAS, pursuant to the Security Agreement, Debtor pledged and granted to Comdisco a security interest in Debtor's entire right, title and interest in and to Debtor's intellectual property, including without limitation the patents and patent applications identified on Schedule A and the trademarks and trademark applications identified on Schedule B attached hereto.

F. WHEREAS, the Secured Parties, as the successors in interest to Comdisco, entered into that certain Amendment No. 1 to Restructuring Agreement, dated as of September 29, 2003 (the "Amendment"), whereby the Secured Parties succeeded to all of the right, title and interest of Comdisco in the Restructuring Agreement and the Security Agreement.

G. WHEREAS, in connection with the Amendment, Comdisco assigned the Security Agreement to the Secured Parties (the "Assignment") which Assignment was recorded with the United States Patent and Trademark Office at Reel 014653, Frame 0611 with respect to patents, and Reel 2858, Frame 0271 with respect to trademarks, for the purpose of securing payment and performance of Debtor's obligations under the Amendment.

H. WHEREAS, the Secured Parties have agreed to terminate and release their security interests in all intellectual property of the Debtor, including without limitation the patents and patent applications identified on Schedule A and the trademarks and trademark applications identified on Schedule B attached hereto as herein provided:

NOW, THEREFORE, for valuable consideration the Secured Parties hereby terminate and release their security interests in Debtor's intellectual property, including without limitation the patents and patent applications identified on Schedule A and the trademarks and trademark applications identified on Schedule B attached hereto, and the Secured Parties hereby assign and transfer to Debtor, without representations, warranty or recourse (other than a warranty that the Secured Parties have not assigned or transferred their rights, titles and interests in and to the intellectual property to any other person or entity), all of the Secured Parties rights, titles and interests in and to the intellectual property, including without limitation the patents and patent applications identified on Schedule A and the trademarks and trademark applications identified on Schedule B attached hereto, effective as of the date set forth above. The Secured Parties will execute such further documents as deemed reasonably necessary by Debtor to confirm and effect this termination and release.

*[Signature Page Follows]*

**IN WITNESS WHEREOF**, this Termination is executed as of the date first above written.

**WK TECHNOLOGY FUND  
WK TECHNOLOGY FUND IV  
WK TECHNOLOGY FUND V  
WK TECHNOLOGY FUND VI  
WK TECHNOLOGY FUND VII  
WK TECHNOLOGY FUND VIII  
WK GLOBAL INVESTMENT LIMITED  
WK GLOBAL INVESTMENT II LIMITED  
WK GLOBAL INVESTMENT III LIMITED**

By: \_\_\_\_\_

Its: \_\_\_\_\_

  
Authorized Signatory

**SEQUOIA CAPITAL IX  
SEQUOIA CAPITAL IX PRINCIPAL'S FUND  
SEQUOIA CAPITAL FRANCHISE FUND  
SEQUOIA CAPITAL FRANCHISE PARTNERS  
SEQUOIA CAPITAL ENTREPRENEURS ANNEX FUND  
AS SUCCESSOR IN INTEREST TO SEQUOIA  
CAPITAL ENTREPRENEURS FUND**

By: \_\_\_\_\_

Its: \_\_\_\_\_

**FOCUS VENTURES II, L.P.  
FOCUS VENTURES II A, L.P.  
FOCUS VENTURES II QP, L.P.**

By: \_\_\_\_\_

Its: \_\_\_\_\_

**SAIYED ATIG RAZA AND NOREEN TIRMIZI RAZA,  
TRUSTEES N&A RAZA REVOCABLE TRUST  
UAD 03/22/97**

By: \_\_\_\_\_

Its: \_\_\_\_\_

[Signature Page to Termination and Release of Intellectual Property Security]

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WK TECHNOLOGY FUND VII  
WK TECHNOLOGY FUND VIII  
WK GLOBAL INVESTMENT LIMITED  
WK GLOBAL INVESTMENT II LIMITED  
WK GLOBAL INVESTMENT III LIMITED

By: \_\_\_\_\_

Its: \_\_\_\_\_

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SEQUOIA CAPITAL FRANCHISE FUND  
SEQUOIA CAPITAL FRANCHISE PARTNERS  
SEQUOIA CAPITAL ENTREPRENEURS ANNEX FUND  
AS SUCCESSOR IN INTEREST TO SEQUOIA  
CAPITAL ENTREPRENEURS FUND

By: Michael J. Jones

Its: Partner

FOCUS VENTURES II, L.P.  
FOCUS VENTURES II A, L.P.  
FOCUS VENTURES II QP, L.P.

By: \_\_\_\_\_

Its: \_\_\_\_\_

SAIYED ATIG RAZA AND NOREEN TIRMIZI RAZA,  
TRUSTEES N&A RAZA REVOCABLE TRUST  
UAD 03/22/97

By: \_\_\_\_\_

Its: \_\_\_\_\_

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WK GLOBAL INVESTMENT LIMITED  
WK GLOBAL INVESTMENT II LIMITED  
WK GLOBAL INVESTMENT III LIMITED

By: \_\_\_\_\_

Its: \_\_\_\_\_

SEQUOIA CAPITAL IX  
SEQUOIA CAPITAL IX PRINCIPAL'S FUND  
SEQUOIA CAPITAL FRANCHISE FUND  
SEQUOIA CAPITAL FRANCHISE PARTNERS  
SEQUOIA CAPITAL ENTREPRENEURS ANNEX FUND  
AS SUCCESSOR IN INTEREST TO SEQUOIA  
CAPITAL ENTREPRENEURS FUND

By: \_\_\_\_\_

Its: \_\_\_\_\_

FOCUS VENTURES II, L.P.  
FOCUS VENTURES II A, L.P.  
FOCUS VENTURES II QP, L.P.

By: George H. Bischof

Its: George H. Bischof  
General Partner of the General Partner

SAIYED ATIG RAZA AND NOREEN TIRMIZI RAZA,  
TRUSTEES N&A RAZA REVOCABLE TRUST  
UAD 03/22/97

By: \_\_\_\_\_

Its: \_\_\_\_\_

[Signature Page to Termination and Release of Intellectual Property Security]



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WK TECHNOLOGY FUND VII  
WK TECHNOLOGY FUND VIII  
WK GLOBAL INVESTMENT LIMITED  
WK GLOBAL INVESTMENT II LIMITED  
WK GLOBAL INVESTMENT III LIMITED

By: \_\_\_\_\_

Its: \_\_\_\_\_

SEQUOIA CAPITAL IX  
SEQUOIA CAPITAL IX PRINCIPAL'S FUND  
SEQUOIA CAPITAL FRANCHISE FUND  
SEQUOIA CAPITAL FRANCHISE PARTNERS  
SEQUOIA CAPITAL ENTREPRENEURS ANNEX FUND  
AS SUCCESSOR IN INTEREST TO SEQUOIA  
CAPITAL ENTREPRENEURS FUND

By: \_\_\_\_\_

Its: \_\_\_\_\_

FOCUS VENTURES II, L.P.  
FOCUS VENTURES II A, L.P.  
FOCUS VENTURES II QP, L.P.

By: \_\_\_\_\_

Its: \_\_\_\_\_

SAIYED ATIG RAZA AND NOREEN TIRMIZI RAZA,  
TRUSTEES N&A RAZA REVOCABLE TRUST  
UAD 03/22/97

By:  \_\_\_\_\_

Its: \_\_\_\_\_

SCHEDULE A

PATENTS

**PATENTS**

<b><u>Name of Patent</u></b>	<b><u>Application No.</u></b>	<b><u>Issue Date</u></b>	<b><u>Patent No.</u></b>
Automatic Gain Control Circuit for Signal with Diverse Power Level Range	09/645,475	07/16/2002	6420934
Oscillator Circuit With Signal Buffering & Start-Up Circuitry	09/832,686	09/03/2002	6445258
Area Efficient Bond Pad Placement	09/847,948	12/03/2002	6489688
Seal Ring Structure for IC Containing Integrated Digital/RF/Analog Circuits & Functions	09/846,335	12/10/2002	6492716
Efficient AC Coupled CMOS RF Amplifier	09/825,252	05/27/2003	6570450
Solder Pad Structure for Low Temperature Co-Fired Ceramic Package and Method for Making the Same	10/235,762	02/03/2004	6686665
Structure to Increase Density of MIM Capacitors Between Adjacent Metal Layers in an Integrated Circuit	09/844,293	03/23/2004	6710425
Analog Memory Cell in a Low Power Oscillator	10/206,256	06/08/2004	6747521
Logarithmic IF Amplifier with Dynamic Large Signal Bias Circuit	09/836,624	06/15/2004	6750715
Low Power Large Signal RF Tuned Buffer Amplifier	09/835,021	08/24/2004	6781445
CMOS Frequency Conversion Using Dual Mixers for Sideband Suppression	09/825,250	08/24/2004	6782247
Analog Hold Cell in a CMOS Process	10/190,129	09/14/2004	6791374
Single Ended Tuning of a Differential Power Amplifier Output	10/253,460	01/18/2005	6844792

**PATENT APPLICATIONS**

<b><u>Name of Patent Application</u></b>	<b><u>Application No.</u></b>	<b><u>Pub. Date</u></b>	<b><u>Publication No.</u></b>
System for Maintaining Synchronization Between Multiple Asynchronous Communication Links	10/101,676	09/26/2002	US 2002/0136198
Transconductance Device Employing Native MOS Transistors	09/837,897	10/24/2002	US 2002/0153973
DC Offset Correction for use in a Direct Conversion Radio Architecture	09/813,420	10/31/2002	US 2002/0160738
DC Offset Calibration for a Radio Transceiver Mixer	09/858,812	11/21/2002	US 2002/0173288
Split Source RF MOSFET Device	10/101,405	09/18/2003	US 2003/0173598
Reduced Instruction Set Baseband Controller	10/101,396	10/21/2004	US 2004/0210875

<b><u>Name of Patent Application</u></b>	<b><u>Application No.</u></b>	<b><u>Filing Date</u></b>
Method for Designing Mixed Signal Integrated Circuits And Configurable Synchronous Digital Noise Emulator Circuit	09/618,971	07/19/2000
Method and Apparatus for Co-Location of Two Radio Frequency Devices	10/233,237	08/29/2002
Package Filter and Combiner Network	10/262,336	09/30/2002
Fully Differential, High-Q, On-Chip, Impedance Matching Section	10/623,084	07/17/2003
System Method and Apparatus for a Three-Line Balun with power Amplifier bias	10/613,346	07/02/2003
Systems and Methods for Ramping Power Amplifier Output Power	10/826,460	04/16/2004
Methods and Systems for Biasing a Power Amplifier	10/826,464	04/16/2004

**SCHEDULE B**

**TRADEMARKS**

**TRADEMARKS**

<b>Trademark</b>	<b>Serial Number</b>	<b>Date Registered</b>	<b>Reg. No.</b>
ENABLING PERVASIVE CONNECTIVITY	78/016,453	December 18, 2001	2521435
ZEEVO	76/167,150	July 6, 2004	2860587

**TRADEMARK APPLICATIONS**

<b>Trademark Application</b>	<b>Serial Number</b>	<b>Date Filed</b>
ZEEVO	76/167,619	November 17, 2000
BLUE SHARK	78/030,222	October 11, 2000