

Form PTO-1595 (Rev. 09/04)  
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## RECORDATION FORM COVER SHEET PATENTS ONLY

To the Director of the U.S. Patent and Trademark Office: Please record the attached documents or the new address(es) below.

### 1. Name of conveying party(ies)/Execution Date(s):

Ecole Polytechnique Fédérale de Lausanne

Execution Date(s) December 6, 2002

Additional name(s) of conveying party(ies) attached? ☐ Yes ☒ No

### 3. Nature of conveyance:

- ☒ Assignment ☐ Merger  
☐ Security Agreement ☐ Change of Name  
☐ Government Interest Assignment  
☐ Executive Order 9424, Confirmatory License  
☐ Other \_\_\_\_\_

### 2. Name and address of receiving party(ies)

Name: Innovative Silicon S.A.

Internal Address: \_\_\_\_\_

Street Address: PSE - Batiment B

City: Lausanne

State: \_\_\_\_\_

Country: Switzerland Zip: CH-1015

Additional name(s) & address(es) attached? ☐ Yes ☒ No

### 4. Application or patent number(s):

A. Patent Application No.(s)

10/487,162

☐ This document is being filed together with a new application.

B. Patent No.(s)

Additional numbers attached? ☐ Yes ☒ No

### 5. Name and address to whom correspondence concerning document should be mailed:

Name: Neil A. Steinberg

Internal Address: \_\_\_\_\_

Street Address: 2665 Marine Way, Suite 1150

City: Mountain View

State: CA Zip: 94043

Phone Number: 650-968-8079

Fax Number: 650-968-8102

Email Address: neils@swpatentlaw.com

### 6. Total number of applications and patents involved:

1

### 7. Total fee (37 CFR 1.21(h) & 3.41) \$ 40.00

- ☐ Authorized to be charged by credit card  
☒ Authorized to be charged to deposit account  
☐ Enclosed  
☐ None required (government interest not affecting title)

### 8. Payment Information

a. Credit Card Last 4 Numbers \_\_\_\_\_  
 Expiration Date \_\_\_\_\_

b. Deposit Account Number 50-0763

Authorized User Name Neil A. Steinberg

### 9. Signature:

  
 Signature

March 3, 2005

Date

Neil A. Steinberg

Name of Person Signing

Total number of pages including cover sheet, attachments, and documents:

14

Documents to be recorded (including cover sheet) should be faxed to (703) 306-5995, or mailed to:  
 Mail Stop Assignment Recordation Services, Director of the USPTO, P.O. Box 1450, Alexandria, V.A. 22313-1450

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**PATENT**  
**REEL: 015831 FRAME: 0914**

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001  
B2-1**AGREEMENT**

between

**Ecole Polytechnique Fédérale de Lausanne,**  
CH-1015 Lausanne, Switzerland,  
represented by Prof. Declercq, Head of Electronics Laboratory  
(hereafter referred to as "EPFL")

on the one part,

and

**Innovative Silicon SA**  
PSE-B  
CH-1015 Lausanne, Switzerland  
represented by André Borschberg and Pierre Fazan  
(hereafter referred to as "ISI")

on the second part,

and

**Pierre Fazan**  
Av. Du Carroz 1  
CH-1110 Morges

and

**Serguei Okhonin**  
Jolimont 2  
CH-1005 Lausanne

and

**André Borschberg**  
9 route de Lausanne  
CH-1260 Nyon

and

**Luc Bauer**  
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(hereafter collectively referred to as "the Founders")

on the third part.

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**Preamble**

Researchers from the Electronics Laboratory of EPFL and Pierre Fazan Innovative Silicon Solutions at Morges, Switzerland (the latter hereafter referred to as "ISS"), have generated and developed a concept of semiconductor devices exploiting the floating body effect.

In an agreement dated September 11, 2001, EPFL and ISS agreed that patent applications to be filed on this concept shall be jointly owned by ISS and EPFL, namely in a proportion of two thirds for ISS and one third for EPFL.

EPFL and ISS have filed an initial patent application on the above mentioned concept.

On May 22, 2002, the Founders have set up ISi.

By a separate agreement dated September 12, 2002, ISS has assigned to ISi all its rights to the initial patent application.

EPFL and ISi have filed other patent applications on the concept and its applications.

With the aim to allow further development of the concept and its use for the benefit of the public, EPFL is willing to irrevocably fully assign to ISi its co-ownership rights on the said patent applications and to grant a non-exclusive license on certain software and layouts, under the terms and conditions set forth hereafter.

**ARTICLE 1 - DEFINITIONS****- Patents**

"Patents" shall mean the patent applications filed or to be filed, which are listed in Exhibit 1 hereto, as well as all patents issued from the said patent applications. The text of the 3 patent applications to be filed is appended in Exhibit 1.

**- Technology**

"Technology" shall mean the technologies covered by one or more Patents.

**- Field**

"Field" shall mean semi-conductor memories excluding CMOS imagers and sensors.

**- Licensed Items**

"Licensed Items" shall mean the software and the layouts defined in Exhibit 2 hereto.

**ARTICLE 2 - ASSIGNMENT - LICENSE**

- 2.1 EPFL hereby irrevocably sells, assigns and transfers to ISi, which accepts, any and all its co-ownership rights to the Patents. As a result thereof, ISi shall have sole and full ownership to the Patents, in particular the exclusive right to use and exploit the Technology and the Patents in all fields of applications, including the right to

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manufacture, have manufactured, sell and license products and/or processes based on the Technology and/or the Patents. EPFL agrees, upon ISi request, to execute any documents necessary for effecting the sale, assignment and transfer of the Patents to ISi contemplated hereunder.

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~~INVENTOR: STEINBERG~~

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*Handwritten signature and initials*

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This Agreement is executed in three originals and duly signed by the parties hereto, as printed below:


Ecole Polytechnique Fédérale  
de Lausanne

Innovative Silicon ISI S.A.

Place and date: Lausanne, Dec 6, 2002

Place and date: Lausanne, Dec 6th, 2002



  
(signature)

  
(signatures)

Prof. Declercq, Head of Electronics  
Laboratory

A. Borschberg,  
Director

P. Fazan,  
Director


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S.O.

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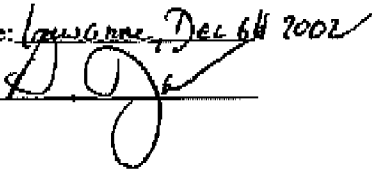
Pierre Fazan

Place and date: Lausanne, Dec. 6, 2002  
Signature: 

Serguei Okhonin

Place and date: Lausanne, Dec 6, 2002  
Signature: 

André Borschberg

Place and date: Lausanne, Dec 6th 2002  
Signature: 

Luc Bauer

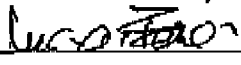
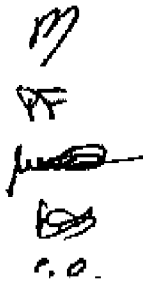


Place and date: Lausanne, Dec 6th 2002  
Signature: 

Exhibit 1 :  
Exhibit 2 :  
Exhibit 3 :

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## Exhibit 1: List of Patent Applications filed or to be filed

## a) List of Patent Applications Filed:

Reference	Law firm	Title	Filing Date	Filing Number	Inventors
Case 1	Gressel	Dispositif semi-conducteur, notamment mémoire DRAM	18.06.01	EP 01810587.4	Pierre Fazan Serguei Okhonin
Case 2	Gressel	Procédé de commande d'un dispositif semi-conducteur	28.03.02	EP 02405247.4	Pierre Fazan Serguei Okhonin
Case 3	Gressel	Dispositif de rafraichissement d'une mémoire	18.04.02	EP 02405314.2	Pierre Fazan Serguei Okhonin
Case 4	Gressel	Procédé pour créer une charge électrique dans la couche d'un composant semi-conducteur	18.04.02	EP 02405315.9	Pierre Fazan Serguei Okhonin
Case 5	Gressel	Dispositif semi-conducteur porteur d'une charge électrique	18.04.02	EP 02405316.7	Pierre Fazan Serguei Okhonin
PCT	UDL	Semiconductor Device	05.08.02	PCT/EP02/06495	Pierre Fazan Serguei Okhonin
Case 3	UDL	Data storage device and refreshing method for use with such device	29.05.02	EP 02077116.6	Pierre Fazan Serguei Okhonin
Case 5	UDL	Semiconductor device	27.08.02	EP02078595.3	Pierre Fazan Serguei Okhonin

  
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b) List of Patent Applications to be Filed:

Case 6	UDL	Method and apparatus for controlling semiconductor memory device	N.A.	N.A.	Maher Kayal Marc Pastre Maria Blagolevich Michel Declercq
Case 7	UDL	Method and apparatus for generating reference current in semiconductor memory device	N.A.	N.A.	Lionel Portmann
Case 10	UDL	Sense & Compare amplifier	N.A.	N.A.	Richard Ferrant

Note: The PCT application is the result of a PCT filing combining cases 1,2,4,B, and 9.

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Exhibit 2 :

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Exhibit 3:

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