

10-04-2004

FORM PTO-1595
(Rev. 10/02)
OMD No. 0651-0011 (exp. 6/30/2005)

Re



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DEPARTMENT OF COMMERCE
Patent and Trademark Office

To the Honorable Commissioner of Patents and Trademarks. Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):

Gilroy J. Vandentop; Rajashree Baskaran

2. Name and address of receiving party(ies):

Name: Intel Corporation

Additional name(s) of conveying party(ies) attached?

☒ No ☐ Yes

Internal Address:

Street Address: 2200 Mission College Boulevard

3. Nature of Conveyance:

☒ Assignment ☐ Merger
☐ Security Agreement ☐ Change of Name
☐ Other

City: Santa Clara State: CA Zip: 95052

Country: USA

Execution Date: 9/20/04

Additional name(s) & address(es) attached? ☒ No ☐ Yes

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is:

9/20/04

A. Patent Application No.(s)

B. Patent No.(s)

Additional numbers attached? ☒ No ☐ Yes

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Blakely, Sokoloff, Taylor & Zafman LLP

Internal Address:

Street Address: 12400 Wilshire Boulevard
7th Floor

City: Los Angeles State: CA ZIP: 90025

6. Total number of applications and patents involved:

1

7. Total Fee (37 CFR 3.41).....\$ 40.00

☒ Enclosed
☐ Authorized to be charged to deposit account

8. Deposit Account Number:

02-2666

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9. Statement and signature.

To the best of my knowledge and belief, the foregoing is true and correct and any attached copy is a true copy of the original document.

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Paul A. Mendonsa

Name of Person Signing

Signature

September 24, 2004

Date

Total number of pages including cover sheet, attachments, and document: 2

Mail documents to be recorded with required cover sheet information to:

Mail Stop Assignment Recordation Services
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Docket No. 42P19936

PATENT
REEL: 015836 FRAME: 0737

ASSIGNMENT

In consideration of good and valuable consideration, the receipt of which is hereby acknowledged, I (we), the undersigned inventor(s), hereby sell, assign, and transfer to Intel Corporation, a corporation of Delaware, having a principal place of business at 2200 Mission College Boulevard, Santa Clara, California, 95052, ("Assignee"), and its successors, assigns, and legal representatives, the entire right, title, and interest for the United States and all foreign countries, in and to any and all improvements that are disclosed in the application for the United States patent entitled:

PATTERNED BACKSIDE STRESS ENGINEERING FOR TRANSISTOR PERFORMANCE OPTIMIZATION

which has been executed by the undersigned prior hereto or concurrently herewith on the date(s) indicated below,


and in and to said application and all divisional applications, continuation applications, continued prosecution applications, continuation-in-part applications, substitute applications, renewal applications, reissue applications, reexaminations, extensions, and all other patent applications that have been or shall be filed in the United States and all foreign countries on any of said improvements; and in and to all original patents, reissued patents, reexamination certificates, and extensions, that have been or shall be issued in the United States and all foreign countries on said improvements; and in and to all rights of priority resulting from the filing of said United States application;

agree that said Assignee may apply for and receive a patent or patents for said improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns, and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all divisional applications, continuation applications, continued prosecution applications, continuation-in-part applications, substitute applications, renewal applications, reissue applications, reexaminations, extensions and all other patent applications on any and all said improvements; execute all rightful oaths, assignments, powers of attorney, and other papers; communicate to said Assignee, its successors, assigns, and representatives all facts known to the undersigned relating to said improvements and the history thereof; and generally assist said Assignee, its successors, assigns, or representatives in securing and maintaining proper patent protection for said improvements and for vesting title to said improvements, and all applications for patents and all patents on said improvements, in said Assignee, its successors, assigns, and legal representatives; and

covenant with said Assignee, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

Note to each inventor:

On the line below, please enter the date on which you signed the accompanying "Declaration and Power of Attorney":



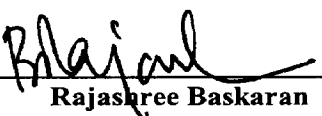
 Gilroy J. Vandentop

9/20/04

 (Today's Date)

9/20/04

 (Date of Declaration)



 Rajashree Baskaran

Sept 20, 2004

 (Today's Date)

Sept 20, 2004

 (Date of Declaration)