


Client Code: IRDM.029CPCDD

**RECORDATION FORM COVER SHEET
PATENTS ONLY**

To the Director, U.S. Patent and Trademark Office: Please record the attached original documents or copy thereof.

<p>1. Name of conveying party(ies): (List using letters or numbers for multiple parties)</p> <p>Iridigm Display Corporation</p> <p>Additional name(s) of conveying party(ies) attached?</p> <p>() Yes (X) No</p>	<p>2. Name and address of receiving party(ies):</p> <p>Name: IDC, LLC</p> <p>Internal Address:</p> <p>Street Address: 2415 Third Street</p> <p>City: San Francisco State: CA</p> <p>ZIP: 94107</p> <p>Additional name(s) of receiving party(ies) attached?</p> <p>() Yes (X) No</p>
<p>3. Nature of conveyance:</p> <p>(X) Assignment () Security Agreement</p> <p>() Merger () Change of Name</p> <p>() Other:</p> <p>Execution Date: (List as in section 1 if multiple signatures)</p> <p>November 4, 2004</p>	<p>4. US or PCT Application number(s) or US Patent number(s):</p> <p>(X) Patent Application No.: 10/844,802</p> <p>Filing Date: May 12, 2004</p> <p>Additional numbers attached?</p> <p>() Yes (X) No</p>
<p>5. Party to whom correspondence concerning document should be mailed:</p> <p>Customer No. 20,995</p> <p>Address: Knobbe, Martens, Olson & Bear, LLP 2040 Main Street, 14th Floor Irvine, CA 92614</p> <p>Return Fax: (949) 760-9502</p> <p>Attorney's Docket No.: IRDM.029CPCDD</p>	<p>6. Total number of applications and patents involved: 1</p>
<p>7. Total fee (37 CFR 1.21(h)): \$40.00</p> <p>(X) Authorized to be charged to deposit account</p>	<p>8. Deposit account number: 11-1410</p> <p>Please charge this account for any additional fees which may be required, or credit any overpayment to this account.</p>
<p>9. Statement and signature.</p> <p>To the best of my knowledge and belief, the foregoing information is true and correct, and any attached copy is a true copy of the original document.</p> <p><u>Mark M. Abumeri</u> Name of Person Signing</p> <p><u>43,458</u> Registration No.</p> <p> Signature</p> <p><u>March 8, 2005</u> Date</p> <p>Total number of pages including cover sheet, attachments and document: 12</p>	

Documents transmitted via Facsimile to be recorded with required cover sheet information to:

Mail Stop Assignment Recordation Services
Director, U.S. Patent and Trademark Office
P.O. Box 1450
Alexandria, VA 22313-1450
Facsimile Number: (703) 306-5995

RECORDPA
S:\DOCS\MMMA\MMMA-9488.DOC
012405

700160093

PATENT
REEL: 015854 FRAME: 0262

CH \$40.00 111410 10844802

ASSIGNMENT

WHEREAS, IRIDIGM DISPLAY CORPORATION (hereinafter "ASSIGNOR"), a Delaware corporation, having a principal place of business 2415 Third Street, San Francisco, California 94107, has certain rights in Improvements in interferometric modulation, for which several Applications for Patents have been or may be filed and/or several Patents have been issued in the United States and internationally, a list of which is set forth in Exhibit A attached hereto.

AND WHEREAS, IDC, LLC (hereinafter "ASSIGNEE"), a Delaware limited liability company, with its principal place of business at 2415 Third Street, San Francisco, CA, 94107, desires to acquire ASSIGNOR'S entire right, title, and interest in and to (1) said Improvements, including said Applications and Patents listed in Exhibit A, AND (2) all subject matter in which ASSIGNOR had rights on or prior to October 1, 2004, which is not particularly identified in Exhibit A (hereinafter "Intellectual Property"), including (i) all inventions, ideas, concepts, designs, materials, methods or processes, and the like, whether or not patentable, (ii) all know-how and trade secrets, whether or not qualifying as trade secret(s), and (iii) all improvements, modifications, or developments relating to any of said Intellectual Property;

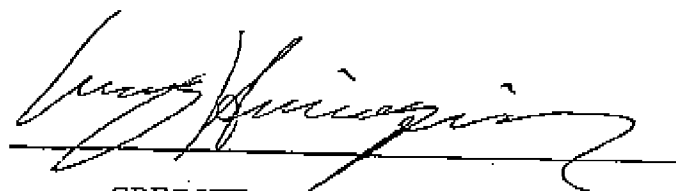
NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, ASSIGNOR does hereby acknowledge that it has sold, assigned, transferred and set over on October 1, 2004, and by these presents does hereby sell, assign, transfer and set over with an effective assignment date of October 1, 2004, unto the said ASSIGNEE, its successors, legal representatives and assigns, ASSIGNOR'S entire right, title, and interest throughout the world in, to and under the said Improvements and Intellectual Property, including the said Applications and Patents and all non-provisionals, divisions, renewals and continuations thereof, and all Patents of the United States which may have been or may be granted thereon and all reissues and extensions thereof, and all rights of priority under International Conventions and applications for Patents which may hereafter be filed for said Improvements and Intellectual Property in any country or countries foreign to the United States, and all Patents which may be granted for said Improvements and Intellectual Property in any country or countries foreign to the United States and all extensions, renewals and reissues thereof; and ASSIGNOR hereby authorizes and request the Commissioner of Patents of the United States, and any Official of any country or countries foreign to the United States, whose duty it is to issue patents on applications as aforesaid, to issue all Patents for said Improvements and Intellectual Property to the said ASSIGNEE, its successors, legal representatives and assigns, in accordance with the terms of this instrument;

AND ASSIGNOR HEREBY sells, assigns, transfers, and conveys to ASSIGNEE, its successors, legal representatives, and assigns all claims for damages and all remedies arising out of any violation of the rights assigned hereby that may have accrued prior to the date of assignment to ASSIGNEE, or may accrue hereafter, including, but not limited to, the right to sue for, collect, and retain damages for past infringements of said Patent, before or after issuance;

AND ASSIGNOR HEREBY covenants and agrees that it will communicate to the said ASSIGNEE, its successors, legal representatives and assigns, any facts known to it respecting said Improvements and Intellectual Property, and will designate an individual on its behalf to testify in any legal proceeding, sign all lawful papers, execute all divisional, continuing and reissue applications, make all rightful oaths and generally do everything possible to aid the said ASSIGNEE, its successors, legal representatives and assigns, to obtain and enforce proper patent protection for said Improvements and Intellectual Property in all countries;

NOTWITHSTANDING the foregoing provisions, this instrument shall not affect any of ASSIGNOR'S rights in said Improvements and Intellectual Property, as licensed by ASSIGNEE to ASSIGNOR in a Patent License Agreement entered between ASSIGNEE and ASSIGNOR on October 1, 2004.

IN TESTIMONY WHEREOF, I hereunto set my hand and seal this 4 day of November, 2004



GREG HEINZINGER

Exhibit A

Assignment, having an effective date of October 1, 2004, from Iridigm Display Corporation to DC, LLC

TITLE	Country	Application No.	Filing Date
ALTERING TEMPORAL RESPONSE OF MICROELECTROMECHANICAL ELEMENTS	US	10/794,737	3/3/2004
DRIVER VOLTAGE ADJUSTER	US	10/772,120	02/03/04
INTEGRATED MODULATOR ILLUMINATION	US	10/794,825	03/05/04
MEMS DEVICES WITH UNRELEASED THIN FILM COMPONENTS	US	10/700,641	11/03/03
MODIFYING THE ELECTRO-MECHANICAL BEHAVIOR OF DEVICES	US	10/839,307	05/04/04
INTERFEROMETRIC MODULATORS WITH THIN FILM TRANSISTORS	US	10/883,902	07/02/04
METHOD OF MANUFACTURE FOR MICROELECTROMECHANICAL DEVICES	US	10/839,329	05/04/04
IMOD ARRAY WITH INTEGRATED OPTICAL COMPENSATION STRUCTURE	US	60/541,607	02/03/04
METHOD FOR OPTIMIZING COLOR IN IMOD DISPLAYS	US	60/550,687	03/06/04
LOW CAPACITANCE MEMS DEVICE	US	10/909,228	07/29/04
METHODS OF ADDRESSING A BI-STABLE MODULATOR	US	60/604,896	08/27/04
METHODS OF ADDRESSING A BI-STABLE MODULATOR	US	60/606,223	08/31/04
CURRENT MODE DISPLAY DRIVER	US	60/604,893	08/27/04
SENSING STATUS OF A MEMS MEMORY DEVICE	US	60/604,892	08/27/04
INTERFEROMETRIC MODULATION	PCT	PCT/US96/17731	11/06/96
INTERFEROMETRIC MODULATION OF RADIATION	US	10/844,802	05/13/04

PATENT

REEL: 015854 FRAME: 0264

Exhibit A

Assignment, having an effective date of October 1, 2004, from Iridigm Display Corporation to IDC, LLC

TITLE	Country	Application No.	Filing Date
SEPARABLE MODULATOR	PCT	PCT/US04/02645	08/12/04
SEPARABLE MODULATOR	Taiwan	93124794	08/18/04
METHOD FOR FABRICATING A STRUCTURE FOR A MICROELECTROMECHANICAL SYSTEMS (MEMS) DEVICE	US	10/941,042	09/14/04
A METHOD FOR FABRICATING A STRUCTURE FOR A MICROELECTROMECHANICAL SYSTEMS	China	2828414.3	04/29/02
A METHOD FOR FABRICATING A STRUCTURE FOR A MICROELECTROMECHANICAL SYSTEMS	Europe	2725847.4	04/29/02
A METHOD FOR FABRICATING A STRUCTURE FOR A MICROELECTROMECHANICAL SYSTEMS	Japan	2003-568475	04/29/02
A METHOD FOR FABRICATING A STRUCTURE FOR A MICROELECTROMECHANICAL SYSTEMS	Korea	2004-7012516	04/29/02
PHOTONIC MEMS AND STRUCTURES	Korea	2004-7000433	07/10/01
MICROELECTROMECHANICAL SYSTEMS DEVICE AND METHOD FOR FABRICATING SAME	China	2828352.X	04/29/02
MICROELECTROMECHANICAL SYSTEMS DEVICE AND METHOD FOR FABRICATING SAME	Europe	2806893.0	04/29/02
MICROELECTROMECHANICAL SYSTEMS DEVICE AND METHOD FOR FABRICATING SAME	Japan	Unknown	04/29/02
MICROELECTROMECHANICAL SYSTEMS DEVICE AND METHOD FOR FABRICATING SAME	Korea	2004-7013279	04/29/02

PATENT

REEL: 015854 FRAME: 0265

Exhibit A

Assignment, having an effective date of October 1, 2004, from Iridigm Display Corporation to IDC, LLC

TITLE	Country	Application No.	Filing Date
THIN FILM PRECURSOR STACK FOR MEMS AND MANUFACTURING AREA ARRAY MODULATION AND LEAD REDUCTION IN INTERFEROMETRIC MODULATORS	US	10/731,989	12/09/03
VISIBLE SPECTRUM MODULATOR ARRAYS	US	10/082,397	08/06/02
A SHORT PULSE METHOD FOR DRIVING AN IMOD	US	60/613418	09/27/04
MINI-ENVIRONMENT RELEASE CHAMBER/POD SYSTEM	US	60/613417	09/27/04
SYSTEM AND METHOD OF EMBEDDING DISPLAY OPTICS	US	60/613482	09/27/04
DEVICE AND METHOD FOR SUPPORTING INTERFEROMETRIC MODULATORS	US	60/613405	09/27/04
METHOD OF PACKAGING INTERFEROMETRIC MODULATORS AND MATERIALS THEREFOR	US	60/613493	09/27/04
METHOD AND SYSTEM FOR DETECTING LEAK IN ELECTRONIC DEVICES	US	60/613385	09/27/04
DEVICE AND METHOD FOR INTERFEROMETRIC MODULATION HAVING "OXIDESTOPS"	US	60/613466	09/27/04
CONTROLLER AND DRIVER FEATURES FOR BI-STABLE DISPLAYS	US	60/613412	09/27/04
DEVICE FOR AND METHOD USING ONE OR MORE PIXEL ARCHITECTURES	US	60/613500	09/27/04
METHOD AND SYSTEM FOR PACKAGING A MEMS DEVICE	US	60/613275	09/27/04
METHOD AND DEVICE FOR LIGHTING A DISPLAY	US	60/613284	09/27/04
METHOD AND DEVICE FOR CORNER INTERFEROMETRIC MODULATION	US	60/613597	09/27/04

PATENT

REEL: 015854 FRAME: 0266

Exhibit A

Assignment, having an effective date of October 1, 2004, from Iridigm Display Corporation to IDC, LLC

TITLE	Country	Application No.	Filing Date
DISPLAY DEVICE HAVING AN ARRAY OF SPATIAL LIGHT MODULATORS WITH INTEGRATED COLOR FILTERS	US	60/613542	09/27/04
METHOD AND DEVICE FOR INHIBITING TILTING OF A MIRROR	US	60/613558	09/27/04
INTERFEROMETRIC MODULATOR ARRAY WITH INTEGRATED MEMS ELECTRICAL SWITCHES	US	60/613501	09/27/04
DEVICE HAVING A CONDUCTIVE LIGHT ABSORBING MASK AND METHOD FOR FABRICATING SAME	US	60/613480	09/27/04
SEPARABLE MODULATOR	US	60/613372	09/27/04
METHOD AND DEVICE FOR SELECTIVE ADJUSTMENT OF HYSTERESIS WINDOW	US	60/613382	09/27/04
PIXEL ELEMENT WITH SUB-PIXELS HAVING DIFFERING ACTUATION VOLTAGES	US	60/613458	09/27/04
METHOD OF MAKING PRESTRUCTURE FOR MEMS SYSTEMS	US	60/613411	09/27/04
INTERFEROMETRIC MODULATORS HAVING CHARGE PERSISTENCE	US	60/613420	09/27/04
DEVICE AND METHOD FOR DISPLAY MEMORY USING MANIPULATION OF MECHANICAL RESPONSE	US	60/613450	09/27/04
DEVICE AND METHOD FOR MANIPULATION OF THERMAL RESPONSE IN A MODULATOR	US	60/613552	09/27/04
METHOD FOR AND ELECTRONIC DEVICES UTILIZING MICROELECTROMECHANICAL SYSTEM	US	60/613465	09/27/04
METHOD AND DEVICE FOR DRIVING INTERFEROMETRIC MODULATORS	US	60/613483	09/27/04
METHOD AND DEVICE FOR COMPENSATING FOR COLOR SHIFT AS A FUNCTION OF ANGLE OF VIEW	US	60/613978	09/27/04

Exhibit A

Assignment, having an effective date of October 1, 2004, from Iridigm Display Corporation to IDC, LLC

TITLE	Country	Application No.	Filing Date
APPARATUS AND METHOD FOR TUNING THE SPECTRAL RESPONSE OF LIGHT SOURCES FOR INTERFEROMETRIC MODULATORS	US	60/613297	09/27/04
SYSTEM AND METHOD FOR ILLUMINATING INTERFEROMETRIC MODULATORS USING BACKLIGHTING	US	60/613536	09/27/04
INTERFEROMETRIC MODULATORS CAPABLE OF RENDERING MULTIPLE OUTPUTS	US	60/613486	09/27/04
METHOD AND POST STRUCTURES FOR INTERFEROMETRIC MODULATION	US	60/613471	09/27/04
METHOD AND DEVICE FOR WAVELENGTH FILTERING	US	60/613403	09/27/04
METHOD AND DEVICE FOR PACKAGING A SUBSTRATE	US	60/613318	09/27/04
METHOD AND DEVICE FOR PACKAGING INTERFEROMETRIC MODULATORS WITH HERMETIC BARRIER	US	60/613476	09/27/04
METHOD AND SYSTEM FOR PACKAGING A DISPLAY	US	60/613563	09/27/04
SYSTEM AND METHOD FOR DISPLAY DEVICE WITH END-OF-LIFE PHENOMENA AND RELAXED CONSTRAINT FOR HUMIDITY SENSITIVITY	US	60/613485	09/27/04
SYSTEM AND METHOD FOR PROTECTING MICRO-STRUCTURE OF DISPLAY ARRAY USING SPACERS IN GAP WITHIN DISPLAY DEVICE	US	60/613682	09/27/04
SYSTEM AND METHOD FOR PROTECTING MICRO-STRUCTURE OF DISPLAY ARRAY USING STRUCTURALLY REINFORCED BACK-PLATE	US	60/613408	09/27/04
METHOD AND SYSTEM FOR REGENERATION OF MEMS DEVICE MONOLAYER	US	60/613564	09/27/04
METHOD OF FABRICATING INTERFEROMETRIC MODULATORS USING PHOTSENSITIVE POLYMERS	US	60/613401	09/27/04

Exhibit A

Assignment, having an effective date of October 1, 2004, from Iridigm Display Corporation to IDC, LLC

TITLE	Country	Application No.	Filing Date
METHODS AND APPARATUS FOR TESTING INTERFEROMETRIC MODULATORS	US	60/613537	09/27/04
OPTICS SYSTEM SUBSTRATE WITH OPTICAL FILTER	US	60/613481	09/27/04
METHOD FOR AND DEVICE HAVING A PARTIALLY REFLECTIVE MIRROR IN INTERFEROMETRIC MODULATION	US	60/613488	09/27/04
METHOD AND SYSTEM FOR DRIVING INTERFEROMETRIC MODULATORS	US	60/613319	09/27/04
INTERFEROMETRIC MODULATOR MODIFICATIONS FOR SUPERIOR IMAGE QUALITY	US	60/613499	09/27/04
METHOD AND DEVICE FOR MANIPULATING COLOR IN A DISPLAY	US	60/613491	09/27/04
EXTERNAL OPTICAL FILM FOR INTERFEROMETRIC MODULATOR SYSTEM	US	60/613535	09/27/04
SYSTEM AND METHOD FOR ILLUMINATION INTERFEROMETRIC MODULATOR DISPLAY	US	60/613951	09/27/04
SYSTEM AND METHOD FOR IMPLEMENTATION OF INTERFEROMETRIC MODULATOR DISPLAYS	US	60/613298	09/27/04
METHOD AND DEVICE FOR MULTI-LEVEL BRIGHTNESS IN INTERFEROMETRIC MODULATION	US	60/613539	09/27/04
METHOD AND SYSTEM FOR SENSING LIGHT USING INTERFEROMETRIC ELEMENTS	US	60/613624	09/27/04
METHOD AND SYSTEM FOR TILING AND DISPLAY USING INTERFEROMETRIC DEVICES	US	60/613487	09/27/04
METHOD AND DEVICE FOR DRIVING INTERFEROMETRIC MODULATORS WITH HYSTERESIS	US	60/613419	09/27/04

PATENT

REEL: 015854 FRAME: 0269

Exhibit A

Assignment, having an effective date of October 1, 2004, from Iridigm Display Corporation to IDC, LLC

TITLE	Country	Application No.	Filing Date
METHOD OF FABRICATING INTERFEROMETRIC DEVICES USING LIFT-OFF PROCESSING TECHNIQUES	US	60/613496	09/27/04
SYSTEM AND METHOD FOR DISPLAY DEVICE WITH INTEGRATED DESICCANT	US	60/613300	09/27/04
SYSTEM AND METHOD FOR DISPLAY WITH ACTIVATED DESICCANT	US	60/613280	09/27/04
SYSTEM AND METHOD OF REMOVING WATER VAPOR DURING ASSEMBLY OF MEMS DEVICE	US	60/613956	09/27/04
SYSTEM AND METHOD OF INTEGRATING APPLICATION OF DESICCANT IN MEMS PROCESSING	US	60/613484	09/27/04
SYSTEM AND METHOD OF PROVIDING MEMS DEVICE WITH SELF-ALIGNED MONOLAYER ON MIRROR SURFACES	US	60/613852	09/27/04
SYSTEM AND METHOD FOR MANUFACTURING MEMS IN AN AMBIENT ENVIRONMENT	US	60/613467	09/27/04
METHOD AND SYSTEM FOR PROVIDING MEMS DEVICE PACKAGE WITH SECONDARY SEAL	US	60/613527	09/27/04
SYSTEM AND METHOD FOR OPTIMIZING DESICCANT USAGE IN A MEMS DEVICE	US	60/613801	09/27/04
SYSTEM AND METHOD OF TESTING HUMIDITY IN A SEALED MEMS DEVICE	US	60/613567	09/27/04
PLATED METAL SEAL FOR SMALL DIAMETER DISPLAYS	US	60/613569	09/27/04
METHOD AND APPARATUS FOR POST-PACKAGING RELEASE ETCHING OF INTERFEROMETRIC ARRAYS	US	60/613320	09/27/04
METHOD AND STRUCTURE FOR PROTECTING DEPOSITED MEMS STRUCTURES	US	60/613406	09/27/04

Exhibit A

Assignment, having an effective date of October 1, 2004, from Iridigm Display Corporation to IDC, LLC

TITLE	Country	Application No.	Filing Date
METHOD AND DEVICE FOR A DISPLAY HAVING TRANSPARENT COMPONENTS INTEGRATED THEREIN	US	60/613290	09/27/04
METHOD AND SYSTEM FOR MAINTAINING PARTIAL VACUUM IN DISPLAY DEVICE	US	60/613502	09/27/04
DISPLAY DEVICE PROTECTIVE PACKAGING PRIOR TO SACRIFICIAL MATERIAL ETCH	US	60/613377	09/27/04
REFLECTIVE DISPLAY DEVICE HAVING VIEWABLE DISPLAY ON BOTH SIDES	US	60/613323	09/27/04
REFLECTIVE DISPLAY DEVICE HAVING VIEWABLE DISPLAY ON BOTH SIDES	US	60/613593	09/27/04
SYSTEM AND METHOD FOR PROVIDING THERMAL COMPENSATION FOR AN INTERFEROMETRIC MODULATOR DISPLAY	US	60/613452	09/27/04
REFLECTIVE DISPLAY PIXELS DEPOSITED IN NON-RECTANGULAR ARRAYS	US	60/613853	09/27/04
DISPLAY DEVICE FABRICATED ON EMBOSSED SUBSTRATE	US	60/613376	09/27/04
METHOD AND DEVICE FOR PROVIDING A DRIVER CHIP USING A BACKPLATE	US	60/613977	09/27/04
METHOD OF FABRICATING A FREE-STANDING MICRO-STRUCTURE	US	60/613299	09/27/04
METHOD OF SELECTIVE ETCHING OF AL-CONTAINING MATERIAL USING ETCH STOP LAYER	US	60/613410	09/27/04
METHOD AND DEVICE FOR INVERSE TYPE INTERFEROMETRIC MODULATION WITH OPAQUE SUBSTRATE	US	60/613566	09/27/04
METHOD AND SYSTEM FOR XENON FLUORIDE ETCHING WITH ENHANCED EFFICIENCY	US	60/613423	09/27/04

PATENT

REEL: 015854 FRAME: 0271

Exhibit A

Assignment, having an effective date of October 1, 2004, from Iridigm Display Corporation to IDC, LLC

TITLE	Country	Application No.	Filing Date
METHOD FOR AND DEVICE HAVING THROUGH BACKPLATE CONNECTION TO BUSES	US	60/613477	09/27/04
PRINTED CIRCUIT BOARD BASED BACKPLATES FOR OPTICAL ARRAYS	US	60/613489	09/27/04
DEVICE HAVING PATTERNED SPACERS FOR BACKPLATES AND METHOD OF MAKING THE SAME	US	60/613478	09/27/04
INTERFEROMETRIC OPTICAL MODULATOR USING FILLER MATERIAL AND METHOD	US	60/613475	09/27/04
SYSTEM AND METHOD FOR INTERFEROMETRIC OPTICAL MODULATOR AND DIFFUSER	US	60/613568	09/27/04
COMPOSITE MIRROR AND MIRROR LAYER FOR OPTICAL MODULATOR AND METHOD	US	60/613538	09/27/04
SYSTEM AND METHOD FOR PROVIDING THERMAL COMPENSATION FOR AN INTERFEROMETRIC MODULATOR DISPLAY	US	60/613610	09/27/04
METHOD AND DEVICE FOR PROTECTING INTERFEROMETRIC MODULATORS FROM ELECTROSTATIC DISCHARGE	US	60/613492	09/27/04
SYSTEM HAVING DIFFERENT UPDATE RATES FOR DIFFERENT PORTIONS OF A PARTITIONED DISPLAY	US	60/613573	09/27/04
METHOD AND SYSTEM FOR SERVER CONTROLLED DISPLAY PARTITIONING AND REFRESH RATE	US	60/613407	09/27/04
SYSTEM WITH SERVER BASED CONTROL OF CLIENT DEVICE DISPLAY FEATURES	US	60/614360	09/27/04
METHOD AND DEVICE FOR BI-STABLE DISPLAY	US	60/613617	09/27/04
METHOD AND DEVICE FOR REDUCING POWER IN INTERFEROMETRIC MODULATION ARRAY	US	60/613404	09/27/04

PATENT

REEL: 015854 FRAME: 0272

Exhibit A

Assignment, having an effective date of October 1, 2004, from Iridigm Display Corporation to IDC, LLC

TITLE	Country	Application No.	Filing Date
METHOD AND SYSTEM FOR SERVER CONTROL OF DRIVER FOR DISPLAY OF CLIENT DEVICE	US	60/613494	09/27/04
SYSTEM AND METHOD FOR PROVIDING A VARIABLE REFRESH RATE OF AN INTERFEROMETRIC MODULATOR DISPLAY	US	60/613526	09/27/04
SYSTEM AND METHOD FOR PROVIDING A MEMS RF VARIABLE ATTENUATOR	US	60/613409	09/27/04
METHOD AND DEVICE FOR REFLECTIVE DISPLAY WITH TIME SEQUENTIAL COLOR ILLUMINATION	US	60/613375	09/27/04
METHOD AND DEVICE FOR GENERATING WHITE IN AN INTERFEROMETRIC MODULATOR DISPLAY	US	60/613504	09/27/04
METHOD AND DEVICE FOR ELECTRICALLY PROGRAMMABLE DISPLAY SEGMENTS	US	60/613379	09/27/04
METHOD AND DEVICE FOR ION IMPLANTED CAVITY WALL FOR MODIFYING ACTUATION AND RELEASE VOLTAGE THRESHOLDS OF A DEFORMABLE MEMBRANE	US	60/613451	09/27/04
SYSTEMS AND METHODS FOR INTERFEROMETRIC MODULATION	US	60/613891	09/27/04
SYSTEM AND METHOD FOR INTERFEROMETRIC MODULATION	US	60/614032	09/27/04

S:\DOCS\IGR\IGR-2145.DOC
102604

PATENT

REEL: 015854 FRAME: 0273

RECORDED: 03/08/2005