

10-18-2004



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Kermit Lopez



October 7, 2004

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ASSIGNMENT

LSI Logic Docket No.: 03-2655

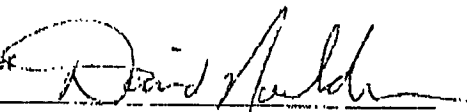
The undersigned is/are the named inventor(s) (the "Inventor(s)") on a United States patent application entitled **TASK QUEUING METHODS AND SYSTEMS FOR TRANSMITTING FRAME INFORMATION OVER AN I/O INTERFACE** (the "Application") for inventions disclosed or claimed therein (the "Invention(s)"), which Application:

- is being executed concurrently herewith,
- was first executed on
- was filed on _____ as Application Number _____
- claims the benefit of U.S. Provisional Application(s) No(s). _____ / _____ filed _____

LSI Logic Corporation, on behalf of itself, its successors and assigns, and its legal representatives ("LSI Logic"), is entitled to all rights in the Application and the Invention(s), and the full cooperation of the Inventor(s).

For valuable consideration, the receipt of which is hereby acknowledged, the Inventor(s) hereby sell(s), assign(s), and transfer(s) to LSI Logic the entire and exclusive right, title and interest in the Application and the Invention(s), in and for the United States, its territories, and all foreign countries, including all applications, patents, design registrations and publications obtained or derived therefrom, world wide, and all priority rights under all present or future conventions and treaties, and any provisional applications for which priority is claimed in the Application; and

- (1) request the U.S. Commissioner of Patents to issue all U.S. Letters Patent granted thereon to LSI Logic;
- (2) agree that LSI Logic may apply for and receive all foreign Letters Patent thereon;
- (3) agree to execute all papers and take all actions necessary or desirable in connection therewith requested by LSI Logic; and
- (4) authorize LSI Logic to subsequently enter the Application Number on this Assignment, if not already entered above.

David Noeldner 

Date Oct 5, 2004

LSI Logic Docket No. 03-2655
Assignment

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