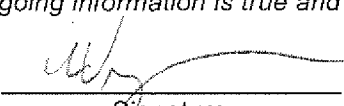


Form PTO-1595 (Rev. 10/02) OMB No. 0651-0027 (exp. 6/30/2005) Tab settings ⇨ ⇨ ⇨ ▼ ▼ ▼ ▼ ▼ ▼ ▼		RECORDATION FORM COVER SHEET PATENTS ONLY		U.S. DEPARTMENT OF COMMERCE U.S. Patent and Trademark Office	
To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.					
1. Name of conveying party(ies): Cognio, Inc. Additional name(s) of conveying party(ies) attached? ☐ Yes ☒ No		2. Name and address of receiving party(ies) Name: <u>IPR LICENSING INC.</u> Internal Address: <u>SUITE 105, HAGLEY BUILDING</u> Street Address: <u>3411 SILVERSIDE ROAD,</u> <u>CONCORD PLAZA</u> City: <u>WILMINGTON</u> State: <u>DELAWARE</u> Zip: <u>19810</u> Additional name(s) & address(es) attached? ☐ Yes ☒ No			
3. Nature of conveyance: ☒ Assignment ☐ Merger ☐ Security Agreement ☐ Change of Name ☐ Other _____ Execution Date: <u>March 9, 2005</u>					
4. Application number(s) or patent number(s): If this document is being filed together with a new application, the execution date of the application is: _____ A. Patent Application No.(s) <u>10/248,432</u> <u>PCT/US04/00915</u> <u>60/509,286</u> Additional numbers attached? ☒ Yes ☐ No B. Patent No.(s)					
5. Name and address of party to whom correspondence concerning document should be mailed: Name: <u>GERALD B. HALT JR.</u> Internal Address: <u>VOLPE AND KOENIG, P.C.</u> <u>UNITED PLAZA, SUITE 1600</u> Street Address: _____ <u>30 SOUTH 17TH STREET</u> City: <u>PHILADELPHIA</u> State: <u>PA</u> Zip: <u>19103</u>		6. Total number of applications and patents involved: <u>68</u> 7. Total fee (37 CFR 3.41).....\$ <u>2720.00</u> ☐ Enclosed ☒ Authorized to be charged to deposit account 8. Deposit account number: <u>22-0493</u> <u>Volpe and Koenig, P.C.</u> (Attach duplicate copy of this page if paying by deposit account)			
DO NOT USE THIS SPACE					
9. Statement and signature. <i>To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.</i> <u>Anthony L. Venezia</u> Name of Person Signing  Signature <u>3/25/2005</u> Date Reg. No. 48,382 Total number of pages including cover sheet, attachments, and documents: <u>14</u>					

CH \$2720.00 220493 10248432

Box 4
Additional Application Numbers or Patent Numbers

Application/Patent Number
60/374,531
60/319,434
60/476,982
60/511,530
10/779,269
PCT/US04/17268
60/361,055
60/365,775
60/365,797
6,687,492
PCT/03/05642
10/695,229
60/365,811
10/249,063
PCT/US03/07561
10/867,249
60/481,399
10/707,312
60/376,722
60/319,336
60/319,360
6,728,517
PCT/US03/12183
10/707,744
60/474,494
60/481,139
10/707,447
PCT/US04/15339
60/380,139
6,785,520
PCT/03/05644
10/800,610
10/064,482
PCT/03/05647
10/707,588
60/319,437
60/461,672
60/479,945
10/627,537
PCT/US03/23408
10/779,268
60/409,677
PCT/US03/28126
10/457,293
10/859,255
60/539,643
11/034,224
60/319,430
6,700,450
60/330,755

Box 4
Additional Application Numbers or Patent Numbers

Application/Patent Number
10/065,494
60/365,774
10/174,690
10/855,279
PCT/03/05646
60/481,995
60/319,275
60/319,335
60/319,334
60/319,337
60/319,629
60/319,672
60/319,361
60/319,518
60/481,327

ASSIGNMENT

WHEREAS, Cognio, Inc. ("Assignor"), a Delaware corporation having a mailing address of 20400 Observation Drive, Suite 206, Germantown, Maryland 20876 is the owner of the entire right, title, and interest in and to the patent properties listed in Schedule A except to the extent set forth in Paragraph 2 "Limitation" of Schedule B (the "Identified IPR") and the know-how, copyrights and other intellectual property rights listed on Schedule B ("the IPR Blocks"); and

WHEREAS, IPR Licensing Inc., a Delaware corporation having a mailing address of Suite 105, Hagley Building, 3411 Silverside Road, Concord Plaza, Wilmington, Delaware 19810 and a wholly owned subsidiary of InterDigital Communications Corporation ("Assignee") is desirous of acquiring the entire right, title, and interest in and to the Identified IPR and IPR Blocks and the additional patent properties identified below (all of said Identified IPR, IPR Blocks, and patent properties being referred to herein as the "Assigned IPR Assets").

NOW, THEREFORE, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, does hereby SELL, ASSIGN, and CONVEY unto Assignee all right, title, and interest throughout the world in and to:

1. The Identified IPR and the IPR Blocks;
2. All inventions disclosed by the Identified IPR and the IPR Blocks;
3. All patents and like protection that have now been or may in the future be granted and that claim the inventions disclosed by the Identified IPR and

the IPR Blocks, whether in the United States of America or in any other country or place anywhere in the world;

4. All Patent Families (as defined in Schedule B hereto) of the Identified IPR and like grants, including without limitation, those obtained or permissible under past, present, and future law or statutes;
5. The right to Assignee to file in its name applications for patents and like protection for said Identified IPR and IPR Blocks in any country or countries foreign to the United States;
6. All international rights or priority associated with said Identified IPR (said Identified IPR, IPR Blocks and any and all rights, including patents and patent applications, covered by Items No. 2-6 hereof collectively referred to herein as the "Assigned IPR Assets");
7. All rights of action on account of past, present, and future unauthorized use of said Assigned IPR Assets and for infringement of said Assigned IPR Assets and like protection; and
8. All past, present, and future rights of recovery for unauthorized use of said Assigned IPR Assets under any provisional rights or like protection;

The U.S. Commissioner of Patents and Trademarks and any and all similarly situated officials in other countries are hereby requested to issue Letters Patent in accordance with this Assignment.

GENERAL

1. Assignee hereby accepts the foregoing assignment but shall not assume any liabilities, debts and obligations associated with the Assigned IPR

Assets, except for obligations for fees to maintain registrations or continue to prosecute the Identified IPR;

2. Assignor shall cooperate with Assignee, at Assignee's sole expense, in any action Assignee reasonably requests that Assignor take in order to effectuate, carry out, or fulfill the parties' intent and/or Assignor's obligations hereunder, including, without limitation, the execution of any instruments and papers that are necessary or desirable, in Assignee's sole discretion, to consolidate, confirm, vest and/or record Assignee's full and complete ownership of the Assigned IPR Assets with, for example, the U.S. Patent and Trademark Office or equivalent foreign offices;
3. This Assignment shall inure to the benefit of Assignee and its successors and assigns and shall be binding upon Assignor and its successors and assigns;
4. This Assignment and all questions relating to its validity, interpretation, performance and enforcement shall be governed by and construed in accordance with the laws of the State of Delaware;
5. This Assignment and the Purchase Agreement between Assignee and Assignor dated March 9, 2005 contain the entire agreement and understanding of the parties relating to the subject matter hereof, and merge and supersede all the parties relating to the subject matter hereof. This Assignment may not be changed or modified, except by an agreement in writing signed by each of the parties; and
6. This Assignment may be executed by facsimile and in counterparts, each of which shall be deemed an original, but all of which together shall constitute one and the same instrument.

IN WITNESS WHEREOF, the parties hereto have caused this Agreement to be executed by their duly authorized representatives and Assignor has delivered this instrument to Assignee effective the 9th day of March, 2005.

ASIGNOR: Cognio, Inc.

Today's Date: March 9, 2005

By: [Signature]

Title: President

State of Maryland

SS.:

County of Frederick

On March 9, 2005 before me, Sarah Bennett, Notary Public, personally appeared John Williams, personally known or proved to me on the basis of satisfactory evidence to be the person whose name is subscribed to the within instrument and acknowledged to me that he/she is executed the same in his/her authorized capacity and that by his/her signature on the instrument the person, or the entity upon behalf of which the person acted, executed the instrument.

Witness my hand and official seal.



Sarah Bennett
NOTARY PUBLIC
FREDERICK COUNTY
MARYLAND
My Commission Expires June 28, 2007

[Signature]
Notary Public

ASSIGNEE: IPR Licensing Inc.

Today's Date: March 14, 2005

By: [Signature]

Title: Vice President

State of DE

SS.:

County of New Castle

On March 14, 2005 before me, Catherine Sinkewicz Notary Public, personally appeared Patrick Donahue, personally known or proved to me on the basis of satisfactory evidence to be the person whose name is subscribed to the within instrument and acknowledged to me that he/she is executed the same in his/her authorized capacity and that by his/her signature on the instrument the person, or the entity upon behalf of which the person acted, executed the instrument.

Witness my hand and official seal.

[Signature]
Notary Public

Schedule A - Identified IPR

CATHERINE E. SINKEWICZ
NOTARY PUBLIC
STATE OF DELAWARE
My Commission Expires Oct. 3, 2007

Matter	Title	Filing Date	Serial Number	Country of Filing	Patent Number
Cognio77US	A Tunable Upconverter Mixer With Image Rejection	01/20/2003	10/248,432	United States of America	
Cognio77PCT	A Tunable Upconverter Mixer With Image Rejection	01/14/2004	PCT/US04/00915	WIPO	
Cognio83US	Compensation Techniques for Group Delay Effects in Transmit Beamforming Radio Communication	02/13/2004	10/779,269	United States of America	
Cognio83PCT	Compensation Techniques for Group Delay Effects in Transmit Beamforming	08/01/2004	PCT/US04/17268	WIPO	
Cognio21PCT	Improving the Efficiency of Power Amplifiers in Devices Using Transmit Beamforming	03/13/2003	PCT/US03/07561	WIPO	
Cognio21US	Improving the Efficiency of Power Amplifiers in Devices Using Transmit Beamforming	03/13/2003	10/249,063	United States of America	
Cognio21US2	Improving the Efficiency of Power Amplifiers in Devices Using Transmit Beamforming	06/14/2004	10/667,249	United States of America	
Cognio99US	Master-Slave Local Oscillator Porting Between Radio Integrated Circuits	12/04/2003	10/707,312	United States of America	
Cognio50CN	Multiple-Input Multiple-Output MIMO Radio Transceiver	12/11/2004	03809045.7	China	
Cognio50US	Multiple-Input Multiple-Output Radio Transceiver	10/11/2002	10/065,388	United States of America	6,728,517 B2
Cognio50TW	Multiple-Input Multiple-Output Radio Transceiver	04/21/2003	92199232	Taiwan	
Cognio50PCT	Multiple-Input Multiple-Output Radio Transceiver	04/21/2003	PCT/US03/12183	WIPO	
Cognio50EP	Multiple-Input Multiple-Output Radio Transceiver	04/21/2003	03726362.1	European Union	
Cognio50US2	Multiple-Input Multiple-Output Radio Transceiver	01/08/2004	10/707,744	United States of America	
Cognio97US	Signal Interfacing Techniques To Simplify Integrated Circuit Radio Designs	12/15/2003	10/707,447	United States of America	

Cognio97PCT	Signal Interfacing Techniques to Simplify Integrated Circuit Radio Designs	05/14/2004	PCT/US04/15339	WIPO	
Aryya40US	System and Method for Antenna Diversity Using Equal Power Joint Maximal Ratio Combining	06/19/2002	10/174,669	United States of America	6,785,520 B2
Cognio40TW	System and Method for Antenna Diversity Using Equal Power Joint Maximal Ratio Combining	02/26/2003	92104066	Taiwan	
Cognio40PCT	System and Method for Antenna Diversity Using Equal Power Joint Maximal Ratio Combining	02/26/2003	PCT/03/05644	WIPO	
Cognio40US2	System and Method for Antenna Diversity Using Equal Power Joint Maximal Ratio Combining	03/15/2004	10/800,610	United States of America	
Aryya18US	System and Method for Antenna Diversity Using Joint Maximal Ratio Combining	06/19/2002	10/174,728	United States of America	6,687,492 B2
Cognio18TW	System and Method for Antenna Diversity Using Joint Maximal Ratio Combining	02/26/2003	92104059	Taiwan	224405
Cognio18PCT	System and Method for Antenna Diversity Using Joint Maximal Ratio Combining	02/26/2003	PCT/03/05642	WIPO	
Cognio18US2	System and Method for Antenna Diversity Using Joint Maximal Ratio Combining	10/28/2003	10/896,229	United States of America	
Cognio38US	System and Method for Joint Maximal Ratio Combining Using Time-Domain Based Signal Processing	07/18/2002	10/064,482	United States of America	
Cognio38TW	System and Method for Joint Maximal Ratio Combining Using Time-Domain Signal Processing	02/26/2003	92104060	Taiwan	1226765
Cognio38PCT	System and Method for Joint Maximal Ratio Combining Using Time-Domain Signal Processing	02/26/2003	PCT/03/05647	WIPO	
Cognio38US2	System and Method for Joint Maximal Ratio Combining Using Time-Domain Based Signal Processing	12/23/2003	10/707,588	United States of America	
Cognio29PCT	System and Method for Multiple-Input Multiple-Output (MIMO) Radio Communication	07/25/2003	PCT/US03/23408	WIPO	
Cognio29US	System and Method for Multiple-Input Multiple-Output (MIMO) Radio Communication	07/25/2003	10/627,537	United States of America	
Cognio29US2	System and Method for Transmit Weight Computation for Vector Beamforming Radio Communication	02/13/2004	10/779,268	United States of America	
Aryya23US	Systems and Methods for Improving Range for Multicast Wireless Communication	06/19/2002	10/174,690	United States of America	

Cognio23TW	Systems and Methods for Improving Range for Multicast Wireless Communication	02/26/2003	92104064	Taiwan	
Cognio23PCT	Systems and Methods for Improving Range for Multicast Wireless Communication	02/26/2003	PCT/03/05646	WIPO	
Cognio23US2	Systems and Methods for Improving Range of Multicast Wireless Communication	05/27/2004	10/855,279	United States of America	
Cognio52US	Techniques for Correcting for Phase and Amplitude Offsets in a MIMO Radio Device	06/09/2003	10/457,293	United States of America	
Cognio52PCT	Techniques for Correcting for Phase and Amplitude Offsets in a MIMO Radio Device	09/09/2003	PCT/US03/28126	WIPO	
Cognio52US2	Techniques for Correcting for Phase and Amplitude Offsets in a MIMO Radio	06/02/2004	10/859,255	United States of America	
Cognio102US	Variable Gain Amplifier with Low Phase Variation	01/12/2005	11/034,224	United States of America	
Cognio57US	Voltage-Controlled Oscillator with Automatic Amplitude Control Circuit	11/13/2002	10/065,719	United States of America	6,700,450 B2
Cognio8US	Improving Throughput in Multi-Rate Wireless Networks Using Variable-Length Packets and Other Techniques	10/30/2001	10/065,494	United States of America	

Matter	Title	Filing Date	Serial Number
30014380.0008	Improving Throughput in Multi-Rate Wireless Networks Using Variable-Length Packets and Other Techniques	10/30/2001	60/330,755
Anyya18Prov	Antenna Diversity Scheme Using Joint Maximal Ratio Combining	03/01/2002	60/361,055
30014380.0021	Improving Efficiency of Power Amplifiers for WLAN Terminals Using Transmit Beamforming	03/21/2002	60/365,811
30014380.0022	Method for Maintaining Channel State Information at the Transmitter to Improve Link Quality in Multi-User WLAN Radio Systems	03/21/2002	60/365,775
30014380.0020	Improving Range and Throughput of Wireless LANs in Frequency Selective Fading Environments	03/21/2002	60/365,797
30014380.0023	Techniques for Improving Range in Composite Beamforming-Enhanced 802.11x Networks	03/21/2002	60/365,774

Aryya31Prov	System and Architecture for Wireless Transceiver Employing Composite Beamforming and Spectrum Management Techniques	04/22/2002	60/374,531
Aryya35Prov	Reducing Cost of a Half-Duplex Transceiver Integrated Circuit By Sharing a Single Filter for Receive and Transmit Operations	04/29/2002	60/376,722
Aryya40Prov	Antenna Diversity Scheme Using Equal Gain Composite Beamforming	05/06/2002	60/380,139
Aryya53Prov	System and Method for Sharing an ADC and a DAC in a Half-Duplex Radio Transceiver	06/21/2002	60/319,336
Cognio44Prov	Half-Duplex Radio Transceiver Supporting Dual Band and Scalable Multi-Channel Operations	06/27/2002	60/319,360
Cognio57Prov	Voltage-Controlled Oscillator with Automatic Amplitude Control Circuit	07/29/2002	60/319,430
Cognio29Prov	System and Method for Vectorized Data Transmission Between Communication Devices	07/30/2002	60/319,437
Cognio50Prov	Radio Transceiver Having Multiple Integrated Receive and Transmit Paths and a Wideband Operation Mode	07/30/2002	60/319,434
Cognio52Prov	Techniques for Correcting Phase Mismatch in MIMO Radio Transceivers	09/10/2002	60/409,677
Cognio29Prov2	System and Method for Equal Power Vectorized Data Communication	04/10/2003	60/461,672
Cognio83Prov	Synchronization Algorithm to Compensate for Group Delay Effects on Transmit Beamforming	05/09/2003	60/476,982
Cognio29Prov3	System and Method for Vectorized Radio Communication	06/19/2003	60/479,945
Cognio97Prov	Signal Multiplexing Techniques to Simplify Integrated Radio Circuit Design	07/25/2003	60/481,139
Cognio89Prov	Master-Slave Local Oscillator Porting Technique Between Multiple Integrated Circuits	09/19/2003	60/481,399
Cognio29Prov4	System and Method for Transmit Weight Computation for Multiple-Input Multiple-Output (MIMO) Radio Communication	10/15/2003	60/511,530
Cognio102Prov	Variable Gain Amplifier With Low Phase Variation	01/28/2004	60/539,643
Cognio97Prov2	Sharing a Connection Pin on a Radio Integrated Circuit for Transmit and Receive Signals	02/02/2004	60/481,995

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Cognio73Prov	Interface Between MIMO Radio Chip and Baseband Chip	05/30/2003	60/474494
Aryya45Prov	RF Amplifier with Bias Boosting Scheme Using a Voltage Divider	05/30/2002	60/319,275
Aryya48Prov	RF Amplifier with a Self-Bias Boosting Scheme Using PNP Transistors	06/21/2002	60/319,335
Aryya47Prov	Bias Boosting Schemes for Cascode-Configured RF Transistors	06/21/2002	60/319,334
Aryya56Prov	RF Amplifier with a Bias Boosting Scheme for a Complementary Push-Pull Configuration	06/21/2002	60/319,337
Cognio66Prov	Self-Bias Boosting Schemes for a Differential RF Amplifier	10/18/2002	60/319,629
Cognio72Prov	RF Amplifier with a Stable Bias Boosting Scheme	11/06/2002	60/319,672
Cognio55Prov	Method of Testing the Divider Circuitry of an Integrated Integer-N Style PLL or Fractional-N Style PLL	08/27/2002	60/319,361
Cognio62Prov	Frequency Synthesizer for Multi-Band Super-Heterodyne Transceiver Applications	09/04/2002	60/319,518
Cognio110Prov	A 5GHz Direct Conversion Receiver with DC Offset Correction (Published May, 2004, International Symposium on Circuits and Systems, pp. IV, 269-272)	10/07/2003	60/509,286
Cognio79Prov	A Fully Integrated Power Detector	09/04/2003	60/481,327

Schedule B – IPR Blocks

1) **IPR BLOCKS ASSIGNED:** Any and all know-how, copyrights and other intellectual property rights related to the Identified IPR set forth on Schedule A hereto (except to the extent set forth in Paragraph 2 below) and the FPGA Development Platforms, but excluding trademarks and the Excluded IPR Blocks.

2) **LIMITATION:** US provisional patent applications identified as Aryya31Prov (Serial No. 60/374,531) and Cognio73Prov (Serial No. 60/474494) each contain information related to the Excluded IPR Blocks, and that, notwithstanding anything to the contrary in this Agreement, Assignee's rights related to such applications extend only to support the priority date for US Patent Number 6,728,517 (with respect to Aryya31Prov) and US Serial No. 10/707, 447 and PCT Serial No. PCT/USQ4/15339 (with respect to Cognio73Prov), all of which are part of the Identified IPR. Except for this patent and these non-provisional patent applications (and their divisionals, continuations, continuations in part, reissues, reexaminations, and foreign equivalents), no other Patent Families associated with such provisional applications are transferred under this Agreement.

3) **NO ASSIGNMENT:** No rights are being assigned to the Excluded IPR Blocks.

4) **DEFINITIONS:**

"Excluded IPR Blocks" means any and all patents, know-how, copyrights and other intellectual property rights related to Seller's ongoing spectrum analysis/management business. Notwithstanding anything to the contrary contained in this Agreement (with the exception of paragraph 2 "Limitation" of this Schedule B above), the Identified IPR shall not be considered part of the Excluded IPR Blocks.

"FPGA Development Platforms" means two MIMO technology development boards, one that receives and the other that transmits, with FPGA Xilinx programmable chips which together form a 4x4 configuration.

"IPR Blocks" means any and all know-how, copyrights and other intellectual property rights related to the Identified IPR (except to the extent set forth in paragraph 2 "Limitation" of this Schedule B above) and the FPGA Development Platforms , but excluding trademarks, the Identified IPR, and the Excluded IPR Blocks.

"Patent Families" means a patent application or patent and all associated patents and patent applications (including without limitation divisionals, continuations, continuations in part, reissues, reexaminations, and foreign equivalents thereof), if any, that share any common priority date or identical specification. In the case of continuations in part that include new matter, the new matter shall be considered part of the same Patent Family as the matter bearing the same priority date or identical specification.