y '	na nata	11 00 0004	11-1-04			
FORM PTO-1595		11-23-2004	OR RECORDATION			
	The state of the s		Atty. Docket: 3344/6			
1:	To the Commissioner Name of conveying party(ies).	102888101	he attached original documents or copy thereof. ame and address of receiving party(ies):			
-	Ching-Yuan WU		Name: SILICON-BASED TECHNOLOGY CORP.			
	Additional name(s) of conveying Yes	ng party(ies) attached? \overline{X} No	Foreign Address: 1F, No. 23, R&D Rd. 1			
3.	Nature of conveyance: X Assignment Security Agreement Other	☐ Merger ☐ Change of Name	Hsinchu, Taiwan, R.O.C. Domestic Address:			
	Execution Date: October 4, 20	04	City: State ZIP			
			Additional name(s) & address(es) attached? Yes X No			
4.	If this document is being filed together with a new application, the execution date of the application is: October 4, 2004 A. Patent Application No.(s) B. Patent Registration No.(s)					
		Additional numbers attached?	☐ Yes X No			
5.	Name and address of party to v	e mailed:	6. Number of applications and patents involved: One (1)			
	Name: Dennison, Schultz, Dougherty & MacDonald					
	1727 King Street		7. Total fee (37 CFR 3.41): \$40.00			
	Suite 105		X Enclosed Authorized to be charged to deposit account			
	Alexandria, Virgi	nia 22314	8. Deposit account number			
	Telephone No.: (703) 837-9 Facsimile No.: (703) 837-0		(Attach duplicate copy of this page if paying by deposit account):			
	DO NOT USE THIS SPACE					
9.	Statement and signature.					
To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document. David E. Dougherty November 1, 2004						
Name of Person Signing Registration No. 19,576 Total number of pages including cover sheet, attachments, and documents: 3						

BHT/dam

ASSIGNMENT

Docket No. 3344/6

This assignment agreement is applicable to an invention entitled (Invention Title)					
SELF-ALIGNED TRENCH DMOS TRANSISTOR STRUCTURE AND ITS					
MANUFACTURING METHODS					
The DATENT DIGHTS referred to in this agreement are:					
The PATENT RIGHTS referred to in this agreement are:					
(check one)⊠a patent application for this invention, executed by the ASSIGNOR(S)					
concurrently with this assignment.					
U.S. patent application Serial No,filed					
a U.S. patent application based on PCT International Application					
Nofiled on (date)(U.S. patent application					
Serial No, if known).					
U.S. patent No, issued					
The PATENT RIGHTS also include all divisions reissues, continuations and extensions of					
the patents and patent applications identified above.					
The PATENT RIGHTS assigned under this agreement are:					
(check one)⊠U.S. patent rights only.					
Worldwide patent rights. In this case, the assignee shall have the right to					
claim the benefit of the filing date of any U.S. or foreign patent application					
for this invention.					
The ASSIGNOR(S) referred to in this agreement is (or are) the inventor(s) whose signatures					
appear on page 2 of this Assignment and any Supplemental Sheet(s).					
The ASSIGNEE referred to in this agreement is:					
(Name of Assignee) Silicon-Based Technology Corp.					
(Address) 1F, NO.23, R&D RD.1, SCIENCE-BASED INDUSTRIAL PARK,					
HSINCHU, TAIWAN, R.O.C.					
The ASSIGNEE is:					
(check one) An individual.					
☐A Partnership.					
☐ A Corporation of					
(other)					
The ASSIGNOR(S), in consideration of \$10.00 paid by the ASSIGNEE, and other good and					
valuable consideration, receipt of which is acknowledged, hereby assign(s) the following					
rights to the ASSIGNEE, its successors and assigns:					
the full and exclusive right to the invention;					
the entire right, title and interest in and to the PATENT RIGHTS;					
the right to sue and recover for any past infringement; and					
the right to claim priority under 35 USC 119, 35 USC 120, or any other applicable					
provisions, based on any earlier patent applications for this invention.					

PATENT REEL: 015996 FRAME: 0908

THIS IS PAGE 2 OF AN ASSIGNMENT FROM THE INVENTOR(S) TO

ASSIGNEE: Silicon-Based Technology Corp.

INVENTION TITLE: SELF-ALIGNED TRENCH DMOS TRANSISTOR

STRUCTURE AND ITS MANUFACTURING METHODS

As to all U.S. patent applications assigned under this agreement, the ASSIGNOR(S) hereby authorize(s) and request(s) the Commissioner of Patents and Trademark to issue all Letters Patent to the ASSIGNEE as the assignee of the entire right, title and interest, for the sole use and enjoyment of said ASSIGNEE, its successors and assigns.

Further, the ASSIGNOR(S) agree(s) to communicate to said ASSIGNEE, or its representatives, any fact known to the ASSIGNOR(S) with respect to said invention, and testify in any legal proceeding, sign all lawful papers, execute all divisional, continuation, substitute, renewal, reexamination and reissue applications, execute all necessary assignment papers to cause and all Letter Patent to be issued to said ASSIGNEE, make all rightful oaths and generally do everything necessary or desirable to aid said ASSIGNEE, its successors and assigns, to obtain and enforce proper protection for said invention.

The ASSIGNOR(S) authorize(s) the attorneys and agents who have the power of attorney in this application to check any appropriate boxes and to insert the Serial Number and filing date in this document after it has been executed.

Ching-Yuan WU	Ching-Guan Wh	Oct. 4, 2004
Name of sole or first inventor	Signature	Date
Name of second inventor, if any	Signature	Date
Name of third inventor, if any	Signature	Date
Name of fourth inventor, if any	Signature	Date
Name of fifth inventor, if any	Signature	Date
Name of sixth inventor, if any	Signature	Date
Name of seventh inventor, if any	Signature	Date

RECORDED: 11/01/2004

PATENT REEL: 015996 FRAME: 0909