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| —————————————————————————————————————— | document is being filed together with a new application. B. Patent No.(s) |
| A. Patent Application No.(s) 10/857,212 | B. Faterit No.(5) |
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(Case No. 205-E,001)

U.S. Serial No.: 10/857,212 Inventor: Ansgar Stammermann

Title: Process and Device for Circuit Design by Means of High-Level Synthesis

U.S. Filing Date: May 28, 2004

NR. 7536 S. 2/2

02

I, the undersigned, for good and valuable considerations in hand paid, the receipt and sufficiency whereof are hereby acknowledged, hereby assign to

Kuratorium OFFIS e.V.

its successors and assigns, the entire right, title and interest in the inventions and/or improvements of the undersigned disclosed in the above-referenced application for Letters Patent of the United States, and in said application and any and all other applications, both United States and foreign, which may be filed, either solely or jointly with others, on said inventions and/or improvements, and in any and all Letters Patent of the United States and foreign countries, which may be obtained on any of said applications, and in any reissue or extension of such patents, and further assign to said assignee the priority right provided by the International Convention.

I, the undersigned, hereby authorize and request the Commissioner of Patents and Trademarks to issue said Letters Patent to said assignes.

Date: May 25, 1004

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