

01-05-2005



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To the Honorable Commissioner of Patents and Trademarks. Please record the attached original documents or copy thereof.

40-02-21

19270 U.S. PTO
11/017017

122004

1. Name of conveying party(ies):

TODD JASON YOUNGMAN
JOHN EMERY NORDMAN

2. Name and address of receiving party(ies):

Name: LSI Logic Corporation

Internal Address:
Mail Stop D-106

Additional names of conveying party(ies) attached?

☐ yes ☒ no

Street Address: 1551 McCarthy Boulevard

City Milpitas

State California Zip 95035

3. Nature of Conveyance

☒ Assignment ☐ Merger

☐ Security Agreement ☐ Change of Name

☐ Other _____

Additional name(s) & address(es) attached? ☐ yes ☒ no

Execution Date: 20 December 2004

4. Application number(s) or patent number(s):

TITLE: LANGUAGE AND TEMPLATES FOR USE IN THE DESIGN OF SEMICONDUCTOR PRODUCTS

If this document is being filed together with a new application, the execution date of the application is 20 December 2004.

A. Patent Application No(s).

LSI Logic Docket 04-0824

B. Patent No(s).

Additional numbers attached? ☐ Yes ☒ No

5. Name and address of party to whom correspondence concerning document should be mailed:

24319

24319

PATENT TRADEMARK OFFICE

6. Total number of applications and patents involved: 1

7. Total fee (37 CVF 3.41):

\$ 40.00

☐ Enclosed

☒ Authorized to be charged to deposit account

☒ Total fee due

☒ Any deficiencies in the enclosed fees

8. Deposit account number:

12-2252

(Attach duplicate copy of this page if paying by deposit account)

01/05/2005 DBYRHE 00000012 122252 11017017

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9. Statement and signature

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Karuna Ojanen
Name of Person Signing

[Signature]
Signature

20 December 2004
Date

Total number of pages comprising cover sheet: 1

A S S I G N M E N T

Whereas, we

(1) Todd Jason Youngman,
County of Olmsted,City of Rochester,
and State of Minnesota,(2) John Emery Nordman,
County of Olmsted,City of Rochester,
and State of Minnesota,

have invented certain improvements in

LANGUAGE AND TEMPLATES FOR USE IN
THE DESIGN OF SEMICONDUCTOR PRODUCTS

and have executed, respectively, a United States patent application therefor on

Dates Inventors (1) 20 December 2004, (2) *20 December 2004*
Signed Declaration

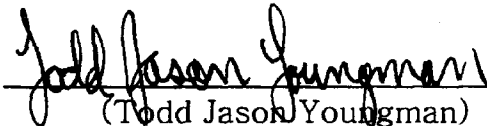
And whereas, LSI LOGIC CORPORATION, a corporation of Delaware, having a place of business at Milpitas, California (hereinafter called LSI Logic) desires to acquire the entire right, title and interest in the said application and invention, and to any United States and foreign patents to be obtained therefor;

Now therefore, for a valuable consideration, receipt whereof is hereby acknowledged, we the above named hereby sell, assign, and transfer to LSI Logic, its successors and assigns, the entire right, title and interest in the said application and invention therein disclosed for the United States and foreign countries, and all rights of priority resulting from the filing of said United States application, and we request the Commissioner of Patents to issue any Letters Patent granted upon the invention set forth in said application to LSI Logic, its successors and assigns; and we hereby agree that LSI Logic may apply for foreign Letters Patent on said invention and we will execute all papers necessary in connection with the United States and foreign applications when called upon to do so by LSI Logic.

Signed

Inventor at Rochester, MN

(1)

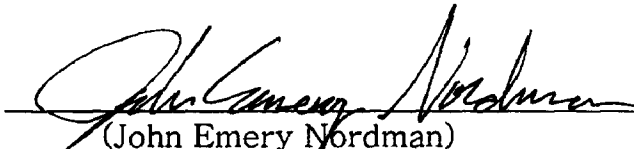
on 20 December 2004


(Todd Jason Youngman)

Inventor's Name

Inventor
SignatureInventor at Rochester, MN

(2)

on 12-20-04


(John Emery Nordman)

Inventor's Name

Inventor
Signature