

01/25/05

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Form PTO-1595

OMB No. 0651-0027 (exp. 6/30/2005)

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U.S. DEPARTMENT OF COMMERCE
U.S. Patent and Trademark Office

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To the Director of the U.S. Patent and Trademark Office: Please record the attached documents or the new address(es) below.

1. Name of conveying party(ies):

MAGEPOWER CORPORATION
formerly MEGAMOS CORPORATION

Execution Date: January 18, 2000

Additional name(s) of conveying party(ies) attached? ☐ Yes ☒ No

2. Name and address of receiving party(ies):

Name: Silicon Services Consortium, Inc.

Street Address: 3500 Comsouth Drive
Suite 100

City: Austin State: TX ZIP: 78744

Additional name(s) and addresses attached? ☐ Yes ☒ No

3. Nature of conveyance

- ☐ Assignment ☐ Merger
- ☐ Security Agreement ☐ Change of Name
- ☐ Government Interest Assignment
- ☐ Executive Order 9424, Confirmatory License
- ☒ Other: Bill of Sale of Intellectual Property

4. Application number(s) or patent numbers:

☐ This document is being filed together with a new application

A. Patent Application No.(s):

B. Patent No.(s):

U.S. Pat. No. 5,767,567 issued on June 16, 1998

Additional numbers attached? ☐ Yes ☒ No

5. Name and address of party to whom correspondence concerning document should be mailed:

Customer No.: 35690

Eric B. Meyertons

Meyertons, Hood, Kivlin, Kowert & Goetzel

P.O. Box 398

Austin, Texas 78767-0398

6. Total number of applications and patents involved: 17. Total Fee (37 C.F.R. 1.21(h) & 3.41): \$40.00

- ☒ Fee Authorization Form Enclosed
- ☐ Authorized to be charged to deposit account
- ☐ None required (government interest not affecting title)

8. Deposit Account No.: 50-1505/5914-00900/EBM

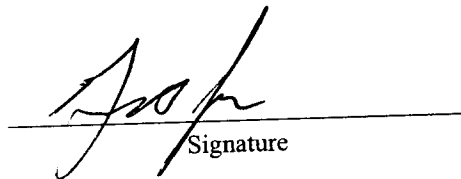
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9. Statement and signature:

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Gareth M. Sampson

Reg. No. 52,191



Signature

1/21/05

Date

Total number of pages including cover sheet, attachments and documents: 13

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PATENT
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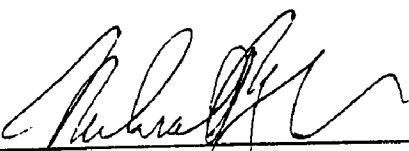
BILL OF SALE

The undersigned, MICHAEL D. MYERS, State Court Receiver for Magepower Semiconductor Corporation ("Seller"), for good and valuable consideration hereby sells, assigns and transfers to Silicon Services Consortium, Inc. A Texas Corporation, all of the patents, trademarks, pending applications for trademarks, copyrights, procedure manuals, manufacturing processes, blueprints, and schematic designs of all intellectual property which formerly belonged to Magepower Semiconductor Corporation and in which the Seller has an interest. The sale includes, but is not limited to, all rights that the Seller has in the patents attached hereto as Exhibit 2. Seller makes no warranty or representations regarding the accuracy, correctness, completeness or value of the intellectual properties that are sold to Seller, including, but not limited to those enumerated in the attached Exhibit 2.

Seller for himself and his successors and assigns, covenants and agrees to execute and deliver to Buyer any further documents that Buyer may subsequently require to pass title to any of the intellectual properties that are transferred by this instrument.

In witness whereof, this document has been executed and delivered by Seller to Silicon Services Consortium, Inc this 18 day of January, 2000.

SELLER: Michael D. Myers,
State Court Receiver for
Magepower Semiconductor Corporation



MICHAEL D. MYERS

EXHIBIT 2

PATENT
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MAGPOWER PRESENTS THE

DATE REVISED: 5/11/1998

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MEGAPOWER PROPRIETARY INFORMATION

PATENT
REEL: 016182 FRAME: 0099

HAGERPOWER PROPRIETARY INFORMATION

PATENT LOG

PATENT LOG

YEAR	DOCKET #	SERIAL #	DATE FILED	DATE OF ISSUE
1996	MMOS 96-001	NEW APPLICATION TRANSMITTAL	4/15/96	
	MMOS 96-002	08/634, 686 08/928, 687	4/13/96 11/28/97	9/23/97 DUE 12/09/98
	MMOS 96-003	NEW APPLICATION TRANSMITTAL	4/28/96	
	MMOS 96-004	08/628, 393	4/6/96	12/28/97
	MMOS 96-005	8230/0548	9/12/96	
	MMOS 96-006	08/728, 834	10/10/96	12/11/96
	MMOS 96-007	NEW APPLICATION TRANSMITTAL	12/11/96	
	MMOS 96-008	NEW APPLICATION TRANSMITTAL	10/28/96	
	MMOS 96-009	08/810, 546	3/3/97	DUE DATE 11/29/98
	MMOS 96-010	NEW APPLICATION TRANSMITTAL	5/2/97	

TITLE: PATENT LOG
FORM#: PG40 - A

DATE REVISED: 5/11/1998

PATENT
REEL: 016182 FRAME: 0100

MAGNETIC PROPRIETARY INFORMATION

PATENT LOG

PATENT LOG

YEAR	DOCKET #	SERIAL #	DATE FILED	DATE OF ISSUE
1997	MMOS 97-001	08/792, 228	8/27/97 1/21/97	9/27/97 DUE - 11/30/98
	MMOS 97-002	NEW APPLICATION TRANSMITTAL	7/10/97	
	MMOS 97-003	NEW APPLICATION TRANSMITTAL	3/11/97	
	MMOS 97-004	NEW APPLICATION TRANSMITTAL	4/18/97	
	MMOS 97-005	NEW APPLICATION TRANSMITTAL	5/20/97	
	MMOS 97-006	08/607, 715	6/21/97	9/20/97
	MMOS 97-007	09/874, 357	8/13/97	DUE DATE 11/30/98
	MMOS 97-008	NEW APPLICATION TRANSMITTAL	10/16/97	
	MMOS 97-009	NEW APPLICATION TRANSMITTAL	7/16/97	
	MMOS 97-010	NEW APPLICATION TRANSMITTAL	7/23/97	
	MPWR 97-11	NEW APPLICATION TRANSMITTAL	9/15/97	
	MPWR 97-12	NEW APPLICATION TRANSMITTAL	9/19/97	
	MPWR 97-13	NEW APPLICATION TRANSMITTAL	10/2/97	
	MPWR 97-14	NEW APPLICATION TRANSMITTAL	11/6/97	

TITLE: PATENT LOG
FORM: FO40 - A

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MagnaPOWER PROPRIETARY INFORMATION

PATENT LOG

PATENT LOG

YEAR	DOCKET #	SERIAL #	DATE FILED	DATE OF ISSUE
1998	MPWR 98-001	NEW APPLICATION TRANSMITTAL	3/3/98	
	MPWR 98-002	NEW APPLICATION TRANSMITTAL	5/27/98	
	MPWR 98-003	NEW APPLICATION TRANSMITTAL	4/28/98	
	MPWR 98-004	NEW APPLICATION TRANSMITTAL	6/2/98	
	MPWR 98-005	NEW APPLICATION TRANSMITTAL	6/12/98	
	MPWR 98-006	NEW APPLICATION TRANSMITTAL	6/11/98	
	MPWR 98-007	NEW APPLICATION TRANSMITTAL	8/11/98	
	MPWR 98-008	FENWICK & WEST LLP 09/159,973	9/10/98	
	MPWR 98-009	FENWICK & WEST LLP 09/059, 534	4/13/98	
	MPWR 98-010	KAM T. TAM 5,747,353	5/5/98	

TITLE: PATENT LOG
FORM#: F040 - A

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MANUFACTURER PROPRIETARY INFORMATION

PATENT LOG

PATENT LOG

YEAR	DOCKET #	TITLE	SERIAL #	DATE FILED	DATE OF ISSUE	INVENTOR(S)
1998	MMOS 96-001	IMPROVED MOSFET STRUCTURE AND FABRICATION PROCESS IMPLEMENTED BY FORMING DEEP AND NARROW.....	NEW APPL TRANSMITTAL	4/15/96		S. SO HSHIEH
	MMOS 96-002	NOVEL MOSFET TERMINATES DESIGN AND CORE CELL CONFIGURATION TO INCREASE BREAKDOWN VOLTAGE.....	08/638, 606 08/928, 867	4/19/96 11/26/97	9/23/97 UE 12/09/9	S. SO et al
	MMOS 96-003	IMPROVED MOSFET STRUCTURE AND FABRICATION PROCESS FOR DECREASING THRESHOLD VOLTAGE.	NEW APPL TRANSMITTAL	4/26/96		HSHIEH et al
	MMOS 96-004	POWER MOSFET DEVICE MANUFACTURED WITH SIMPLIFIED FABRICATION PROCESS TO ACHIEVE RUGGEDNESS & PRODUCT.....	08/628, 393	4/8/96	12/26/97	S. SO et al
	MMOS 96-005	NOVEL CELL TOPOLOGY FOR SEMICON. POWER DEVICES WITH SHORTENED SOURCE WIDTH TO ACHIEVE DEVICE.....	8230/0648	9/12/96		S. SO et al
	MMOS 96-006	IMPROVED STRUCTURE OF FABRICATION PROCESS FOR TRENCHED CMOS PROC. FOR J-EFET RESISTANCE.....	08/728, 834	10/10/96	12/11/96	HSHIEH Y. M. TSUI
	MMOS 96-007					
	MMOS 96-008	POWER MOSFET DEVICE MANUFACTURED WITH IMPROVED FAB. PROCEDURE TO ACHIEVE ENHANCE.....	NEW APPL TRANSMITTAL	10/28/96		S. SO et al
	MMOS 96-009	IMPROVED STRUCTURE AND FAB PROCESS TO PROVIDE EFFECTIVE CHANNEL STOP- IN ERMINATION AREA FOR TRENCHED.....	09/610, 546	3/3/97	DUE DATE 11/29/98	S. SO et al
	MMOS 96-010	POWER TRANSISTOR CELLS PROVIDED WITH RELIABLE TRENCHED SOURCE CONTACTS CONNECTED TO.....	NEW APPL TRANSMITTAL	5/8/97		HSHIEH et al

TITLE: PATENT LOG
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HIGHPOWER PROPRIETARY INFORMATION

PATENT LOG

YEAR	DOCKET #	TITLE	SERIAL #	DATE FILED	DATE OF ISSUE	INVENTOR(S)
1997	MMOS 97-001	IMPROVED GATE CONTACT STRUCTURE TO PREVENT CONTROL METER PENETRATE THROUGH GATE LAYER.	08/792, 225	1/31/96 1/21/97	9/22/97 DUE -11/30/98	TRUE LIN SYDNEY SO FWU HSHIEH
	MMOS 97-002	SEMICONDUCTOR POWER DEVICE WITH FLEXIBLE BILATERAL SWITCH STRUCTURE	NEW APPL TRANSMITTAL	7/10/97		SYDNEY SO TRUE LIN Y.M. TSUI
	MMOS 97-003	HIGH DENSITY POWER TRANSISTOR WITH PUNCH THROUGH PREVENTING REDUCED JFET RESISTANCE.	NEW APPL TRANSMITTAL	3/11/97		SYDNEY SO TRUE LIN Y.M. TSUI
	MMOS 97-004	SELF-ADJUSTED & SELF ALIGNED HIGH DENSITY POWER TRANSISTOR WITH GATE SIDEWALLS.	NEW APPL TRANSMITTAL	4/18/97		SYDNEY SO TRUE LIN Y.M. TSUI
	MMOS 97-005	HIGH DENSITY TRENCHED OMOS TRANSISTOR WITH IMPROVED SOURCE DOPING PROFILE.	NEW APPL TRANSMITTAL	5/20/97		FWU HSHIEH SYDNEY SO Y.M. TSUI
	MMOS 97-006	POWER MOSFET TRANSISTOR WITH IMPROVED GATE INTEGRITY & IMPROVED RUGGEDNESS.	08/807, 715	2/27/96	9/20/97	FWU HSHIEH SYDNEY SO Y.M. TSUI
	MMOS 97-007	EDGE WRAP-AROUND PROTECTIVE EXT. FOR COVERING AND PROTECTING EDGES OF THICK OXIDE LAYER.	09/874, 357	6/13/97	DUE DATE 11/30/98	SYDNEY SO FWU HSHIEH Y.M. TSUI
	MMOS 97-008	ENHANCING OMOS DEVICE RUGGEDNESS BY REDUCING TRANSISTOR PARASITIC RESISTANCE BY INDUCING.	NEW APPL TRANSMITTAL	10/18/97		FWU HSHIEH
	MMOS 97-009	NOVEL CELL TOPOLOGY FOR POWER TRANSISTOR WITH INCREASED PACKING DENSITY.	NEW APPL TRANSMITTAL	7/16/97		FWU HSHIEH SYDNEY SO
	MMOS 97-010	DEVICE RUGGEDNESS IMPROVEMENT AND/OR RESISTANCE REDUCTION FOR POWER MOSFET.	NEW APPL TRANSMITTAL	7/23/97		FWU HSHIEH SYDNEY SO DANNY NIM
	MPWR 97-11	OMOS TRANSISTOR MANUFACTURED WITH REDUCED NUMBER OF MASKS AND ENHANCED RUGGEDNESS.	NEW APPL TRANSMITTAL	9/15/97		FWU HSHIEH SYDNEY SO DANNY NIM
	MPWR 97-12	NOVEL MOSFET STRUCTURE MANUFACTURED BY SIMPLIFIED METHOD AT LOWER COSTS OF IMPROVED RELIABILITY.	NEW APPL TRANSMITTAL	9/19/97		FWU HSHIEH S.L. WENG
	MPWR 97-13	MOSFET POWER DEVICE MANUFACTURED WITH REDUCED NUMBER OF MASKS BY FAB SIMPLIFIED PROCESS.	NEW APPL TRANSMITTAL	10/2/97		FWU HSHIEH Y.M. TSUI
	MPWR 97-14	HIGH SPEED MOSFET POWER DEVICE WITH ENHANCED RUGGEDNESS FABRICATED BY SIMPLIFIED PROCESS.	NEW APPL TRANSMITTAL	11/8/97		FWU HSHIEH SYDNEY SO

TITLE: PATENT LOG
FORM#: F040 - A

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HighPOWER SUPPLEMENTARY INFORMATION

PATENT LOG

YEAR	DOCKET #	TITLE	SERIAL #	DATE FILED	DATE OF ISSUE	INVENTOR(S)
1998	MPWR 98-001	SCALABLE MANUFACTURE PROCESS FOR POWER MOSFET WITH FULLY SELF-ALIGNED GATE AND DRAIN	NEW APPL TRANSMITTAL	5/20/98		TED OKASHITA S.L. WENG FWU HSHIEH
	MPWR 98-002	DOUBLE GATE OXIDE FOR REDUCING GATE PROCESS CAPACITANCE IN TRENCHED CMOS WITHOUT INCREASING THRESHOLD VOLTAGE	NEW APPL TRANSMITTAL	2/27/98		SYDNEY SO AINGLUYEN S.L. WENG
	MPWR 98-003	DOUBLE GATE OXIDE FOR REDUCING GATE DRAIN CAPACITANCE IN TRENCHED CMOS.	NEW APPL TRANSMITTAL	4/28/98		FWU HSHIEH
	MPWR 98-004	ENHANCING RUGGEDNESS FOR TRENCHED CMOS DEVICE.	NEW APPL TRANSMITTAL	6/2/98		FWU HSHIEH SYDNEY SO Y.M. TSUI
	MPWR 98-005	TRENCHED CMOS DEVICE WITH LOW GATE CHARGES.	NEW APPL TRANSMITTAL	6/12/98		FWU HSHIEH SYDNEY SO Y.M. TSUI
	MPWR 98-006	LATERAL-VERTICAL HYBRID LOW DROP-OUT REGULATOR.	NEW APPL TRANSMITTAL	6/11/98		SYDNEY SO ZEN WU Y.M. TSUI
	MPWR 98-007	TRENCHED CMOS DEVICE PROVIDED WITH BODY DOPANT REDISTRIBUTION.....	NEW APPL TRANSMITTAL	8/21/98		FWU HSHIEH
	MPWR 98-008	STRUCTURE AND METHOD FOR IMPROVING DEVICE RUGGEDNESS SPR.	FENWICK & WEST LLP 09/159,973	9/10/98		FWU HSHIEH
	MPWR 98-009	STRUCTURE WITH NON-ORTHOGONAL POLYGRAM FOR POWER.....	FENWICK & WEST LLP 09/069, 534	4/13/98		FWU HSHIEH
	MPWR 98-010	SEMICONDUCTOR STRUCTURE CONTROLLED BREAKDOWN PROTECTION.	KAM T. TAM 9,747,863	5/6/98		SYDNEY SO FWU HSHIEH DANNY NIM

TITLE: PATENT LOG
FORM#: F040 - A

DATE REVISED: 5/11/1999

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PATENT
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