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(Rev. 10/02)	
OMB No. 0651-0027 (exp. 6/30/2005)	
Tab settings ➡ ➡ ➡ V	Please record the attached original documents or copy thereof.
Name of conveying party(ies):	2. Name and address of receiving party(ies)
1) Kuei Shun Chen	Name: Taiwan Semiconductor Manufacturing Co., Ltd.
2) Chin-Hsiang Lin 3) Chih-Cheng Chiu	Internal Address:
3) Chin-Cheng Chiu	آ ۾
Additional name(s) of conveying party(es) attached? Yes V	
3. Nature of conveyance:	
✓ Assignment	262
	Street Address: No. 8, Li-Hsin Road 6
Security Agreement Change of Name	Science-Based Industrial Park
Other	Science-pased industrial Faik
	City: Hsin-Chu State: Zip:
	Country: Taiwan, 300-77 R.O.C.
Execution Date: 01/25/2005	Additional name(s) & address(es) attached? Yes Vo
4. Application number(s) or patent number(s):	
If this document is being filed together with a new appli	cation, the execution date of the application is: 01/25/2005
A. Patent Application No.(s)	B. Patent No.(s)
A. Faterit Application No.(s)	
Additional numbers attached? Yes No	
5. Name and address of party to whom correspondence	6. Total number of applications and patents involved:
concerning document should be mailed:	7. Total fee (37 CFR 3.41)\$ 40.00
Name: William H. Murray	
Internal Address:	Enclosed
internal Address.	Authorized to be charged to deposit account
	Tradition 250 to 35 on all god to deposit decision
	8. Deposit account number:
Street Address: Duane Morris LLP	04 4670
One Liberty Place	04-1679
One Liberty Flace	
City: Philadelphia State: PA Zip: 19103-7396	
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DO NOT USE THIS SPACE	
9. Signature.	
Richard A. Paikoff	Signature January 27, 2005 Date
Name of Person Signing	Signature Date
	er sheet, attachments, and documents:
/ Mail documents to be recorded with	n required cover sheet information to:
40.00 DA Commissioner of Patents & Trademarks, Box Assignments Washington, D.C. 20231	

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ATTORNEY DOCKET NO.: 2003.1647CIP/1085.00324

ASSIGNMENT AND AGREEMENT

For value received, we, Kuei Shun Chen, Chin-Hsiang Lin, and Chih-Cheng Chiu hereby sell, assign and transfer to Talwan Semiconductor Manufacturing Co., Ltd., with its principal place of business located at No. 8, Li-Hsin Road 6, Science-Based Industrial Park, Hsin-Chu, Taiwan 300-77, Republic of China, and its successors, assigns and legal representatives, the entire right, title and interest, for the United States of America, in and to certain inventions related to UTILIZING COMPENSATION FEATURES IN PHOTOLITHOGRAPHY FOR SEMICONDUCTOR DEVICE FABRICATION described in an application for Letters Patent of the United States and all the rights and privileges in said applications and under any and all Letters Patent and any divisions, continuations, continuations-in-part, reexamination certificates, reissues, and extensions thereof that may be granted in the United States for said inventions; and we also concurrently hereby sell, assign and transfer to Taiwan Semiconductor Manufacturing Co., Ltd. the entire right, title and interest in and to said inventions for all countries foreign to the United States, including all rights of priority arising from the application aforesaid, and all the rights and privileges under any and all forms of protection, including Letters Patent, that may be granted in said countries foreign to the United States for said inventions.

We authorize Taiwan Semiconductor Manufacturing Co., Ltd. to make application for such protection in its own name and maintain such protection in any and all countries foreign to the United States, and to invoke and claim for any application for patent or other form of protection for said inventions, without further authorization from us, any and all benefits, including the right of priority provided by any and all treaties, conventions, or agreements.

We hereby consent that a copy of this assignment shall be deemed a full legal and formal equivalent of any document which may be required in any country in proof of the right of Taiwan Semiconductor Manufacturing Co., Ltd. to apply for patent or other form of protection for said inventions and to claim the aforesaid benefit of the right of priority.

We request that any and all patents for said inventions be issued to Taiwan Semiconductor Manufacturing Co., Ltd. in the United States and in all countries foreign to the United States, or to such nominees as Taiwan Semiconductor Manufacturing Co., Ltd. may designate.

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PATENT REEL: 016240 FRAME: 0715 EXPRESS MAIL LABEL NO. EV 516214824 US

ATTORNEY DOCKET NO.: 2003.1647CIP/1085.00324

We agree that, when requested, we shall, without charge to Taiwan Semiconductor Manufacturing Co., Ltd. but at its expense, sign all papers, and do all acts which may be necessary, desirable or convenient in connection with said applications, patents, or other forms of protection.

Inventor No. 1

Dated: //s /2005

Kuei Shun Clan
Kuei Shun Chen

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Inventor No. 2

Dated: 1/25/2005

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Inventor No. 3

Dated: //5 / >60 5

Chih-Cheng Chin

Residence:

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RECORDED: 01/27/2005

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