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A. Patent Application No.(s): 11/024,946

B. Patent No.(s):

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Stephen Bongini
FLEIT, KAIN, GIBBONS,
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6. Total number of applications and patents involved: 1

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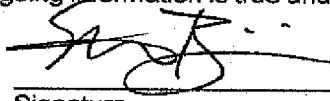
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(hereinafter ASSIGNEE) all right, title and interest for the United States, its territories and possessions in and to this invention relating to

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IMPROVED BOUNDARY SCAN TESTER FOR LOGIC DEVICES

as set forth in this United States Patent Application

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- executed concurrently herewith
- executed on _____
- Serial No. 11/024,946 Filed December 29, 2004

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NAME AND SIGNATURE OF INVENTOR(S)

Name: Rohit DUBEY

Signature: *Rohit Dubey*

Date: 17/03/2005

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