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Form PTO-1595 (Rev. 06/04) OMB No. 0651-0027 (exp. 6/30/2005)  102950 To the Director of the U.S. Patent and Trademark Office: Please	United States Patent and Trademark Office
Name of conveying party(ies)/Execution Date(s):  Chang-Ho Do and Jin-Seok Son	2. Name and address of receiving party(ies) Name: Hynix Semiconductor, Inc. Internal Address:
Execution Date(s) 11/29/04 and 12/3/04  Additional name(s) of conveying party(ies) attached? Yes No	Street Address: San 136-1, Ami-ri, Bubal-eub
3. Nature of conveyance:  Assignment Merger  Change of Name	City: Ichon-shi, Kyoungki-do
Security Agreement Change of Name Government Interest Assignment Executive Order 9424, Confirmatory License Other	State:  Country: Republic of Korea Zip: 467-860  Additional name(s) & address(es) attached? Yes No
4. Application or patent number(s):	s document is being filed together with a new application.  B. Patent No.(s)
Additional numbers att	ached? Yes No
5. Name and address to whom correspondence concerning document should be mailed:	6. Total number of applications and patents involved:
Name: Rodney C. LeRoy	7. Total fee (37 CFR 1.21(h) & 3.41) \$ 40.00
Internal Address:	Authorized to be charged by credit card
TOWNSEND AND TOWNSEND AND CREW LLP	Authorized to be charged to deposit account
Street Address: Two Embarcadero Center,	Enclosed
Eighth Floor	None required (government interest not affecting title)
City: San Francisco State: California Zip: 94111-3834	8. Payment Information  a. Credit Card Last 4 numbers  Expiration Date

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Total number of pages including cover sheet, attachments, and documents:

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February 16, 2005

Date

b. Deposit Account Number 20-1430

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## ASSIGNMENT OF PATENT APPLICATION

WHEREAS, DO, Chang-Ho and SON, Jin-Seok
of San 136-1, Ami-ri, Bubal-eub, Ichon-shi, Kyoungki-do 467-860, Republic of Korea
hereinafter referred to as "Assignor," is the inventor of the invention described and set forth in the below-
identified application for United States Letters Patent:
METHOD FOR MEASURING OFFSET VOLTAGE OF SENSE AMPLIFIER Title of Invention: AND SEMICONDUCTOR MEMROY DEVICE EMPLOYING THE METHOD
Date(s) of execution of Declaration: 11/29/04 and 12/3/04
Filing Date: February 16, 2005
Application No.:; and

WHEREAS, <u>Hynix Semiconductor, Inc.</u> of <u>San 136-1, Ami-ri, Bubal-eub, Ichon-shi, Kyoungki-do 467-860, Republic of Korea</u> hereinafter referred to as "ASSIGNEE," is desirous of acquiring an interest in the invention and application and in any U.S. Letters Patent and Registrations which may be granted on the same;

For good and valuable consideration, receipt of which is hereby acknowledged by Assignor, Assignor has assigned, and by these presents does assign to Assignee all right, title and interest in and to the invention and application and to all foreign counterparts (including patent, utility model and industrial designs), and in and to any Letters Patent and Registrations which may hereafter be granted on any patent application claiming priority from the same in the United States and all countries throughout the world, and to claim the priority from the application as provided by the Paris Convention. The right, title and interest is to be held and enjoyed by Assignee and Assignee's successors and assigns as fully and exclusively as it would have been held and enjoyed by Assignor had this Assignment not been made, for the full term of any Letters Patent and Registrations which may be granted thereon, or of any division, renewal, continuation in whole or in part, substitution, conversion, reissue, prolongation or extension thereof.

Assignor further agrees that Assignor will, without charge to Assignee, but at Assignee's expense, (a) cooperate with Assignee in the prosecution of U.S. Patent applications and foreign counterparts on the invention and any improvements, (b) execute, verify, acknowledge and deliver all such further papers, including applications and instruments of transfer, and (c) perform such other acts as Assignee lawfully may request to obtain or maintain Letters Patent and Registrations for the invention and improvements in any and all countries, and to vest title thereto in Assignee, or Assignee's successors and assigns.

PATENT RECORDED: 02/16/2005 REEL: 016299 FRAME: 0276