(Rev. 03/01)	26-2005 U.S. DEPARTMENT OF COMMERCE U.S. Patent and Trademark Office
OMB No. 0651-0027 (exp. 5/31/2002) Tab settings ⇔ ⇔ ⇒ ▼ ▼	A a a a a a a a a a a a a a a a a a a a
	988826 Jinal documents or copy thereof.
1. Name of conveying party(ies):	2. Name and address of receiving party(ies)
1. Yeou-Lang Hsieh	Name: Taiwan Semiconductor Manufacturing Co., Ltd.
<ol> <li>Ching-Kwun Huang</li> <li>Yi-Jing Chu</li> </ol>	Internal Address:
Additional name(s) of conveying party(ies) attached? Yes 🗸 No	
3. Nature of conveyance:	-
✓ Assignment Merger	
Security Agreement Change of Name	Street Address: No. 8, Li-Hsin Road 6
Other	Science Based Industrial Park
	<sub>City:</sub> Hsin-Chu State: Zip:
Execution Date: 03/30/2005	Country: Taiwan 300-77 R.O.C.
	Additional name(s) & address(es) attached? Yes 🖌 No
4. Application number(s) or patent number(s):	
	lication, the execution date of the application is: <u>3/30/2005</u> B. Patent No.(s)
A. Patent Application No.(s)	B. Fatent No.(S)
Additional numbers a	attached? Yes VNo
5. Name and address of party to whom correspondence	
concerning document should be mailed:	
Name:Steven E. Koffs, Esquire	7. Total fee (37 CFR 3.41)\$ <u>40.00</u>
Internal Address: Duane Morris LLP	Enclosed
	Authorized to be charged to deposit account
	8. Deposit account number:
Street Address: One Liberty Place	04-1679
City: Philadelphia State: PA zip: 19103	
	E THIS SPACE
9. Signature.	
Richard A. Paikoff	hark A. Parkoff 04/14/2005
Name of Person Signing	Signature Date
	ver sheet, attachments, and documents:

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PATENT				
<b>REEL:</b>	016483	FRAME:	0227	

## ASSIGNMENT AND AGREEMENT

For value received, we, Yeou-Lang Hsieh, Ching-Kwul Huang, and Yi-Jing Chu, hereby sell, assign and transfer to Taiwan Semiconductor Manufacturing Co., Ltd., with its principal place of business located at No. 8, Li-Hsin Road 6, Science-Based Industrial Park, Hsin-Chu, Taiwan 300-77, Republic of China, and its successors, assigns and legal representatives, the entire right, title and interest, for the United States of America, in and to certain inventions related to CAPACITOR AND INDUCTOR SCHEME WITH E-FUSE APPLICATION described in an application for Letters Patent of the United States and all the rights and privileges in said applications and under any and all Letters Patent and any divisions, continuations, continuations-in-part, reexamination certificates, reissues, and extensions thereof that may be granted in the United States for said inventions; and we also concurrently hereby sell, assign and transfer to Taiwan Semiconductor Manufacturing Co., Ltd. the entire right, title and interest in and to said inventions for all countries foreign to the United States, including all rights of priority arising from the application aforesaid, and all the rights and privileges under any and all forms of protection, including Letters Patent, that may be granted in said countries foreign to the United States for said inventions.

We authorize Taiwan Semiconductor Manufacturing Co., Ltd. to make application for such protection in its own name and maintain such protection in any and all countries foreign to the United States, and to invoke and claim for any application for patent or other form of protection for said inventions, without further authorization from us, any and all benefits, including the right of priority provided by any and all treaties, conventions, or agreements.

We hereby consent that a copy of this assignment shall be deemed a full legal and formal equivalent of any document which may be required in any country in proof of the right of Taiwan Semiconductor Manufacturing Co., Ltd. to apply for patent or other form of protection for said inventions and to claim the aforesaid benefit of the right of priority.

We request that any and all patents for said inventions be issued to Taiwan Semiconductor Manufacturing Co., Ltd. in the United States and in all countries foreign to the United States, or to such nominees as Taiwan Semiconductor Manufacturing Co., Ltd. may designate.

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We agree that, when requested, we shall, without charge to Taiwan Semiconductor Manufacturing Co., Ltd. but at its expense, sign all papers, and do all acts which may be necessary, desirable or convenient in connection with said applications, patents, or other forms of protection.

Inventor No. 1

Dated: 2004/3/30

Yeou-Long Hoseh

**Residence:** 

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Inventor No. 2

Dated: \_7005/3/30

Ching-Kwun Huang

Residence:

idence: 7F. No. 9-1, the la. 485. Sec-1 Kuang-fu Rd, Hsinchu, Taiwan.

Inventor No. 3

Dated: 2005/3/20

Yi-Jing	Chy
Yi-Jing Chu	

Residence:

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## **RECORDED: 04/14/2005**

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