

PATENT ASSIGNMENT

Electronic Version v1.1

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SUBMISSION TYPE:

NEW ASSIGNMENT

NATURE OF CONVEYANCE:

ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
TeraForce Technology Corporation	09/07/2005
DNA Computing Solutions, Inc.	09/07/2005

RECEIVING PARTY DATA

Name:	GE Fanuc Embedded Systems, Inc.
Street Address:	PO Box 8106
City:	Charlottesville
State/Country:	VIRGINIA
Postal Code:	22906

PROPERTY NUMBERS Total: 11

Property Type	Number
Patent Number:	6393530
Patent Number:	6456628
Patent Number:	6163829
Patent Number:	6279096
Patent Number:	6678801
Patent Number:	6370496
Patent Number:	6757761
Application Number:	10260749
Patent Number:	6898657
Application Number:	10341177
Application Number:	10198021

CORRESPONDENCE DATA

Fax Number: (212)310-8007

Correspondence will be sent via US Mail when the fax attempt is unsuccessful.

PATENT

500050341

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CH \$440.00 6393530

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NAME OF SUBMITTER:

Charan Sandhu

Total Attachments: 3

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PATENT ASSIGNMENT

This Patent Assignment is made and entered into this 7th day of September 2005 by and between TeraForce Technology Corporation, a Delaware corporation ("TeraForce"), and DNA Computing Solutions, Inc., a Delaware corporation, a wholly-owned subsidiary of TeraForce ("DNA" and together with TeraForce, "Assignor") and GE Fanuc Embedded Systems, Inc., a Delaware corporation ("Assignee").

WHEREAS, Assignor and Assignee are parties to that certain Asset Purchase Agreement dated August 1, 2005 (the "Asset Purchase Agreement"), pursuant to which Assignee agreed to purchase the Business (as defined in therein) from Assignor;

WHEREAS, Assignor is the owner of the patents and patent applications set forth on Schedule A hereto (the "Patents");

WHEREAS, Assignee desires to acquire all of Assignor's right, title and interest in and to the Patents and Assignor is willing to assign the Patents to Assignee; and

WHEREAS, the execution and delivery of this Agreement is a condition to Closing (as defined in the Asset Purchase Agreement).

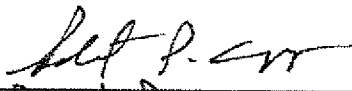
NOW THEREFORE, for the consideration stated in the Asset Purchase Agreement, and for other good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, the parties agree as follows:

1. Effective upon the Closing, Assignor hereby assigns to Assignee all of the right, title and interest in and to the Patents, including all rights therein provided by international conventions and treaties, the same to be held and enjoyed by Assignee for its own use and enjoyment and the use and enjoyment of its successors, assigns or other legal representatives to the end of the term or terms for which the Patents are granted or reissued or extended, as fully and entirely as the same would have been held and enjoyed by Assignor if this assignment and sale had not been made and to all income, royalties, damages and payments now or hereafter due or payable with respect thereto and to all causes of action (either in law or in equity) relating to, and the right to sue, counterclaim, and recover for, past, present and future infringement and misappropriation of the rights assigned or to be assigned under this Patent Assignment.
2. Assignor hereby covenants that Assignor shall, at the cost and expense of Assignee, take all actions and execute all documents necessary or desirable to perfect the interest of Assignee in and to the Patents, and shall not enter into any agreement in conflict with this Patent Assignment.
3. The parties hereto authorize and request the Commissioner of Patents and Trademarks of the United States to record Assignee as the assignee and owner of the Patents.
4. This Patent Assignment shall be governed by and construed in accordance with the laws of the State of New York without regard to the conflicts of law principles thereof.

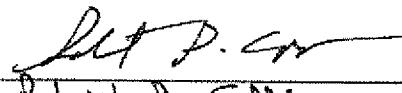
5. All capitalized terms not otherwise defined in this Patent Assignment, as used in this Patent Assignment, will have the respective meanings set forth in the Asset Purchase Agreement.

IN WITNESS WHEREOF, Assignor has caused this Patent Assignment to be executed as of the date first written above by its duly authorized officer.

TERAFORCE TECHNOLOGY CORPORATION (ASSIGNOR)

By: 
Name: Robert P. Capps
Title: Exec - Vice Pres.

DNA COMPUTING SOLUTIONS, INC. (ASSIGNOR)

By: 
Name: Robert P. Capps
Title: Exec - Vice Pres.

SCHEDULE A
United States Patents and Applications

- | | | |
|-----|-----------|---|
| 1. | 6,393,530 | Paging Method for DSP |
| 2. | 6,456,628 | DSP Interconnection Network |
| 3. | 6,163,829 | DSP Interrupt Control for Handling Multiple Interrupts |
| 4. | 6,279,096 | DSP Memory Logic Unit |
| 5. | 6,678,801 | DSP with Distributed RAM Structure |
| 6. | 6,370,496 | System for Intelligent Visualization & Control of Computer Hardware |
| 7. | 6,757,761 | Multi-processor Architecture for Parallel Signal & Image Processing Pending Signal Processing Resource for Selective Series Processing of Data in Transit on Comm. Paths in Multi-processor Arrangements- Smith |
| 8. | 10/260749 | Signal Processing Resource with Sample-by-Sample Selective Characteristics- Smith |
| 9. | 6,898,657 | Autonomous Signal Processing Resource for Selective Series Processing of Data in Transit on Comm. Paths in Multi-processor Arrangements |
| 10. | 10/341177 | Heterogeneous Design Process and Apparatus for Systems Employing Static Design Components and Programmable Gate Array Sub-Array Areas- Smith |
| 11. | 10/198021 | Signal Processing Resource for Selective Series Processing of Data in Transit on Comm. Paths in Multi-processor Arrangements- Smith |