

06-15-2005



G-9.05

FORM PTO-1595  
(Rev. 10/02)

RE

U.S. DEPARTMENT OF COMMERCE  
Patent and Trademark Office

103021628  
PATENTS ONLY

Docket No.: 067161-0270

To the Honorable Commissioner for Patents and Trademarks: Please record the attached original documents or copy thereto:

1. Name of Conveying Party(ies):

Tomoya KAWAGOE, Tsukasa OOISHI

Additional name(s) of conveying party(ies) attached?  Yes  No

2. Name and address of receiving party(ies):

Name: RENESAS TECHNOLOGY CORP.

Address: 4-1, Marunouchi 2-chome,  
Chiyoda-ku  
Tokyo 100-6334 JAPAN

Additional name(s) & address(es) attached?  Yes  No

3. Nature of Conveyance:

- Assignment  Merger
- Security Agreement  Change of Name
- Other

Execution Date: May 31, 2005

4. Application number(s) or patent number(s):

If the document is being filed together with a new application, the execution date of the application is: May 31, 2005

A. Patent Application No(s).

B. Patent No(s).

Additional numbers attached?  Yes  No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: MCDERMOTT WILL & EMERY LLP  
Internal Address:

Street Address: 600 13th Street, N.W.

City: Washington State: D. C. Zip: 20005-3096

6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 3.41) \$40.00

- Enclosed
- Authorized to be charged to deposit account

8. Deposit account number:  
500417

DO NOT USE THIS SPACE

9. Statement and signature.

*To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.*

Stephen A. Becker, 26,527

June 9, 2005

Name and Registration No. of Person Signing

Signature

Date

Total number of pages including cover sheet: 3

OMB No. 0651-0027 (exp. 6/30/2005)

113211 U.S. PTO  
11/148365  
060905

06/14/2005 DBYRNE 00000141 500417 11148365  
01 FC:8021 40.00 DA

**ASSIGNMENT**

WHEREAS, Tomoya KAWAGOE and Tsukasa OOISHI,

hereinafter called the "Assignors," have jointly invented a new and useful invention entitled Semiconductor Device Improving Error Correction Processing Rate

\_\_\_\_\_ , for which he has:

- (a) filed an application for United States Letters Patent on \_\_\_\_\_ as (Serial No. \_\_\_\_\_); or
- (b) executed an application for United States Letters Patent on May 31, 2005; or
- (c) filed a provisional application on \_\_\_\_\_ as (Serial No. \_\_\_\_\_); and

WHEREAS, Renesas Technology Corp., a corporation organized and existing under the laws of Japan, having a place of business at: 4-1 Marunouchi 2-chome Chiyoda-ku TOKYO 100-6334 JAPAN, hereinafter called the "Assignee," is desirous of acquiring the entire right, title and interest in and to said invention, the application above identified, and in, to and under any Letters Patent which may be obtained to said invention, as hereinafter more fully set forth;

NOW, THEREFORE, TO ALL WHOM IT MAY CONCERN, be it known that for and in consideration of the sum of One Dollar (\$1.00), and other valuable and legally sufficient considerations, the receipt of which by said Assignors from the said Assignee is hereby acknowledged, the said Assignors have sold, assigned and transferred, and by these presents do sell, assign and transfer unto the said Assignee, the entire, right, title and interest for the United States in and to the invention and application hereinabove identified, and any Letters Patent of the United States that may issue for said invention, together with the entire right, title and interest in and to said invention and applications for Letters Patent and Letters Patent therefor, in all countries foreign to the United States, including the full right to claim for any such application all benefits and priority rights under any applicable convention; to have and to hold for the sole and exclusive use and benefit of the said Assignee, its successors and assigns, to the full end of the term or terms for which any and all of said Letters Patent for said inventions may issue.