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PATENT REEL: 016711 FRAME: 0556 In consideration of value received, I, having a residence as stated below above my name, the sole inventor (if only one name is listed below) or a joint inventor (if plural inventors are listed below) of an invention described in an application for United States patent entitled:

SEMICONDUCTOR MEMORY DEVICE WITH MOS TRANSISTORS, EACH INCLUDING A FLOATING GATE AND A CONTROL GATE, AND A MEMORY CARD INCLUDING THE SAME

sell and assign to KABUSHIKI KAISHA TOSHIBA a Japanese corporation, having a business address at 1-1, Shibaura 1-chome, Minato-ku, Tokyo 105-8001, Japan

its successors, assigns or nominees, hereinafter referred to as "Assignee", my entire right, title and interest in and to said invention as disclosed, shown and described in said United States patent application:

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and in and to all applications for patent and patents for invention including all divisions, reissues, continuations, substitutes and extensions thereof and all rights arising under or pursuant to any and all international agreements, treaties or laws relating to the protection of industrial property, including rights of priority, resulting from the filing of any of said applications; and I authorize and request any official whose duty is to issue patents, to issue any patent on said invention or resulting therefrom to said Assignee, and I agree that on request and without further consideration, but at the expense of said Assignee, I will communicate to said Assignee or its representatives all facts known to me respecting said invention and testify in any legal proceedings, sign all lawful papers, execute all divisional, continuing, continued prosecution, reissue, or other applications, make all rightful oaths and declarations, and generally do everything possible to aid said Assignee to obtain and enforce proper patent protection for said invention.

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PATENT REEL: 016711 FRAME: 0557

ASSIGNMENT

ACG038485-USA-A (04S0388-1)

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May. 16, 2005

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