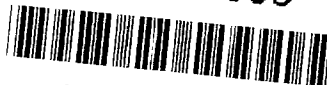


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Form PTO-1595 (Rev. 03/05)
OMB No. 0651-0027 (exp. 6/30/2005)

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Jonathan W. Byrn
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Execution Date(s) 09 June 2005 and 15 June 2005

- ☒ Assignment ☐ Merger
☐ Security Agreement ☐ Change of Name
☐ Joint Research Agreement
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2. Name and address of receiving party(ies)

Name: LSI Logic Corporation

Internal Address: Intellectual Property Law

Mail Stop D-106

Street Address: 1551 McCarthy Boulevard

City: Milpitas

State: California

Country: USA Zip: 95035

Additional name(s) & address(es) attached? ☐ Yes ☐ No

4. Application or patent number(s):

☐ This document is being filed together with a new application.

A. Patent Application No.(s)

11/151,043 entitled Method and Apparatus for the Automatic Generation of Correct Minimal Clocking Constraints for a Semiconductor Product, LSIL Docket No. 04-1314

B. Patent No.(s)

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REEL: 016737 FRAME: 0374

A S S I G N M E N T

Whereas, we

(1) Jonathan W. Byrn,
County of Dodge,City of Kasson,
and State of Minnesota,(2) Matthew S. Wingren,
County of Olmsted,City of Rochester,
and State of Minnesota,

have invented certain improvements in

Method and Apparatus for the Automatic Generation of
Correct Minimal Clocking Constraints for a Semiconductor Product

and have executed, respectively, a United States patent application therefor on

Dates Inventors (1) 09 June 2005 , (2) 09 June 2005
Signed Declaration

And whereas, LSI LOGIC CORPORATION, a corporation of Delaware, having a place of business at Milpitas, California (hereinafter called LSI Logic) desires to acquire the entire right, title and interest in the said application and invention, and to any United States and foreign patents to be obtained therefor;

Now therefore, for a valuable consideration, receipt whereof is hereby acknowledged, we the above named hereby sell, assign, and transfer to LSI Logic, its successors and assigns, the entire right, title and interest in the said application and invention therein disclosed for the United States and foreign countries, and all rights of priority resulting from the filing of said United States application, and we request the Commissioner of Patents to issue any Letters Patent granted upon the invention set forth in said application to LSI Logic, its successors and assigns; and we hereby agree that LSI Logic may apply for foreign Letters Patent on said invention and we will execute all papers necessary in connection with the United States and foreign applications when called upon to do so by LSI Logic.

Signed

Inventor at Rochester, MN

(1)

on 6/9/05

(Jonathan W. Byrn)

Inventor's Name

Inventor
Signature

Inventor at Rochester, MN

(2)

on 6/15/2005

(Matthew S. Wingren)

Inventor's Name

Inventor
Signature