

FORM PTO-1595 (modified)

(Rev 8-93)

RECORDATION FORM COVER SHEET PATENTS ONLY

U.S. DEPARTMENT OF COMMERCE

Patent and Trademark Office

To the Director of the United States Patent and Trademark Office: Please record the attached original documents or copies thereof.

1. Name of conveying party(ies):

**Semiconductor Leading Edge
Technologies, Inc.**

2. Name and address of receiving party(ies):

**Renesas Technology Corp.
2-4-1, Marunouchi, Chiyoda-ku,
Tokyo 100-6334, Japan**

Additional conveying party(ies) **NO**

3. Nature of conveyance:

**REQUEST TO RECORD PREVIOUSLY SUBMITTED NON-
RECORDABLE ASSIGNMENT, DOC ID # 700193395**

Execution Date:

June 1, 2005

Additional name(s) & address(es) attached? **NO**

4. Application number(s) or patent number(s):

If this is being filed together with a new application, the execution date of the application is:

A. Patent Application Number(s):

**10/050,161
10/191,802
10/676,082
11/078,728**

B. Patent Number(s):

**6,363,382
6,473,996
6,731,375
6,803,170
6,817,822**

Additional numbers attached? **NO**

5. Name and address of party to whom correspondence concerning document should be FAXED:

**FAX: 202-672-5399
William T. Ellis
FOLEY & LARDNER LLP
Washington Harbour
3000 K Street, N.W., Suite 500
Washington, D.C. 20007-5143**

6. Total number of applications/patents involved: **2**

7. Total fee (37 C.F.R. § 3.41): **\$360.00**

Check Enclosed

Charge to deposit account

8. Deposit account number: **19-0741**

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9. Statement and signature:

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document. The Commissioner is hereby authorized to charge any additional recordation fees which may be required in this matter to the above-identified deposit account.

William T. Ellis

September 23, 2005

Name of person signing

Signature

Date

Total number of pages including cover sheet, attachments, and document: **3**

CH \$360.00 190741 10050161

Atty. Dkt. No. 025311-0123

PATENT ASSIGNMENT

WHEREAS Semiconductor Leading Edge Technologies, Inc. of 16-1 Onogawa, Tsukuba-shi, Ibaraki, 305-8569, Japan (hereinafter "Assignor") is the owner of the following United States Letters Patent(s), and the invention(s) set forth therein, as shown by the records of the United States Patent and Trademark Office:

U.S. PATENT APPLICATION NO or U.S. PATENT NO.	FILING or GRANT DATE
6,363,382	March 26, 2002
6,473,996	November 5, 2002
6,731,375	May 4, 2004
6,803,170	October 12, 2004
6,817,822	November 16, 2004
10/050,161	January 18, 2002
10/191,802	July 10, 2002
10/676,082	October 2, 2003
11/078,728	March 14, 2005

WHEREAS Renesas Technology Corp. of 2-4-1, Marunouchi, Chiyoda-ku, Tokyo 100-6334, Japan, (hereinafter referred to singly and collectively as "Assignee") is desirous of acquiring Assignor's entire right, title and interest in and to said inventions for the United States of America, and in and to said United States Patent Applications;

NOW, THEREFORE, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged by Assignor, Assignor has sold, assigned, transferred and set over, and by these presence hereby sells, assigns, transfers and sets over to Assignee all of its right, title and interest in and to said inventions for the United States of America, and in and to said patents and applications for Letters Patent listed above, including any and all divisional, continuation, continuation-in-part, reissues or extensions thereof, to be held and enjoyed by Assignee for its own use as fully and entirely as the same would have been held and enjoyed by Assignor had this assignment not been made. The Commissioner of Patents and Trademarks of the United States of America is hereby authorized to transfer the portion of the title indicated to said applications to said Assignee in accordance herewith; this assignment being under covenant, not only that full power to make the same is had by the Assignor, but Assignor hereby undertakes to execute and deliver to Assignee upon request all lawful documents which may be requested by Assignee, and to furnish Assignee with all facts relating to said invention as may be requested.

Atty. Dkt. No. 025311-0123

The undersigned hereby grant the firm of FOLEY & LARDNER LLP, the power to insert in this Assignment any further identification which may be necessary or desirable to comply with the rules of the U.S. Patent and Trademark Office for recordation of this Assignment.

Semiconductor Leading Edge Technologies, Inc.

Date: June 1, 2005

By: Hiroimi Sakurai

Name: Hiroimi SAKURAI
Title: Director, General Manager
Research Planning Promotion Dept.

Affirmed and Agreed to:

Renesas Technology Corp.

Date: June 16, 2005

By: Satoru Ito

Name: Satoru Ito
Title: President & CEO