PATENT ASSIGNMENT

Electronic Version v1.1
Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	Assignment of Rights Under Filings in the United States Patent and Trademark Office

CONVEYING PARTY DATA

Name	Execution Date
Enable Capital Management, LLC	12/01/2005

RECEIVING PARTY DATA

Name:	Enable Growth Partners, LP
Street Address:	One Sansome
Internal Address:	c/o Enable Capital Management, LLC, Ste 2900
City:	San Francisco
State/Country:	CALIFORNIA
Postal Code:	94104

PROPERTY NUMBERS Total: 48

Property Type	Number
Patent Number:	5909153
Patent Number:	5974089
Patent Number:	6107844
Patent Number:	6127893
Patent Number:	6169681
Patent Number:	6229390
Patent Number:	6246283
Patent Number:	6281747
Patent Number:	6297697
Patent Number:	6316992
Patent Number:	6329876
Patent Number:	6348836
Patent Number:	6351184

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Patent Number:	6362679
Patent Number:	6362683
Patent Number:	6411165
Patent Number:	6414560
Patent Number:	6515604
Patent Number:	6518849
Patent Number:	6549069
Patent Number:	6566946
Patent Number:	6577189
Patent Number:	6577194
Patent Number:	6580322
Patent Number:	6603355
Patent Number:	6617642
Patent Number:	6621339
Patent Number:	6628166
Patent Number:	6630899
Patent Number:	6693491
Patent Number:	6724248
Patent Number:	6737713
Patent Number:	6781458
Patent Number:	6785392
Patent Number:	6798288
Patent Number:	6940703
Application Number:	10084580
Application Number:	10807903
Application Number:	10900500
Application Number:	10990287
Application Number:	10990288
Application Number:	11000215
Application Number:	11004396
Application Number:	60681062
Application Number:	60683123
PCT Number:	US0438358
PCT Number:	US0325153
PCT Number:	US0438359

CORRESPONDENCE DATA

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Correspondence will be sent via US Mail when the fax attempt is unsuccessful.	
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NAME OF SUBMITTER:	Christine Wilson

Total Attachments: 12
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ASSIGNMENT OF RIGHTS UNDER FILINGS IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

This Assignment of Rights on Filings in the United States Patent and Trademark Office (the "Assignment") dated this 1st day of December 2005, is by and between Enable Capital Management, LLC (the "Assignor") and Enable Growth Partners, LP as Agent (the "Assignee");

WHEREAS, Assignor is listed as the assignee of the patents of Tripath Technology Inc. in a filing with the United States Patent and Trademark Office made on November 17, 2005 as evidenced on Reel 016793 and Frame 0178 (the "Patent Filing");

WHEREAS, Assignor is listed as the assignee of the trademarks of Tripath Technology Inc. in a filing with the United States Patent and Trademark Office made on November 17, 2005 as evidenced on Reel 003196 and Frame 0326 (the "Trademark Filing");

WHEREAS, Assignor wishes to assign its rights and obligations under the Patent Filing and Trademark Filing to Assignee;

WHEREAS, Assignee wishes to accept the assignment of Assignor's rights and obligations under the Patent Filing and Trademark Filing;

NOW, THEREFORE, for good and adequate consideration, the receipt and sufficiency of which is hereby acknowledged, Assignor hereby assigns to Assignee all rights and obligations as Assignor may possess under the Patent Filing and Trademark Filing and Assignee hereby accepts such assignment.

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Dated: December 1, 2005

ENABLE CAPITAL MANAGEMENT, LLC

Name: Mitch Levine
Title: Managing Member

ENABLE GROWTH PARTNERS, LP AS AGENT

Name: Adam Epstein
Title: Principal

Schedule F to Security Agreement

TRIPATH TECHNOLOGY INC. PATENTS

Title	Serial No./ Patent No.	Filing Date/ Issue Date
METHOD AND APPARATUS FOR PERFORMANCE IMPROVEMENT BY QUALIFYING PULSES IN AN OVERSAMPLED NOISE-SHAPING SIGNAL PROCESSOR	08/898,544 5,974,089	7/22/97 10/26/99
METHOD AND APPARATUS FOR PERFORMANCE IMPROVEMENT BY QUALIFYING PULSES IN AN OVERSAMPLED NOISE-SHAPING SIGNAL PROCESSOR	98937027.5	7/20/98
METHOD AND APPARATUS FOR PERFORMANCE IMPROVEMENT BY QUALIFYING PULSES IN AN OVERSAMPLED NOISE-SHAPING SIGNAL PROCESSOR	2000-504669	7/20/98
METHOD AND APPARATUS FOR PERFORMANCE IMPROVEMENT BY QUALIFYING PULSES IN AN OVERSAMPLED NOISE-SHAPING SIGNAL PROCESSOR	200000340-0 70509	7/20/98 2/7/02
METHOD AND APPARATUS FOR COMPENSATING FOR DELAYS IN MODULATOR LOOPS	09/019,217 5,909,153	2/5/98 6/1/99
METHOD AND APPARATUS FOR COMPENSATING FOR DELAYS IN MODULATOR LOOPS	88107176 135600	4/30/99 10/26/01
METHOD AND APPARATUS FOR COMPENSATING FOR DELAYS IN MODULATOR LOOPS	99905754.0	2/5/99
METHOD AND APPARATUS FOR COMPENSATING FOR DELAYS IN MODULATOR LOOPS	2000-530980	2/5/99

TRIPATH TECHNOLOGY INC. PATENTS

Title	Serial No./ Patent No.	Filing Date/ Issue Date
METHOD AND APPARATUS FOR COMPENSATING FOR DELAYS IN MODULATOR LOOPS	2000-04232-5 74974	2/5/99 8/16/02
METHODS AND APPARATUS FOR REDUCING MOSFET BODY DIODE CONDUCTION IN A HALF-BRIDGE CONFIGURATION	09/162,243 6,107,844	9/28/99 8/22/00
METHODS AND APPARATUS FOR REDUCING MOSFET BODY DIODE CONDUCTION IN A HALF-BRIDGE CONFIGURATION	88116635 129936	9/28/99 8/1/01
METHODS AND APPARATUS FOR REDUCING MOSFET BODY DIODE CONDUCTION IN A HALF-BRIDGE CONFIGURATION	99949858.7	9/24/99
METHODS AND APPARATUS FOR REDUCING MOSFET BODY DIODE CONDUCTION IN A HALF-BRIDGE CONFIGURATION	2000-572990	9/24/99
METHOD AND APPARATUS FOR CONTROLLING AN AUDIO SIGNAL LEVEL	09/156,262 6,127,893	9/18/99 10/3/00
METHOD AND APPARATUS FOR CONTROLLING AN AUDIO SIGNAL LEVEL	88116025 130682	9/16/99 8/16/01
METHOD AND APPARATUS FOR CONTROLLING AN AUDIO SIGNAL LEVEL	99948313.4	9/17/99
METHOD AND APPARATUS FOR CONTROLLING AN AUDIO SIGNAL LEVEL	2000-571560	9/17/99
POWER EFFICIENT LINE DRIVER	09/432,507 6,246,283	11/2/99 6/12/01

TRIPATH TECHNOLOGY INC. PATENTS

Title	Serial No./ Patent No.	Filing Date/ Issue Date
POWER EFFICIENT LINE DRIVER	09/769,234 6,281,747	1/24/01 8/28/01
POWER EFFICIENT LINE DRIVER	89104095	3/7/00
METHODS AND APPARATUS FOR NOISE SHAPING A MIXED SIGNAL POWER OUTPUT	09/432,296 6,229,390	11/2/99 5/8/01
METHODS AND APPARATUS FOR NOISE SHAPING A MIXED SIGNAL POWER OUTPUT	09/759,005 6,297,697	1/11/01 10/2/01
METHODS AND APPARATUS FOR NOISE SHAPING A MIXED SIGNAL POWER OUTPUT	89104092 148839	3/7/2000 5/8/02
METHODS AND APPARATUS FOR NOISE SHAPING A MIXED SIGNAL POWER OUTPUT	00915985.6	3/1/00
METHODS AND APPARATUS FOR NOISE SHAPING A MIXED SIGNAL POWER OUTPUT	2000-604520	3/1/00
NOISE REDUCTION SCHEME FOR OPERATIONAL AMPLIFIERS	09/406,319 6,329,876	9/27/99 12/11/01
NOISE REDUCTION SCHEME FOR OPERATIONAL AMPLIFIERS	99965342.1	12/28/99
NOISE REDUCTION SCHEME FOR OPERATIONAL AMPLIFIERS	2000-592932	12/28/99
NOISE REDUCTION SCHEME FOR OPERATIONAL AMPLIFIERS	88123145 148300	12/28/99 5/1/02
NOISE REDUCTION SCHEME FOR OPERATIONAL AMPLIFIERS	09/908,862 6,566,946	7/18/01 5/20/03

TRIPATH TECHNOLOGY INC. PATENTS

Title	Serial No./ Patent No.	Filing Date/ Issue Date
POWER SUPPLY TOPOLOGY TO REDUCE THE EFFECTS OF SUPPLY PUMPING	09/407,004 6,169,681	9/28/99 1/2/01
POWER SUPPLY TOPOLOGY TO REDUCE THE EFFECTS OF SUPPLY PUMPING	89104095 146798	3/2/00 4/10/02
DC OFFSET SELF-CALIBRATION SYSTEM FOR A DIGITAL SWITCHING POWER AMPLIFIER	09/624,503 6,316,992	7/24/00 11/13/01
DC OFFSET SELF-CALIBRATION SYSTEM FOR A DIGITAL SWITCHING POWER AMPLIFIER	89115266 191538	7/29/00 3/24/04
DC OFFSET SELF-CALIBRATION SYSTEM FOR A DIGITAL SWITCHING POWER AMPLIFIER	00948943.6	7/25/00
DC OFFSET SELF-CALIBRATION SYSTEM FOR A DIGITAL SWITCHING POWER AMPLIFIER	2001-514530	7/25/00
BREAK-BEFORE-MAKE DISTORTION COMPENSATION SYSTEM FOR THE DIGITAL POWER AMPLIFIER	09/624,521 6,362,683	7/24/00 3/26/02
BREAK-BEFORE-MAKE DISTORTION COMPENSATION SYSTEM FOR THE DIGITAL POWER AMPLIFIER	89115265 148952	7/29/00 5/9/02
BREAK-BEFORE-MAKE DISTORTION COMPENSATION SYSTEM FOR THE DIGITAL POWER AMPLIFIER	2001-514535	7/26/00
DYNAMIC SWITCHING FREQUENCY CONTROL METHOD FOR A DIGITAL SWITCHING POWER AMPLIFIER	09/624,506 6,351,184	7/24/00 2/26/02

TRIPATH TECHNOLOGY INC. PATENTS

Title	Serial No./ Patent No.	Filing Date/ Issue Date
DYNAMIC SWITCHING FREQUENCY CONTROL METHOD FOR A DIGITAL SWITCHING POWER AMPLIFIER	2001-514534	7/26/00
DYNAMIC SWITCHING FREQUENCY CONTROL METHOD FOR A DIGITAL SWITCHING POWER AMPLIFIER	89115264	7/25/00
DYNAMIC SWITCHING FREQUENCY CONTROL METHOD FOR A DIGITAL SWITCHING POWER AMPLIFIER	10/057,790 6,580,322	1/24/02 6/17/03
OVERVOLTAGE PROTECTION CIRCUIT	09/738,267 **6,940,703**	12/15/00 **9/6/05**
IMPROVED POWER FET DRIVER CIRCUIT	09/765,833 6,362,679	1/19/01 3/26/02
IMPROVED POWER FET DRIVER CIRCUIT	90104210 173026	2/23/01 7/2/03
METHOD AND CIRCUIT TO OBTAIN HIGH FREQUENCY SWITCHING POWER FET STAGE FOR INDUCTIVE LOADS	09/690,926 6,617,642	10/17/00 9/9/03
METHOD AND CIRCUIT TO OBTAIN HIGH FREQUENCY SWITCHING POWER FET STAGE FOR INDUCTIVE LOADS	90103276 168866	2/23/01 4/23/03
RF COMMUNICATION SYSTEM USING AN RF DIGITAL AMPLIFIER	09/796,735 6,628,166	2/28/01 9/30/03
RF COMMUNICATION SYSTEM USING AN RF DIGITAL AMPLIFIER	90105026 163502	3/5/01 2/7/03
RESONANT GATE DRIVE TECHNIQUE FOR A DIGITAL POWER AMPLIFIER	09/796,734 6,577,194	2/28/01 6/10/03
SELF-TIMED SWITCHING FOR A DIGITAL POWER AMPLIFIER	09/796,731 6,549,069	2/28/01 4/15/03

TRIPATH TECHNOLOGY INC. PATENTS

Title	Serial No./ Patent No.	Filing Date/ Issue Date
DUAL INDEPENDENTLY CLOCKED ANALOG-TO-DIGITAL CONVERSION FOR A DIGITAL POWER AMPLIFIER	09/796,845 6,348,836	2/28/01 2/19/02
LOOP DELAY COMPENSATION FOR AN RF DIGITAL POWER AMPLIFIER	09/796,634 6,414,560	2/28/01 7/2/02
DYNAMICALLY DELAY COMPENSATION VERSUS AVERAGE SWITCHING FREQUENCY IN A MODULAR LOOP	09/836,108 6,518,849	4/16/01 2/11/03
ACTIVE COMMON MODE FEEDBACK	09/836,623 6,411,165	4/16/01 6/25/02
ACTIVE COMMON MODE FEEDBACK	10/137,105 6,603,355	5/1/02 8/5/03
METHOD AND APPARATUS FOR CONTROLLING AN AUDIO SIGNAL LEVEL	09/836,154 6,693,491	4/16/01 2/17/04
DIGITAL SIGNAL PROCESSING UNIT WITH IMPROVED DISTORTION AND NOISE	09/836,622 6,515,654	4/16/01 2/4/03
A MUTE-IN-SILENCE SCHEME FOR AUDIO AMPLIFIERS	09/759,044 6,785,392	1/11/01 8/31/04
METHODS AND APPARATUS FOR ADAPTIVE EQUALIZATION	10/084,580	2/27/02
SCHEME FOR REDUCING TRANSMIT-BAND NOISE FLOOR AND ADJACENT CHANNEL POWER WITH POWER BACKOFF	09/908,967 6,577,189	7/18/01 6/10/03
SCHEME FOR MAXIMIZING EFFICIENCY OF POWER AMPLIFIER UNDER POWER BACKOFF CONDITIONS	09/908,879 6,630,899	7/18/01 10/7/03
METHOD FOR OPTIMAL OPERATION OF LOOP STRUCTURE OF CLASS-T AMPLIFIERS FOR FDD SYSTEMS	09/963,874 6,798,288	9/25/01 9/28/04

TRIPATH TECHNOLOGY INC. PATENTS

Title	Serial No./ Patent No.	Filing Date/ Issue Date
AN IMPROVED DC OFFSET SELF-CALIBRATION SYSTEM FOR A DIGITAL SWITCHING AMPLIFIER	10/127,357 6,724,248	4/19/02 4/20/04
AN IMPROVED DC OFFSET SELF-CALIBRATION SYSTEM FOR A DIGITAL SWITCHING AMPLIFIER	2002-584470	4/19/02
SUBSTRATE CONNECTION IN INTEGRATED POWER CIRCUIT	10/189,284 6,737,713	7/2/02 5/18/04
METHODS AND APPARATUS FOR FACILITATING NEGATIVE FEEDBACK, PROVIDING LOOP STABILITY, AND IMPROVING AMPLIFIER EFFICIENCY	10/107,524 6,621,339	3/26/02 9/16/03
PROVIDING DC ISOLATION IN SWITCHING AMPLIFIERS	10/454,789 6,781,458	6/3/03 8/24/04
PROVIDING DC ISOLATION IN SWITCHING AMPLIFIERS	PCT/US03/ 25153	8/12/03
PROVIDING DC ISOLATION IN SWITCHING AMPLIFIERS	03824347.4	8/12/03
A DC OFFSET SELF-CALIBRATION SYSTEM FOR A SWITCHING POWER AMPLIFIER	10/807,903	3/24/04
DIGITAL-TO-ANALOG CONVERTER WITH LEVEL CONTROL	10/900,500	7/28/04
INDUCTOR-BASED CURRENT SENSING	10/990,287	11/15/04
INDUCTOR-BASED CURRENT SENSING	PCT/US2004/ 038358	11/16/04
OVERCURRENT PROTECTION IN AMPLIFIER TOPOLOGIES EMPLOYING DC ISOLATION	10/990,288	11/15/04

TRIPATH TECHNOLOGY INC. PATENTS

Title	Serial No./ Patent No.	Filing Date/ Issue Date
OVERCURRENT PROTECTION IN AMPLIFIER TOPOLOGIES EMPLOYING DC ISOLATION	PCT/US04/38 359	11/16/04
OFFSET CANCELLATION IN A SWITCHING AMPLIFIER	11/000,215	11/29/04
INDUCTORLESS ARCHITECTURE FOR A SWITCHING AMPLIFIER	11/004,396	12/2/04
MODIFIED SIGMA-DELTA ARCHITECTURE WITH FREQUENCY LOCK	60/681,062	5/12/05
WIRELESS TRANSMITTER FRONT-END TOPOLOGY EMPLOYING AUXILIARY TRANSMIT-PATH FOR LOWER-POWERED SIGNALS TO ENHANCE RF POWER AMPLIFIER EFFICIENCY	60/683,123	5/17/05

Schedule F to Security Agreement

TRIPATH TECHNOLOGY, INC.
TRADEMARKS

COUNTRY	MARK	APPL. NO.	FILING DATE	REG. NO.	REG. DATE
Canada	COMBINANT DIGITAL	1,006,798	25-Feb-1999		
Canada	DESIGN (T)	1,006,800	25-Feb-1999		
Canada	TRIPATH	1,006,801	25-Feb-1999		
China	COMBINANT	9900019747	01-Mar-1999	1513827	28-Jan-2001
China	COMBINANT	9900019749	01-Mar-1999	1445662	14-Sep-2000
China	DESIGN (T)	9900019748	01-Mar-1999	1505735	14-Jan-2001
China	DESIGN (T)	9900019750	01-Mar-1999	1436813	21-Aug-2000
China	TRIPATH	2000132579	29-Aug-2000	1654287	21-Oct-2001
China	TRIPATH TECHNOLOGY	9900020211	03-Mar-1999		
China	TRIPATH TECHNOLOGY	9900020210	03-Mar-1999		
China	TRIPATH TECHNOLOGY	9900120569	12-Oct-1999	1538082	14-Mar-2001
European Union	TRIPATH	1812890	17-Aug-2000	1812890	17-Dec-2001
European Union	TRIPATH TECHNOLOGY	1089028	26-Feb-1999	1089028	06-Jun-2000
Europen Union	COMBINANT DIGITAL	1089366	26-Feb-1999	1089366	26-Feb-1999
Europen Union	DESIGN (T)	1089663	26-Feb-1999	001089663	21-Dec-2001
Hong Kong	COMBINANT DIGITAL	2313/1999	26-Feb-1999	2011/2001	31-Aug-1998
Hong Kong	COMBINANT DIGITAL	2314/1999	26-Feb-1999		
Hong Kong	DESIGN (T)	2317/1999	26-Feb-1999	200016783	31-Aug-1998
Hong Kong	DESIGN (T)	2318/1999	26-Feb-1999	4304/2002	31-Aug-1998
Hong Kong	TRIPATH	2315/1999	26-Feb-1999		
Hong Kong	TRIPATH	2316/1999	26-Feb-1999		
Japan	COMBINANT DIGITAL	017318/1999	25-Feb-1999	4426096	20-Oct-2000
Japan	DESIGN (T)	017317/1999	25-Feb-1999	4426095	20-Oct-2000
Japan	TRIPATH	90580/2000	17-Aug-2000	4532845	28-Dec-2001
Japan	TRIPATH TECHNOLOGY	017316/1999	25-Feb-1999	4434289	24-Nov-2000
Korea	COMBINANT DIGITAL	1999-409	25-Feb-1999	1293	02-Jun-2000
Korea	DESIGN (T)	1999-408	25-Feb-1999	1292	02-Jun-2000
Korea	TRIPATH TECHNOLOGY	1999-410	25-Feb-1999	1176	01-May-2000

TRIPATH TECHNOLOGY, INC.
TRADEMARKS

Singapore	COMBINANT DIGITAL	1774/99	25-Feb-1999		
Singapore	COMBINANT DIGITAL	1775/99	25-Feb-1999	T99/01775D	31-Aug-1998
Singapore	DESIGN (T)	1772/99	25-Feb-1999	T99/01772Z	31-Aug-1998
Singapore	DESIGN (T)	1773/99	25-Feb-1999	T99/01773H	31-Aug-1998
Singapore	TRIPATH	T00/08697Z	24-May-2000	T00/08697Z	06-Dec-1999
Singapore	TRIPATH TECHNOLOGY	1776/99	25-Feb-1999		
Singapore	TRIPATH TECHNOLOGY	1777/99	25-Feb-1999		
Taiwan	COMBINANT DIGITAL	88008013	26-Feb-1999		16-Jun-2000
Taiwan	COMBINANT DIGITAL	88008012	26-Feb-1999	00133792	01-Dec-2000
Taiwan	DESIGN (T)	88008011	26-Feb-1999	00903068	01-Sep-2000
Taiwan	DESIGN (T)	88008010	26-Feb-1999	00130297	01-Oct-2000
Taiwan	TRIPATH	89048652	21-Aug-2000	984786	16-Feb-2002
Taiwan	TRIPATH TECHNOLOGY	88008014	26-Feb-1999	00915918	01-Dec-2000
Taiwan	TRIPATH TECHNOLOGY	88008015	26-Feb-1999	00127054	01-Aug-2000
U.S.	CLASS-T	76/073,920	20-Jun-2000	2,809,670	03-Feb-2004
U.S.	COMBINANT DIGITAL	75/545,470	31-Aug-1998		
U.S.	DESIGN (T)	75/545,868	31-Aug-1998		
U.S.	DIGITAL POWER PROCESSING	75/586,992	12-Nov-1998	2,526,206	01-Jan-2002
U.S.	DPP	75/587,539	12-Nov-1998	2,453,669	22-May-2001
U.S.	TIO	76/096,294	24-Jul-2000		
U.S.	TIO AND DESIGN	76/096,234	24-Jul-2000		
U.S.	T-PATH				
U.S.	TRIPATH	75/866,037	06-Dec-1999	2,398,029	24-Oct-2000
U.S.	TRIPATH AND DESIGN (T)	76/157,810	31-Oct-2000	2,685,346	11-Feb-2003
U.S.	TRIPATH TECHNOLOGY	75/501,525	12-Jun-1998		