# PATENT ASSIGNMENT

Electronic Version v1.1 Stylesheet Version v1.1

| SUBMISSION TYPE:      | NEW ASSIGNMENT |
|-----------------------|----------------|
| NATURE OF CONVEYANCE: | ASSIGNMENT     |

# **CONVEYING PARTY DATA**

| Name              | Execution Date |
|-------------------|----------------|
| Rajiv K. Mongia   | 12/20/2005     |
| Himanshu Pokharna | 12/24/2005     |
| Eric DiStefano    | 12/20/2005     |

### **RECEIVING PARTY DATA**

| Name:           | Intel Corporation          |
|-----------------|----------------------------|
| Street Address: | 2200 Mission College Blvd. |
| City:           | Santa Clara                |
| State/Country:  | CALIFORNIA                 |
| Postal Code:    | 95052                      |

#### PROPERTY NUMBERS Total: 1

| Property Type       | Number   |
|---------------------|----------|
| Application Number: | 11239957 |

#### **CORRESPONDENCE DATA**

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| ATTORNEY DOCKET NUMBER: | P21672/I06.412  |
|-------------------------|-----------------|
| NAME OF SUBMITTER:      | Nathaniel Levin |

**Total Attachments: 3** 

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PATENT 500068321 **REEL: 016957 FRAME: 0485**  source=P21672 - Signed Assignment for Mongia\_Pokharna\_DiStefano#page3.tif

PATENT REEL: 016957 FRAME: 0486

P.08

Attorney Docket No.: P21672
(BMT Ref. No.: 106.412)

# ASSIGNMENT OF RIGHTS, TITLE AND INTEREST IN INVENTION

| Th          | is is an Assignment of the followi   | ng rights, title and interest: | (check all that apply) |
|-------------|--------------------------------------|--------------------------------|------------------------|
| $\boxtimes$ | United States of America rights,     | title and interest in the inve | ntion                  |
| $\boxtimes$ | Foreign rights, title and interest i | in the invention               |                        |
| $\boxtimes$ | United States Patent Application     | Serial No.: 11/239,957         |                        |
|             | Date of Execution:                   | Date of Filing: September      | 30, 2005               |
|             | United States Provisional Patent     | Application No.:               |                        |
|             | United States Patent No(s).:         |                                |                        |
|             | International (PCT) Patent Appli     | ication Serial No.:            |                        |
|             | Other (specify)::                    |                                |                        |
|             |                                      |                                |                        |
| TITLE OF    | THE INVENTION:                       |                                |                        |
|             | OOLANT MICROCHANNEL A                | ASSEMBLY WITH INTE             | GRATED                 |

# **Inventors** (assignors)

Name

Address

Rajiv K. Mongia

3532 Langdon Common, Fremont, CA 94538

Himanshu Pokharna

439 Camille Circle #13, San Jose, CA 95134

Eric DiStefano

1535 Frankfurt Way, Livermore, CA 94550

# Assignee

Name

Address

Intel Corporation

2200 Mission College Blvd., Santa Clara, CA 95052 US

Whereas, we, the above-identified Inventors, have invented certain new and useful improvements in the Invention identified above and described in the above-identified patent application(s) and/or patent(s) (hereinafter referred to as "Invention");

Attorney Docket No.: P21672 (BMT Ref. No.: 106.412)

And, whereas we desire to assign our above-identified rights, title and interest in the Invention to Intel Corporation, 2200 Mission College Blvd., Santa Clara, CA 95052 US;

Now, this indenture witnesseth, that for good and valuable consideration, the receipt whereof is hereby acknowledged;

We hereby assign, sell and transfer our above-identified rights, title and interest in said Invention, said application(s) as identified above, including any divisions, continuations, and continuations-in-part thereof, and in and to any and all Letters Patent of the United States, and countries foreign thereto, which may be granted or have granted for said Invention, and in and to any and all reissues and reexaminations thereof, and in and to any and all priority rights, Convention rights, and other benefits accruing or to accrue to us with respect to the filing of applications for patents or securing of patents in the United States and countries foreign thereto, unto said Assignee;

And we hereby authorize and request the Commissioner of Patents and Trademarks to issue any United States Letters Patent which may issue for said Invention to said Assignee, as assignee of the whole right, title and interest thereto;

And we further agree to sign and execute all necessary and lawful future documents, including applications for foreign patents, for filing divisions, continuations and continuationsin-part of said application for patent, and/or, for obtaining any reissue or reissues of any Letters Patent which may be granted for our aforesaid Invention, as the Assignee or its Designee(s) may from time to time require and prepare at its own expense.

Inventors' Signatures

Rajily K. Mongia

Date

Himanshu Pokharna

Fax :

Attorney Docket No.: P 672 (BMT Ref. No.: 16.412)

And, whereas we desire to assign our above-identified rights, title and interest in the Invention to Intel Corporation, 2200 Mission College Blvd., Santa Clara, CA 95052 US;

Now, this indenture witnesseth, that for good and valuable consideration, the receipt whereof is hereby acknowledged;

Invention, said application(s) as identified above, including any divisions, continuations, and continuations-in-part thereof, and in and to any and all Letters Patent of the United States, and countries foreign thereto, which may be granted or have granted for said Invention, and in and to any and all priority rights,

Convention rights, and other benefits accruing or to accrue to us with respect to the filing of applications for patents or securing of patents in the United States and countries foreign thereto, unto said Assignee;

And we hereby authorize and request the Commissioner of Patents and Trademarks to issue any United States Letters Patent which may issue for said Invention to said Assignee, assignee of the whole right, title and interest thereto;

And we further agree to sign and execute all necessary and lawful future documents, including applications for foreign patents, for filing divisions, continuations and continuations in-part of said application for patent, and/or, for obtaining any reissue or reissues of any Letters Patent which may be granted for our aforesaid Invention, as the Assignee or its Designee(s) may from time to time require and prepare at its own expense.

| Date         | Rajiv K. Mongia   |
|--------------|-------------------|
| Date         |                   |
| 1th Dec 2005 | ximhu Poters      |
| Date         | Himanshu Pokharna |
|              |                   |
|              |                   |
| Date         | Eric DiStefano    |

Inventors' Signatures

ORDED: 12/30/2005

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