



U.S. DEPARTMENT OF COMMERCE U.S. Patent and Trademark Office

OMB No. 0651-0027 (exp. 6/30/2008) U.S. DEPARTMENT OF COMMERCE

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| To the Honorable Commissioner of Patents and Trademarks: Please | | | | | | |
|--|---|---|--|--|--|--|
| Name of conveying party(ies): | Name and address of receiving party(ies) | | | | | |
| SiGe Semiconductor Inc. | Name: | SiGe Semiconductor (Europe) Limited | | | | |
| Additional name(s) of conveying party(ies) attached? | Street Address: | South Building, Walden Court, Parsonage Lane | | | | |
| 3. Nature of conveyance: | City: | Bishop's Stortford | | | | |
| Assignment | Oity. | Dishop a Stortior | | | | |
| ☐ Security Agreement ☐ Change of Name | State: | Hertfordshire | | | | |
| Other: | ZIP: | CM23 5DB | | | | |
| | Country: | United Kingdom | | | | |
| Execution Date: | Additional name(s) & ad | dress(s) attached? Yes No | | | | |
| 4. Application Number(s) or patent number(s): | | | | | | |
| This document is being filed together with a new application. | | | | | | |
| A. Patent Application No.(s): | B. Patent No.(s): | | | | | |
| 10/847,609 10/875,570 | | | | | | |
| 10/984,747 10/969,866 | | | | | | |
| Additional numbers attached | Yes 🗵 No | | | | | |
| Name and address of party to whom correspondence concerning document should be mailed: | 6. Total number of applications and patents involved: 1 | | | | | |
| | 7. Total fee (37 CFR | 3.41 \$160.00 | | | | |
| Name: Freedman & Associates | _ | | | | | |
| Street Address: 117 Centrepointe Drive, Suite 350 | ⊠ Authorized to be charged to deposit account | | | | | |
| City: Nepean | Deposit account n | umber: | | | | |
| Province: Ontario | | 50-1142 | | | | |
| Postal Code: K2G 5X3 | /Attack disalizate | nu of this name if naving his damasis | | | | |
| Country: Canada | (Attach duplicate cop account) | by of this page if paying by deposit | | | | |
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| 9. Statement and signature. | | | | | | |
| To the best of my knowledge and belief, the foregoing information of the original document. | n is true and correct and | d any attached copy is a true copy | | | | |
| Q, ¬ | • | | | | | |
| Gordon Freedman, Reg. No. 41,553 September 19, 2005 | | | | | | |
| (Name of Person Signing) (Signature) (Date) | | | | | | |
| Total number of pages including cover sheet, attachments, and documents: 3 | | | | | | |
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Mail documents to be recorded with required cover sheet information to: Commissioner of Patents & Trademarks, Box Assignments

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Washington, D.C. 20231

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ASSIGNMENT

WHEREAS, SiGe Semiconductor Inc., a Canadian corporation, whose full post office address is 2680 Queensview Drive, Ottawa, Ontario, K2B 8J9, Canada (hereinafter the "Assignor"), is the owner of the patent applications (hereinafter the "Applications") listed in the attached schedule (hereinafter "Schedule A");

WHEREAS, SiGe Semiconductor (Europe) Limited, a British corporation, whose address is South Building, Walden Court, Parsonage Lane, Bishop's Stortford, Hertfordshire, CM23 5DB, United Kingdom (hereinafter the "Assignee"), desires to obtain the entire right, title and interest in and to the Applications listed in Schedule A and in and to any and all patents to be obtained therefor;

NOW, THEREFORE, in consideration of One Dollar (\$1.00) and other valuable consideration, the receipt of which is hereby acknowledged, the Assignor does hereby sell, assign and transfer unto said Assignee, its entire right, title and interest in and to the Applications listed in Schedule A, and any and all extensions, divisionals, reissues, substitutes, renewals or continuations thereof, including any and all patents which may be granted thereof, and the right to all benefits under the International Convention for the Protection of Industrial Property;

WHEREAS, the Assignor hereby authorizes and requests the Commissioner of Patents and Trademarks to issue any and all of said Letters Patent, when granted to the Assignee, its successors and assigns;

WHEREAS the Assignor hereby covenants that it has full right to convey its entire interest in the Applications listed in Schedule A and that it has not executed, and will not execute, any agreement or instrument in conflict therewith; and

WHEREAS the Assignor agrees that it will execute all appropriate documents, papers, forms or authorization, and depose to or swear to all proper declarations or oaths which may be necessary or appropriate for securing, completing, or absolutely vesting title in the Assignee, to the interests herein assigned.

| IN TESTIMONY WHEREOF, the Assig | mor, has caused this Assignment to be executed by its duly |
|--|--|
| authorized representative, on this 22 da | ay of August , 2005. |
| WITNESS: | SiGe Semiconductor Inc. |
| BEV HOUISH Printed Name of Witness | Per: |
| /// | Name: Stephen J. Kovacic |
| Signature of Witness | Title: <u>Director, Research and Development and IP</u> Management |

SCHEDULE "A"

U.S. PATENT APPLICATIONS

| Attorney Docket | Application No. | Filing Date | Title. | inventor(s) |
|-----------------|-----------------|-------------|---|-------------------------------|
| 115-43 US | 10/847,609 | 18-May-04 | Segmented Switching Power Amplifier | Loraine, Wojtiuk, Macphail |
| 115-44 US | 10/875,570 | 25-Jun-04 | Transmit Signal Generator and Method | Macphail, Loraine |
| 115-45 US | 10/969,866 | 22-Oct-04 | Polar Signal Processor to Drive a Segmented Power Amplifier and Method Therefore | Wojtiuk |
| 115-46 US | 10/984,747 | 10-Nov-04 | Driver Circuit for Driving a Power Amplifier | Wojtiuk |

PATENT
RECORDED: 09/21/2005 REEL: 017012 FRAME: 0385