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RECORDATION FORM COVER SHEET
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To the Director of the U.S. Patent and Trademark Office: Please record the attached documents or the new address(es) below.

1. Name of conveying party(ies)

TriQuint Semiconductor, Inc.

Additional name(s) of conveying party(ies) attached? ☐ Yes ☒ No

3. Nature of conveyance/Execution Date(s):

Execution Date(s) 9/8/05

- ☒ Assignment ☐ Merger
☐ Security Agreement ☐ Change of Name
☐ Joint Research Agreement
☐ Government Interest Assignment
☐ Executive Order 9424, Confirmatory License
☐ Other _____

2. Name and address of receiving party(ies)

Name: Null Networks LLC

Internal Address: Suite 5

Street Address: 2215-B Renaissance Drive

City: Las Vegas

State: Nevada

Country: USA Zip: 89119

Additional name(s) & address(es) attached? ☐ Yes ☒ No

4. Application or patent number(s):

☐ This document is being filed together with a new application.

A. Patent Application No.(s)

B. Patent No.(s)

Additional numbers attached? ☒ Yes ☐ No

5. Name and address to whom correspondence concerning document should be mailed:

Name: Berkeley Law & Technology Group, LLC

Internal Address: Suite 240

Street Address: 1700 NW 167th Place

City: Beaverton

State: Oregon Zip: 97006

Phone Number: 503.439.6500

Fax Number: 503.439.6558

Email Address: sverboort@bltg-ip.com

6. Total number of applications and patents involved: 37

7. Total fee (37 CFR 1.21(h) & 3.41) \$ 1480

- ☐ Authorized to be charged by credit card
☒ Authorized to be charged to deposit account
☐ Enclosed
☐ None required (government interest not affecting title)

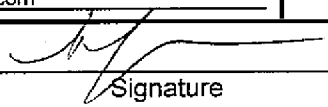
8. Payment Information

a. Credit Card Last 4 Numbers _____
Expiration Date _____

b. Deposit Account Number 50-3703

Authorized User Name Howard A. Skaist

9. Signature:


Signature

12/27/05
Date

Michael J. Willardson, Reg. No. 50,856

Name of Person Signing

Total number of pages including cover sheet, attachments, and documents: 6

Documents to be recorded (including cover sheet) should be faxed to (703) 306-5995, or mailed to:
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LIST OF APPLICATIONS FOR RECORDATION
TRIQUINT SEMICONDUCTOR, INC. TO NULL NETWORKS LLC

	Patent No.	App. No.	Filing Date
1.	6,576,413		5/18/01
2.	6,580,731		5/18/01
3.	6,701,479		5/15/01
4.	6,732,228		7/19/01
5.	6,803,562		3/1/02
6.	6,820,228		6/18/01
7.	6,842,816		7/31/01
8.		09/836,965	4/17/01
9.		09/836,970	4/17/01
10.		09/860,207	5/18/01
11.		09/910,116	7/19/01
12.		09/918,691	7/30/01
13.		09/918,896	7/30/01
14.		09/918,931	7/30/01
15.		09/919,728	7/31/01
16.	6,904,537		8/27/01
17.		10/087,263	3/1/02
18.		10/087,722	3/1/02
19.		10/087,878	3/1/02
20.		10/106,977	3/25/02
21.		10/136,662	4/30/02
22.		10/210,989	8/2/02
23.		10/211,001	8/2/02
24.		10/211,002	8/2/02
25.		10/211,005	8/2/02
26.		10/211,046	8/2/02
27.		10/211,047	8/2/02
28.		10/211,158	8/2/02
29.		10/211,174	8/2/02
30.		10/273,626	10/18/02
31.		10/279,154	10/23/02
32.		10/307,104	11/27/02
33.		10/393,088	3/19/03
34.		10/414,115	4/14/03
35.		10/428,167	4/30/03
36.		10/444,460	5/22/03
37.	6,679,636		6/29/01

Exhibit B

ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, TriQuint Semiconductor, Inc. having offices at 2300 NE Brookwood Parkway, Hillsboro, Oregon 97124, ("Assignor"), does hereby sell, assign, transfer and convey unto Null Networks LLC, a Nevada limited liability company, having an office at 2215-B Renaissance Drive, Suite 5, Las Vegas, NV 89119 ("Assignee") or its designees, all of Assignor's right, title and interest in and to the patent applications and patents listed below, any patents, registrations, or certificates of invention issuing on any patent applications listed below, the inventions disclosed in any of the foregoing, any and all counterpart United States, international and foreign patents, applications and certificates of invention based upon or covering any portion of the foregoing, and all renewals, re-examinations, divisionals, renewals, extensions, provisionals, continuations and continuations-in-part of any of the foregoing (collectively "Patent Rights"):

Patent No.	Country	Filing Date	Description
6,557,413	USA	5/18/2001	OPTICAL NETWORKING MODULE INCLUDING PROTOCOL PROCESSING AND UNIFIED SOFTWARE CONTROL
6,580,731	USA	5/18/2001	MULTI-STAGE SONET OVERHEAD PROCESSING
6,701,479	USA	5/15/2001	FAST CYCLIC REDUNDANCY CHECK (CRC) GENERATION
6,732,228	USA	7/19/2001	MULTI-PROTOCOL DATA CLASSIFICATION USING ON-CHIP CAM
6,803,562	USA	3/1/2002	INDIRECT MONITORING OF SEMICONDUCTOR LIGHT SOURCE WITHIN A PHOTONIC PACKAGE
6,820,226	USA	6/18/2001	FAST CYCLIC REDUNDANCY CHECK (CRC) GENERATION
6,842,816	USA	7/31/2001	CONFIGURABLE GLUELESS MICROPROCESSOR INTERFACE
09/836,935	USA	4/17/2001	PIPELINED DECIPHERING ROUND KEYS GENERATION
09/838,970	USA	4/17/2001	UTILIZING AVAILABLE SONET OVERHEAD BYTES FOR ADDITIONAL SIGNALING CHANNELS
09/880,207	USA	5/18/2001	MULTI-PROTOCOL NETWORKING PROCESSOR WITH DATA TRAFFIC SUPPORT SPANNING LOCAL, REGIONAL AND WIDE AREA NETWORKS
09/910,118	USA	7/19/2001	PROGRAMMABLE PACKET FILTERING IN A PRIORITIZED CHAIN
09/918,881	USA	7/30/2001	DATA LINK/PHYSICAL LAYER PACKET DIVERSION AND INSERTION
09/918,886	USA	7/30/2001	PROGRAMMABLE SONET FRAMING

Document Id: TriQuint NEI Patents - Patent Purchase Agreement - Final(3) - Execution Copy - 2005-07-05

Patent No.	Country	Issue Date	Patent Title
09/918,931	USA	7/30/2001	DATA LINK PHYSICAL LAYER PACKET BUFFERING AND FLUSHING
09/919,728	USA	7/31/2001	METHOD AND APPARATUS FOR PROGRAMMABLE GENERATION OF TRAFFIC STREAMS
6,904,537	USA	8/27/2001	DATA TRANSMISSION ACROSS ASYNCHRONOUS TIME DOMAINS USING PHASE-SHIFTED DATA PACKET
10/087,263	USA	3/1/2002	DATA BYTE INSERTION CIRCUITRY
10/087,722	USA	3/1/2002	HARDWARE SELF-SORTING SCHEDULING QUEUE
10/087,878	USA	3/1/2002	MONITORING A SEMICONDUCTOR LASER UTILIZING AN INCORPORATED BEAM SPLITTER DEVICE
10/106,977	USA	3/25/2002	IMPEDANCE MATCHING CONNECTION SCHEME FOR HIGH FREQUENCY CIRCUITS
10/136,862	USA	4/30/2002	METHOD AND APPARATUS FOR FAST MODULAR REDUCTION
10/210,989	USA	8/2/2002	SOFTWARE METHODS OF AN OPTICAL NETWORK APPARATUS WITH MULTIPLE MULTI-PROTOCOL OPTICAL NETWORKING MODULES HAVING INSERTION
10/211,001	USA	8/2/2002	FLEXIBLE INTERRUPT HANDLING METHODS FOR OPTICAL NETWORK APPARATUSES WITH MULTIPLE MULTI-PROTOCOL OPTICAL NETWORKING MODULE
10/211,002	USA	8/2/2002	SOFTWARE METHODS OF AN OPTICAL NETWORK APPARATUS WITH INTEGRATED MODULES HAVING PROTOCOL PROCESSORS AND PHYSICAL LAYER ROM
10/211,005	USA	8/2/2002	SOFTWARE METHODS OF AN OPTICAL NETWORK APPARATUS WITH MULTIPLE MULTI-PROTOCOL OPTICAL NETWORKING MODULES
10/211,046	USA	8/2/2002	COUNTER FUNCTIONS IN AN APPLICATION PROGRAM INTERFACE FOR NETWORK DEVICES
10/211,047	USA	8/2/2002	EXTENSIBLE CONFIGURATION METHODS FOR OPTICAL NETWORK APPARATUSES WITH MULTIPLE MULTI-PROTOCOL OPTICAL NETWORKING MODULES
10/211,158	USA	8/2/2002	METHODS OF AN OPTICAL NETWORK APPARATUS WITH MULTIPLE MULTI-PROTOCOL OPTICAL NETWORKING MODULES HAVING PACKET FILTERING RESOURCES
10/211,174	USA	8/2/2002	PREEMPTIVE NETWORK TRAFFIC CONTROL FOR REGIONAL AND WIDE AREA NETWORKS
10/273,826	USA	10/18/2002	FLEXIBLE ARCHITECTURE FOR SONET AND OTN FRAME PROCESSING
10/279,164	USA	10/23/2002	A CONTENT ADDRESSABLE MEMORY WITH AUTOMATED LEARNING
10/307,104	USA	11/27/2002	METHOD AND APPARATUS FOR CONTROLLING AN OPTICAL TRANSPONDER

PATENT NO.	COUNTRY	DATE	DESCRIPTION
10/393,068	USA	2/19/2003	DATA TRANSMISSION ERROR REDUCTION VIA AUTOMATIC DATA SAMPLING TIMING ADJUSTMENT
10/414,118	USA	4/14/2003	OPTICAL NETWORKING MODULE INCLUDING PROTOCOL PROCESSING AND UNIFIED SOFTWARE CONTROL
10/428,167	USA	4/30/2003	MULTI-STAGE SONET OVERHEAD PROCESSING
10/444,480	USA	5/22/2003	LASER DRIVE METHOD AND APPARATUS
PCT/US02/16800	PCT	5/17/2002	AN OPTICAL NETWORKING MODULE INCLUDING PROTOCOL PROCESSING AND UNIFIED SOFTWARE CONTROL
PCT/US02/15384	PCT	5/17/2002	MULTI-STAGE SONET OVERHEAD PROCESSING
PCT/US02/15408	PCT	5/14/2002	FAST CYCLIC REDUNDANCY CHECK (CRC) GENERATION
PCT/US02/12316	PCT	4/17/2002	PIPELINED DECIPHERING ROUND KEYS GENERATION
PCT/US02/15755	PCT	5/17/2002	A MULTI-PROTOCOL NETWORKING PROCESSOR WITH TRAFFIC SUPPORT SPANNING LOCAL, REGIONAL AND WIDE AREA NETWORKS
PCT/US02/24288	PCT	7/30/2002	DATA LINK/PHYSICAL LAYER PACKET DIVERSION AND INSERTION
PCT/US02/24289	PCT	7/30/2002	PROGRAMMABLE SONET FRAMING
PCT/US02/24348	PCT	7/30/2002	METHOD AND APPARATUS FOR PROGRAMMABLE GENERATION OF TRAFFIC STREAMS
6,879,636	USA	8/29/2001	METHOD AND APPARATUS FOR MAINTAINING ALIGNMENT OF A LASER DIODE WITH AN OPTICAL FIBER

Assignor represents, warrants and covenants that: (i) it is the sole owner, assignee and holder of record title to the Patent Rights identified above, (ii) it has obtained and properly recorded previously executed assignments for all patent applications and patents identified above as necessary to fully perfect its rights and title therein in accordance with governing law and regulations in each respective jurisdiction, and (iii) it has full power and authority to make the present assignment.

Assignor further agrees to and hereby does sell, assign, transfer and convey unto Assignee all of Assignor's rights: (i) in and to causes of action and enforcement rights for the Patent Rights including all rights to pursue damages, injunctive relief and other remedies for past and future infringement of the Patent Rights, and (ii) to apply in any or all countries of the world for patents, certificates of invention or other governmental grants for the Patent Rights, including without limitation under the Paris Convention for the Protection of Industrial Property, the International Patent Cooperation Treaty, or any other convention, treaty, agreement or understanding. Assignor also hereby authorizes the respective patent office or governmental agency in each juris-

diction to issue any and all patents or certificates of invention which may be granted upon any of the Patent Rights in the name of Assignee, as the assignee to the entire interest therein.

Assignor will, at the reasonable request of Assignee and, except as provided below, without demanding any further consideration therefor, take commercially reasonable steps, including without limitation the execution, acknowledgment and recordation of specific assignments, oaths, declarations and other documents on a country-by-country basis, to assist Assignee in obtaining, fully perfecting, sustaining and/or conveying unto Assignor the Patent Rights. Such assistance shall include using commercially reasonable efforts to provide, and obtain from the respective inventors, prompt production of pertinent facts and documents, giving of testimony, execution of petitions, oaths, powers of attorney, specifications, declarations or other papers and other assistance reasonably necessary for filing patent applications, complying with any duty of disclosure, and conducting prosecution, reexamination, appeal, interference or other priority proceedings, opposition proceedings, cancellation proceedings, public use proceedings, infringement or other court actions and the like with respect to the Patent Rights. Assignee agrees, as a condition to Assignor's assistance, to reimburse Assignor's out-of-pocket expenses associated with enforcing the Patent Rights including any incurred in connection with infringement or other actions, provided that Assignor obtains Assignee's approval prior to incurring such expenses, which approval shall not be unreasonably withheld.

The terms and conditions of this Assignment shall inure to the benefit of Assignee, its successors, assigns and other legal representatives, and shall be binding upon Assignor, its successor, assigns and other legal representatives.

IN WITNESS WHEREOF this Assignment of Patent Rights is executed at 10:25 am

on September 8, 2005

ASSIGNOR

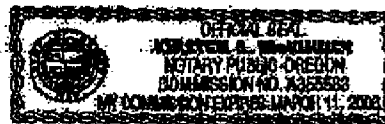
By:

Name:

Glen A. Riley

Title:

VP Business Development



State of OREGON

County of Washington

Signed or attested before me on September 8, 2005 by Glen Riley

Kristen A. McKinney

Notary Public - State of Oregon

My commission expires March 11, 2006

