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U.S. DEPARTMENT OF COMMERCE Patent and Trademark Office

IEN IS ONLY	Attorney Docket Number 05

	PATENTS ONLY	,	Attorney Docket Number 05-0453
To th	ne Honorable Commissioner of Patents and T	rademarks	Please record the attached original assignment document.
Name of all Party(ies) conveying an interest:			2. Name and Address of receiving Party(ies):
	Jason D. Hudson Sean Erickson Michael J. Saunders		LSI Logic Corporation 1621 Barber Lane, MS D-106 Milpitas, CA 95035
Additio	onal name(s) of conveying party(ies)attached? Yes	⊠ No	Additional name(s) and address(es)attached?
Nature of Conveyance: Assignment		ne	Assignment Execution Date(s): Oct. 31, 05 Oct. 31, 05 Oct. 31, 05
			Title: MULTI-SURFACED PLATE-TO-PLATE CAPACITOR AND METHOD OF FORMING SAME
4.	Application number(s) or patent num If this document is being filed together with a	nber(s): i new applic	ation, the execution date of the application is:
	Oct. 31, (A. Patent Application No.(s)	05, Oct. 3	81, 05, Oct. 31, 05 B. Patent No.(s)
		Addition	al numbers attached? Yes No
5.	Name and Address of party to whom correspondence concerning document should be mailed:	6. Total	number of applications and patents involved = 1
Δttn	Attn: LSI Logic Docket No. 05-0453		Fee (37 CFR 3.41): \$ 40.00
LSI Logic Corporation			Rosed
1621	l Barber Lane, MS D-106 litas, CA 95035	Aut	horized to be charged to Deposit Account
		8 Den	osit Account Number: 12-2252
	DO N		THIS SPACE
9.	Statement and signature.	nformation is true and correct and any attached copy is a	
	Name of Person Signing Kermit Lopez	Sign	Kermi Lope
	Date November 2, 2005	Tota	number of pages comprising cover sheet: 1

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ASSIGNMENT

LSI Logic Docket No.: 05-0453

The undersigned is/are the named inventor(s) (the "Inventor(s)") on a United States patent application entitled MULTI-SURFACED PLATE-TO-PLATE CAPACITOR AND METHOD OF FORMING SAME (the "Application") for inventions disclosed or claimed therein (the "Invention(s)"), which Application:

• •		
is being executed concurrently herewith,		
was first executed on		
was filed on as Application Number .		
claims the benefit of U.S. Provisional Application(s) No(s).	1	filed

LSI Logic Corporation, on behalf of itself, its successors and assigns, and its legal representatives ("LSI Logic"), is entitled to all rights in the Application and the Invention(s), and the full cooperation of the Inventor(s).

For valuable consideration, the receipt of which is hereby acknowledged, the Inventor(s) hereby sell(s), assign(s), and transfer(s) to LSI Logic the entire and exclusive right, title and interest in the Application and the Invention(s), in and for the United States, its territories, and all foreign countries, including all applications, patents, design registrations and publications obtained or derived therefrom, world wide, and all priority rights under all present or future conventions and treaties, and any provisional applications for which priority is claimed in the Application; and

- (1) request the U.S. Commissioner of Patents to issue all U.S. Letters Patent granted thereon to LSI Logic;
- (2) agree that LSI Logic may apply for and receive all foreign Letters Patent thereon;
- (3) agree to execute all papers and take all actions necessary or desirable in connection therewith requested by LSI Logic; and
- (4) authorize LSI Logic to subsequently enter the Application Number on this Assignment, if not already entered above.

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Assignment
LSI Logic Docket No. 05-0453

DOCKET NUMBER: 05-0453

Full name of 1st Joint Inventor:

Jason D. Hudson

inventor's Signature:

10/31/05

Date:

Residence (City, State):

Eaton, Colorado

Post Office Address:

438 Dogwood Ct.

Eaton, CO 80615-9071

Citizenship:

U.S.A.

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DOCKET NUMBER: 05-0453

Full name of 2nd Joint Inventor:

Sean Erickson

Inventor's Signature:

Date:

Residence (City, State):

Fort Collins, Colorado

Post Office Address:

2918 Sagebrush Drive Fort Collins, CO 80525

Citizenship:

U.S.A.

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Assignment
LSI Logic Docket No. 05-0453

DOCKET NUMBER: 05-0453

Full name of 3rd Joint Inventor:

Michael J. Saunders

Inventor's Signature:

monion o orginatore.

Date:

Residence (City, State):

RECORDED: 11/02/2005

Fort Collins, Colorado

Post Office Address:

2980 Cornerstone Lane Fort Collins, CO 80528

Citizenship:

U.S.A.

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LSI Logic Docket No. 05-0453