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SUBMISSION TYPE:			NEW ASSIGNMENT			
NATURE OF CONVEYANCE:			ASSIGNMENT			
CONVEYING PARTY	DATA					
N N			lame	Execution Date		
Tavanza, Inc.				10/15/2003		
RECEIVING PARTY D	ΑΤΑ					
Name:	Anadigics Inc.					
Street Address:	Address: 141 Mt. Bethel Road					
City:	Warren					
State/Country:	NEW JERSEY					
Postal Code:	07059					
Property Type			Number			
		10327	0327512			
			004976			
			024943			
CORRESPONDENCE	DATA					
Fax Number: (212)309-6001						
Correspondence will be sent via US Mail when the fax attempt is unsuccessful.						
Phone: (212) 309-6000 Email: nswanson@morganlewis.com						
Email: nswanson@morganlewis.com Correspondent Name: Nancy Swanson						
Address Line 1: 101 Park Avenue						
Address Line 2: 60990-0000 (Tavanza)						
Address Line 4: New York, NEW YORK 10178						
ATTORNEY DOCKET NUMBER:			60999-0000 (TAVANZA)			
NAME OF SUBMITTER:			Nancy Swanson			

Total Attachments: 3

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> PATENT REEL: 017205 FRAME: 0249

PATENT ASSIGNMENT

This Patent Assignment ("<u>Assignment</u>") is made as of October 15, 2003, by Tavanza, Inc., a Delaware corporation having a principal place of business at 3236 Scott Boulevard, Santa Clara, California 95054 ("<u>Assignor</u>"), to Anadigics Holding Corp. a Delaware corporation having a place of business at 131 Mount Bethel Road, Warren, New Jersey 07059 ("<u>Assignee</u>").

RECITALS

A. Assignor, Assignee, Celeritek, Inc, and Anadigics, Inc. have entered into an Asset Purchase Agreement dated as of October 14, 2003 (the "<u>Purchase Agreement</u>").

B. Assignor owns the patent applications filed with the United States Patent and Trademark Office, as set forth on <u>Schedule A</u> hereto (the "<u>Patents</u>").

C. Pursuant to the Purchase Agreement, Assignor desires to assign to Assignee all of Assignor's right, title and interest in and to the Patents.

AGREEMENT

NOW, THEREFORE, in consideration of the foregoing premises, the mutual covenants and agreements contained in the Purchase Agreement and the covenants and agreements in this Assignment and to induce Assignee to consummate the transactions contemplated by the Purchase Agreement, Assignor agrees as follows:

1. Assignor does hereby irrevocably sell, transfer, convey, assign and deliver to Assignee all of Assignor's right, title and interest in and to the Patents (and regarding Patents that are patent applications, any patents that may issue therefrom), including any foreign counterparts, patents or patent applications to which the Patents claim priority, divisionals, continuations in whole or in part, reexaminations, reissues or extensions thereof, and the right to claim priority to any of the preceding, the same to be held by Assignee for Assignee's own use and enjoyment, and for the use and enjoyment of Assignee's successors, assigns and other legal representatives, as fully and entirely as the same would have been held and enjoyed by Assignor if this Assignment and sale had not been made; together with all claims for damages by reason of past infringements of the Patents and the right to sue for and collect such damages, as permitted under the applicable laws for any jurisdiction or country in which such claims may be asserted, for the use and benefit of Assignee and its successors, assigns and other legal representatives.

2. Assignor hereby authorizes and requests the Commissioner of Patents and Trademarks of the United States, and any officer of any country or countries foreign to the United States, whose duty it is to issue patents or other evidence or forms of intellectual property protection or applications as aforesaid, to issue the same to Assignee and its successors, assigns and other legal representatives in accordance with the terms of this instrument.

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PATENT REEL: 017205 FRAME: 0250

2011-1010-1010-10100

調査の方

IN WITNESS WHEREOF, Assignor has executed this Assignment on the date first above written.

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TAVANZA, INC.

By: Tamer Husseini

Chief Executive Officer

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Schedule A

Compact layout for a semiconductor device	
10/327,512	
Integrated circuits with scalable design 10/004,976	
Internal impedance match in integrated circuits 10/025,437	
Dynamic matching in cascode circuits 10/024,943	
Passive free technology integrated circuit 60/448,714	
	10/327,512 Integrated circuits with scalable design 10/004,976 Internal impedance match in integrated circuits 10/025,437 Dynamic matching in cascode circuits 10/024,943 Passive free technology integrated circuit

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PATENT REEL: 017205 FRAME: 0252

RECORDED: 02/23/2006