PATENT ASSIGNMENT

Electronic Version v08 Stylesheet Version v02

SUBMISSION TYPE:

NEW ASSIGNMENT

APPLICATION NUMBER
11/307928

NATURE OF CONVEYANCE:

ASSIGNMENT OF ASSIGNOR'S INTEREST

CONVEYING PARTY DATA

Name	Execution Date		
Wenzhi Gao	2006-02-16		
Young Way Teh	2006-02-22		

RECEIVING PARTY DATA

Name	Street Address	Internal Address	City	State/Country	Postal Code
Chartered	60 Woodlands				
Semiconductor	Industrial Park D,		Singapore	MALAYSIA	738046
Manufacturing LTD	Street 2				

CORRESPONDENCE DATA

FAX NUMBER: 8458926363

Correspondence will be sent via US Mail when the fax attempt is unsuccessful.

When the customer number has been provided, the Office of Public Records will obtain the correspondence data from the

official record on file at the USPTO. CUSTOMER NUMBER: 045988

 NAME OF PERSON SIGNING:
 Joseph P. Abate

 DATE SIGNED:
 2006-02-28

Total Attachments: 2

source=FIS920050321US1PatentAssnChartered1.tif source=FIS920050321US1PatentAssnChartered2.tif

CH \$40.00 090458 11

PATENT REEL: 017227 FRAME: 0683

800024725

PATENT ASSIGNMENT FORM

For and in consideration of good and valuable consideration, the receipt and sufficiency thereof being hereby acknowledged, each undersigned inventor acknowledges his/her prior and ongoing obligation to sell, assign, and transfer, and agrees to sell, assign, and transfer the entire world wide right, title and interest for the United States and all foreign countries, in and to any and all inventions and improvements conceived during and within the scope of his/her employment, and further acknowledges that he/she has sold, assigned, and transferred, and hereby sells, assigns, and transfers, unto:

Chartered Semiconductor Manufacturing LTD, 60 Woodlands Industrial Park D, Street 2 Singapore, 738046

a corporation of Singapore, and CHARTERED SEMICONDUCTOR MANUFACTURING LTD desires to acquire all right, title, and interest, in and to the certain inventions (identified below), applications, and any United States and foreign patents to be obtained therefor relating to:

Title of Invention for SPACER AND PROCESS TO ENHANCE THE STRAIN IN THE CHANNEL WITH STRESS LINER
Linited States Potent

United States Patent
as set forth in United States Patent Application executed by each undersigned inventor on the date indicated by each inventor signature below, and further identified by Attorney Docket Number FIS920050321US1; (select one): executed concurrently herewith or Serial No. Filed on Filed on any and to said United States Patent Application including any and all divisions or continuations thereof and in and to any and all Letters Patent of the United States and foreign patents and all rights of priority to be obtained therefor which may issue on any such application or for said invention therein disclosed, including any and all reissues or extensions thereof, to be held and enjoyed by, its successors, legal representatives and assigns to the full end of the term or terms for which any and all such Letters Patent may be granted as fully and entirely as would have been held and enjoyed by the undersigned had this Assignment not been made;
Each undersigned inventor hereby authorizes and requests the Commissioner of Patents and Trademarks to issue any and all such Letters Patent to CHARTERED SEMICONDUCTOR MANUFACTURING LTD, its successors and assigns, in accordance herewith;
Each undersigned inventor warrants and covenants that he/she has the full and unencumbered right to sell and assign the interests hereby sold and assigned and that he/she has not executed and will not execute any document or instrument in conflict herewith;
Each undersigned inventor further covenants and agrees that he/she, upon the request and at the expense of the CHARTERED SEMICONDUCTOR MANUFACTURING LTD, will execute and deliver any papers, make all rightful oaths, testify in any legal proceeding relating to said inventions and improvements, communicate to the CHARTERED SEMICONDUCTOR MANUFACTURING LTD all facts known to the undersigned relating to such inventions and improvements and the history thereof; and perform all other lawful acts deemed necessary or desirable by CHARTERED SEMICONDUCTOR MANUFACTURING LTD, and its legal representatives, to secure, maintain, and enforce patent protection for such inventions and improvements and for vesting title to such inventions and improvements in the CHARTERED SEMICONDUCTOR MANUFACTURING LTD, and in particular to perfect title to said above-identified certain inventions, and applications including divisions and continuations thereof, and any and all Letters Patent which may be granted therefor or thereon, including reissues, extensions, or counterparts;
Each undersigned inventor hereby grants the CHARTERED SEMICONDUCTOR MANUFACTURING LTD and the firm of the power to insert in this Assignment any further identification which may be necessary or desirable for recordation of this Assignment.
This assignment is governed by the substantive laws of the Singapore.
Signature:

PATENT REEL: 017227 FRAME: 0684

	Exc	ecuted by Inventor 2 of 2			
Signature: _	Volum Way TEH	Date:	2/2	1 20do	

PATENT

REEL: 017227 FRAME: 0685