

11-18-2005

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To the Honorable Commissioner for Patents and Trademarks: Please record the attached original document or copy thereof.

1. Name(s) of conveying party(ies):

1) H. MONTGOMERY MANNING
 2) HOWARD KIRSCH
 3) _____
 4) _____
 5) _____
 6) _____

Additional names of conveying parties attached? Yes No

2. Name and address of receiving party:

Name: MICRON TECHNOLOGY, INC.

Internal Address: _____

Street Address: 8000 SOUTH FEDERAL WAY

P.O. BOX 6 / 83707-0006

City: BOISE State: IDAHO

Zip: 83716-9632

Additional names & addresses attached? Yes No



3. Nature of conveyance:

Assignment Merger

Security Agreement Change of Name

Other _____

Execution Dates:

1) SEPTEMBER 21, 2005 4) _____
 2) OCTOBER 5, 2005 5) _____
 3) _____ 6) _____

4. Application number(s) or patent number(s): This document is being filed together with a new application.

A. Patent Application No(s). 11/15/2005 FFANAI2 00000010 11271024

B. Patent No(s). _____

Additional numbers attached? Yes No

11/15/2005 FFANAI2 00000010 11271024

40.00 DP

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: DORSEY & WHITNEY LLP

Internal Address: EDWARD W. BULCHIS

SUITE 3400

Street Address: 1420 FIFTH AVENUE

City: SEATTLE State: WA ZIP: 98101

6. Total number of applications and patents involved..... One (1)

7. Total Fee (37 CFR 3.41): \$40

Enclosed

Authorized to be charged to deposit account

8. Deposit account number:

50-1266

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9. Statement and signature.

EDWARD W. BULCHIS Edward W. Bulchis Nov 9, 2005

Name of Person Signing Signature Date

Total number of pages including cover sheet, attachments, and document: 4

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Mail Stop Assignment Recordation Services, Director of the USPTO, P.O. Box 1450, Alexandria, VA 22313-1450

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

H. Montgomery Manning and Howard
Kirsch

Docket No. 186226/US

Disclosure No. 03-1334

Filed : Concurrently herewith

For : LOCAL DIGIT LINE
ARCHITECTURE AND METHOD
FOR MEMORY DEVICES HAVING
MULTI-BIT OR LOW CAPACITANCE
MEMORY CELLS

ASSIGNMENT :

 Enclosed for recording
 Previously recorded

Date: _____

Reel: _____

FOR GOOD AND VALUABLE CONSIDERATION, the receipt, sufficiency and adequacy of which are hereby acknowledged, the undersigned do hereby:

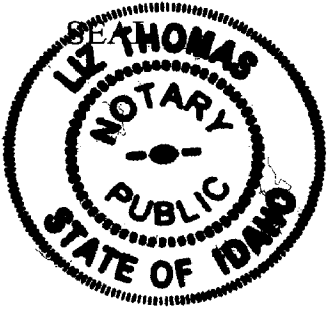
SELL, ASSIGN AND TRANSFER to **Micron Technology, Inc.** (the "Assignee"), a corporation of Delaware, having a place of business at 8000 South Federal Way, Boise, Idaho 83716-9632, the entire right, title and interest for the United States and all foreign countries, in and to any and all improvements which are disclosed in the application for United States Letters Patent, which has been executed by the undersigned concurrently herewith and is entitled "**LOCAL DIGIT LINE ARCHITECTURE AND METHOD FOR MEMORY DEVICES HAVING MULTI-BIT OR LOW CAPACITANCE MEMORY CELLS**"; such application and all divisional, continuing, substitute, renewal, reissue and all other applications for patent which have been or shall be filed in the United States and all foreign countries on any of such improvements; all original and reissued patents which have been or shall be issued in the United States and all foreign countries on such improvements; and specifically including the right to file foreign applications under the provisions of any convention or treaty and claim priority based on such application in the United States of America;

Howard Kirsch
Howard Kirsch

Date: 10-5-2005

STATE OF IDAHO)
) ss.
County of Ada)

BEFORE ME, this 5th day of October, 2005, personally appeared the above-named individual, to me known to be the person who is described in and who executed the foregoing assignment instrument and acknowledged to me that he executed the same of his own free will for the purpose therein expressed.



Liz Thomas
Notary or Consular Officer
My Commission Expires 3/24/09

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