FEB. 3.2006— 5:24PM——HARNESS, DICKEY & PI Form PTO-1595 RECORDATION FORM (Rev. 10/02) OMB No. 0651-0027 (exp. 6/30/2005) PATENT	- AR HI - I - I bla			
Tab settings	Please Record the attached original documents or copy thereof.			
1. Name of conveying party(ies): Marvell Semiconductor, Inc. Additional name of conveying party(les) attached? Yes No	2. Name and address of receiving party(les) Name: Marvell International Ltd. Internal Address:			
3. Nature of conveyance:	Street Address: Argyle House			
⊠ Assignment ☐ Merger	41A Cedar Avenue			
☐ Security Agreement ☐ Change of Name	City: Hamilton State: Bermude Zip: HM12			
Execution Date: April 4, 2005	Additional Name(s) & address(es) attached? ☐ Yes ☑ No			
If this document is being filed together with a new application A. Patent Application No.(s) Unassigned, filed herewith. Additional numbers at	B. Patent No.(s) tached? Yes No			
Name and address of party to whom correspondence concerning this document should be malled:	6. Total number of applications and patents involved: 1			
Name: Michael D. Wiggins Internal Address: Hamess, Dickey & Pierce, P.L.C.	7. Total fee (37 CFR 3.41)			
Street Address; P.O. Box 828	8. 8. The Commissioner is hereby authorized to charge any additional fees that may be required or credit any overpayments to Deposit Account No. 08-0750. A duplicate copy of this sheet is			
City: Bloomfield Hills State: MI Zip: 48303	enclosed. (Attach duplicate copy of this page if paying by deposit account)			
DO NOT USE THIS SPACE				
Name of Person Signing	mation is true and correct and any attached copy April 4, 2005 Signature Date cover sheet, attachments, and documents:			

Mail documents to be recorded with required cover sheet information to: Commissioner of Patents & Trademarks, Box Assignments

PATENT REEL: 017233 FRAME: 0465

Total number of pages including cover sheet, attachments, and documents:

Mail documents to be recorded with required cover street information to:

Commissioner of Patents & Trademarks, Box Assignments

Signature Y

- 4

To the best of my knowledge and ballef, the longoing information is true and correct and any attached copy

is a true copy of the original document,
Michael D. Windine

Name of Person Signing

PATENT

Date

-April 4, 2005

REEL: 017233 FRAME: 0466

ASSIGNMENT Corporate

Marvell Semiconductor, Inc., a California corporation, having a principal place of business at 700 First Avenue, Sunnyvale, CA 94089 (hereafter the "Assignor"), is the owner by respective Assignment of United States Patent Application identified below (hereafter sometimes called the "Patent Application").

Marvell International Ltd., a corporation of Bermuda, having a place of business at Argyla House, 41A Cedar Avenue, Hamilton, Hm12, Bermuda (hereafter the "Assignee"), desires to acquire all of the rights to the Patent Application and all inventions described and claimed therein.

THEREFORE, in consideration of the sum of One Dollar (\$1.00) or equivalent and other good and valuable consideration paid to it, Assigner hereby sells and easigns to the Assignes the entire right, title and interest in and to the Petent Application identified below, and all inventions described and claimed therein, in any and all Letters Patent therefor, and in any and all reissues, extensions, renewals, respansive divisions and continuations of such applications or Letters Patent to the full end of the term or terms for which such Letters Patent issue, such sutire right, title and interest to be held and enjoyed by the above-named Assignes the same as they would have been held and enjoyed by the Assignor had this Assignment and sale not been made.

Marvell Semiconductor Inc. Cose No.	U.S. Seriel No.	U.S. Filips Date	Title
MP0601	To Be Antiqued	Herewith	Low Voltage Logic Operation Using Higher Voltage Supply Levels

By its undersigned representative, the Assigner agrees

- s. to execute all papers necessary in connection with the Patent Application and any continuing, divisional, retagns, regrammation or corresponding application thereof and also to execute separate Assignment in connection with such application as the Assignee may deem necessary or expedient;
- b. to execute all papers necessary in connection with any interference which may be declared concerning the Patent Application or any continuation, division, raissus or reexamination thereof and to cooperate with the Assignes in avery way possible in obtaining evidence and going forward with such interference; and
- c. to perform all affirmative acts which may be necessary to obtain a grant of a valid United States patent to the Assignee on any of the Patent Application and on

Corporate Assignment Metro

pp V

PATENT REEL: 017233 FRAME: 0467

ASSIGNMENT Corporate

Marvell Semiconductor, Inc., a California corporation, having a principal place of business at 700 First Avenue, Sunnyvale, CA 94089 (hereafter the "Assignor"), is the owner by respective Assignment of United States Patent Application identified below (hereafter sometimes called the "Patent Application").

Marvell International Ltd., a corporation of Bermuda, having a place of business at Argyle House, 41A Cedar Avenue, Hamilton, Hm12, Bermuda (hereafter the "Assignee"), desires to acquire all of the rights to the Patent Application and all inventions described and claimed therein.

THEREFORE, in consideration of the sum of One Dollar (\$1.00) or equivalent and other good and valuable consideration paid to it, Assignor hereby sells and assigns to the Assignee the entire right, title and interest in and to the Patent Application identified below, and all inventions described and claimed therein, in any and all Letters Patent therefor, and in any and all reissues, extensions, renewals, reexaminations, divisions and continuations of such applications or Letters Patent to the full end of the term or terms for which such Letters Patent issue, such entire right, title and interest to be held and enjoyed by the above-named Assignee the same as they would have been held and enjoyed by the Assignor had this Assignment and sale not been made.

Marvell Semiconductor, Inc. Case No.	<u>U. S. Serial</u> <u>No.</u>	U. S. Filing Date	Title
MP0601	To Be Assigned	Herewith	Low Voltage Logic Operation Using Higher Voltage Supply Levels

By its undersigned representative, the Assignor agrees

- a. to execute all papers necessary in connection with the Patent Application and any continuing, divisional, reissue, reexamination or corresponding application thereof and also to execute separate Assignment in connection with such application as the Assignee may deem necessary or expedient;
- b. to execute all papers necessary in connection with any interference which may be declared concerning the Patent Application or any continuation, division, reissue or reexamination thereof and to cooperate with the Assignee in every way possible in obtaining evidence and going forward with such interference; and
- c. to perform all affirmative acts which may be necessary to obtain a grant of a valid United States patent to the Assignee on any of the Patent Application and on

Corporate Assignment MP00

page 1/2

PATENT REEL: 017233 FRAME: 0468

P.9/9

Application.

any continuation, division, reissue or reexamination of any of the Patent

The Assignor hereby covenants that it has the full right to convey the entire interest herein assigned, and that it has not executed, and will not execute, any agreement in conflict therewith.

Marvell Semiconductor, Inc., a California corporation, certifies that it is the Assignee of the entire right, title and interest in the Patent Application identified above by virtue of Assignment from the inventors of the Patent Application identified above. Assignment to be recorded in the Patent and Trademark Office as identified below.

Marvell Semiconductor. Inc. Case No.	U. S. Serial No.	Assignment Execution Date
MP0601	To Be Assigned	April 1, 2005

The undersigned has reviewed the documents in the Patent Application identified above and, to the best of undersigned's knowledge and belief, title is in the Assignor identified above.

The undersigned is empowered to sign this certificate on behalf of the Assignor.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

IN WITNESS WHEREOF, executed by the Assignor's undersigned representative on the date following the undersigned's name.

Marvell Semiconductor, Inc.

By:

Vice President Intellectual Property and General Patent Counsel Title:

Date <u>April 4, 2005</u>

Form Letters1 Customer No. 26703

page 2/2

PATENT REEL: 017233 FRAME: 0469

RECORDED: 02/03/2006