

11-21-05

Form PTO-1595

(Rev. 10/02)

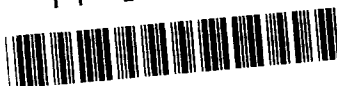
R

OMB No. 0651-0027 (exp. 6/30/2005)

Tab settings ⇌ ⇌ ⇌ ▼

To the Honorable Commissioner of F

11-30-2005



103127635

U.S. DEPARTMENT OF COMMERCE  
U.S. Patent and Trademark Office13260 U.S. P.T.O.  
11/28/2005

112105

1. Name of conveying party(ies):

Chung-Zen Chen

2. Name and address of receiving party(ies)

Name: Elite Semiconductor Memory Technology Inc.

Internal Address: \_\_\_\_\_

Additional name(s) of conveying party(ies) attached? ☐ Yes ☒ No

3. Nature of conveyance:



Assignment



Merger



Security Agreement



Change of Name



Other \_\_\_\_\_

Street Address: No. 23, Industry E. Rd IV,

Science-Based Industrial Park

City: Hsinchu 300

State: \_\_\_\_\_

Zip: \_\_\_\_\_

Taiwan, R.O.C.

Execution Date: 11/21/2005

Additional name(s) & address(es) attached? ☐ Yes ☒ No

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is: 11/21/2005

A. Patent Application No.(s) \_\_\_\_\_

B. Patent No.(s) \_\_\_\_\_

Additional numbers attached? ☐ Yes ☒ No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Joseph A. Powers

Internal Address: \_\_\_\_\_

Street Address: Duane Morris LLP

30 South 17th Street

City: Philadelphia State: PA Zip: 19103

6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 3.41).....\$ 40.00



Enclosed



Authorized to be charged to deposit account

8. Deposit account number:

04-1679

DO NOT USE THIS SPACE

9. Signature.

Joseph A. Powers

11-21-05

Date

Name of Person Signing

00000121 041679

11284281

Total number of pages including cover sheet, attachments, and documents: 3

11/29/2005 DBYRNE

01 FC:8021

40.00 DA

Mail documents to be recorded with required cover sheet information to:

Commissioner of Patents &amp; Trademarks, Box Assignments

Washington, D.C. 20231

PATENT  
REEL: 017246 FRAME: 0528

**ASSIGNMENT AND AGREEMENT**

For value received, I, **Chung-Zen Chen**, hereby sell, assign and transfer to **Elite Semiconductor Memory Technology Inc.**, with its principal place of business located at No. 23, Industry E. Rd IV, Science-Based Industrial Park, Hsinchu 300, Taiwan, Republic of China, and its successors, assigns and legal representatives, the entire right, title and interest, for the United States of America, in and to certain inventions related to **METHOD AND APPARATUS FOR REDUCING STRESS IN WORD LINE DRIVER TRANSISTORS DURING ERASURE** described in an application for Letters Patent of the United States and all the rights and privileges in said application and under any and all Letters Patent and any divisions, continuations, continuations-in-part, reexamination certificates, reissues, and extensions thereof that may be granted in the United States for said inventions; and I also concurrently hereby sell, assign and transfer to Elite Semiconductor Memory Technology Inc. the entire right, title and interest in and to said inventions for all countries foreign to the United States, including all rights of priority arising from the application aforesaid, and all the rights and privileges under any and all forms of protection, including Letters Patent, that may be granted in said countries foreign to the United States for said inventions.

I authorize Elite Semiconductor Memory Technology Inc. to make application for such protection in its own name and maintain such protection in any and all countries foreign to the United States, and to invoke and claim for any application for patent or other form of protection for said inventions, without further authorization from me, any and all benefits, including the right of priority provided by any and all treaties, conventions, or agreements.

I hereby consent that a copy of this assignment shall be deemed a full legal and formal equivalent of any document which may be required in any country in proof of the right of Elite Semiconductor Memory Technology Inc. to apply for patent or other form of protection for said inventions and to claim the aforesaid benefit of the right of priority.

I request that any and all patents for said inventions be issued to Elite Semiconductor Memory Technology Inc. in the United States and in all countries foreign to the United States, or to such nominees as Elite Semiconductor Memory Technology Inc. may designate.

EXPRESS MAIL LABEL NO.: EV782546324US

ATTORNEY DOCKET NO. E1821-00008

I agree that, when requested, I shall, without charge to Elite Semiconductor Memory Technology Inc. but at its expense, sign all papers, and do all acts which may be necessary, desirable or convenient in connection with said applications, patents, or other forms of protection.

(Sole) Inventor

Dated: 11-21, 2005.

Chung-Zen Chen  
**Chung-Zen Chen**

Residence: 2F.-2, No. 50, Daxue Rd.  
Hsinchu 300, Taiwan, Republic of China