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Attorney Docket No. H-1142					
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4. Application number(s) or patent number(s):					
A. Patent Application No.(s) <p style="text-align: center;">10/859,220, filed June 3, 2004</p>			<input type="checkbox"/> This document is being filed together with a new application. B. Patent Registration No.(s)		
Additional number(s) attached <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No					
5. Name and address of party to whom correspondence concerning document should be mailed: Name: MATTINGLY, STANGER, MALUR & BRUNDIDGE, P.C. Internal Address: 1800 Diagonal Road – Suite 370 City <u> Alexandria </u> State: <u> VA </u> Zip <u> 22314 </u>			6. Total number of applications and patents involved 7. Total fee (37 CFR 3.41)..... \$40.00 <input checked="" type="checkbox"/> Enclosed <input checked="" type="checkbox"/> Authorized to be charged to deposit account 8. Deposit account number: <p style="text-align: center;">50-1417</p> (Attach duplicate copy of this page if paying by deposit account)		
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9. Statement and signature. <i>To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.</i>					
Carl I. Brundidge, Reg. No. 29,621 <hr style="width: 100%;"/> Name of Person Signing			 <hr style="width: 100%;"/> Signature		
			February 21, 2006 <hr style="width: 100%;"/> Date		
Total number of pages including cover sheet, attachments, and document: (4)					

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ASSIGNMENT (譲渡証)

As a below named inventor, I hereby declare that:

IN CONSIDERATION of the sum of One Dollar (\$1.00) or the equivalent thereof, and other good and valuable consideration paid to me citizen of Japan by Renesas Technology Corp., a corporation organized under the laws of Japan, located at 4-1, Marunouchi 2-chome, Chiyoda-ku, Tokyo, Japan, receipt of which is hereby acknowledged I do hereby sell and assign to said Renesas Technology Corp., its successors and assigns, all my right, title and interest, in and for the United States of America, in and to

SWITCHING POWER SUPPLY DEVICE AND SEMICONDUCTOR INTEGRATED CIRCUIT

invented by me (if only one is named below) or us (if plural inventors are named below) and described in the application for United States Letters Patent therefor, executed on even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the entire ownership of the said Letters Patent when granted, to be held and enjoyed by said Renesas Technology Corp., its successors, assigns or other legal representatives, to the full end of term for which said Letters Patent may be granted as fully and entirely as the same would have been held and enjoyed by me or us if this assignment and sale had not been made;

And I hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of the above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And I do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said Renesas Technology Corp.,

Signed on the date(s) indicated aside our signatures:

INVENTOR(S)
(発明者フルネームサイン)

Date Signed
(署名日)

1) Tomohiro TAZAWA Tomohiro TAZAWA

11 / March / 2004

2) Shinichi Yoshida Shinichi YOSHIDA

11 / March / 2004

3) _____

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