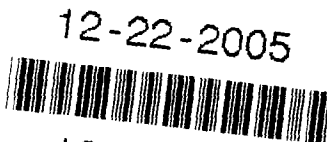


REC



PATENT 103142493

To the Director of the U.S. Patent and Trademark Office: Please record the attached documents or the new address(es) below.

1. Name of conveying party(ies):
Silicon Valley Bank
3003 Tasman Dr HF-154
Santa Clara, CA 95054

Additional name(s) of conveying party(ies) attached? Yes No

3. Nature of conveyance/Execution Date(s):
Execution Date: 12/12/05
 Assignment Merger
 Security Agreement Change of Name
 Joint Research Agreement
 Government Interest Assignment
 Executive Order 9424, Confirmatory License
 Other: Release

2. Name and address of receiving party(ies):
Name: Sonics Inc
Internal Address:
Street Address: 1098 Alta Ave Ste 101
City: Mountain View
State: CA
Country: Zip: 94043
Additional name(s) & address(es) attached? Yes No

OFFICE OF THE
2005 DEC 20 AM 7:28
FINANCE SECTION

4. Application or patent number(s):
A. Patent Application No.(s)
09/977,510 09/634,045 10/288,973 10/146,989
10/138,284 10/163,192 10/418,370 10/330,591
10/138,772 09/977,600 09/802,405 10/293,734
10/286,721 10/138,946 09/977,602
10/418,624 10/285,987 10/144,073

This document is being filed together with a new application.
B. Patent No.(s)
6,725,313 6,578,117
6,785,753 6,683,474
5,948,089
6,330,225
6,182,183

Additional numbers attached? Yes No

5. Name and address of party to whom correspondence concerning document should be mailed:
Name: Silicon Valley Bank
Internal Address: Loan Collateral HF154
Street Address: 3003 Tasman Drive
City: Santa Clara
State: CA Zip: 95054
Phone Number: (408) 654-4042
Fax Number: (408) 654-6313
Email Address: ldc@svbank.com

6. Total number of applications and patents involved: 25
7. Total fee (37 CFR 1.21 (h) & 3.41) \$1,000.00
 Authorized to be charged by credit card
 Authorized to be charged to deposit account
 Enclosed
 None required (government interest not affecting title)

8. Payment Information
a. Credit Card Last 4 Numbers
Expiration Date
b. Deposit Account Number
Authorized User Name

9. Signature: Lana Lowe
Signature

12/12/05
Date

Lana Lowe
Name of Person Signing

Total number of pages including cover sheet, attachments, and documents: 8

Documents to be recorded (including cover sheet) should be faxed to (571) 273-0140, or mailed to:
Mail Stop Assignment Recordation Services, Director of the USPTO, P.O. Box 1450, Alexandria, V.A. 22313-1450

12/21/2005 ECOMPER 00000091 09977510

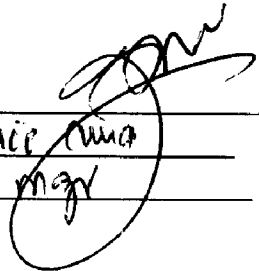
01 FC:8021 1000.00 DP

**RELEASE OF SECURITY AGREEMENT COVERING
INTERESTS IN PATENTS**

Silicon Valley Bank ("Secured Party"), hereby releases its security interest in the interests of **Sonics, Inc.** ("Assignor") in the **patented** works set forth in that certain **Intellectual Property Security Agreement** dated **February 27, 2004** executed by Assignor in favor of Secured Party recorded with the United States Department of Commerce, Patent and Trademark Office on **March 9, 2004** Reel **015056**, Frame(s) **0313**.

Dated: **December 12, 2005**

SILICON VALLEY BANK

By: 
Name: Jamie O'Neil
Title: NSA mgv

INTELLECTUAL PROPERTY SECURITY AGREEMENT

This Intellectual Property Security Agreement is entered into as of February 27, 2004, by and between SILICON VALLEY BANK ("Bank") and Sonics, Inc. ("Grantor").

RECITALS

A. Bank has agreed to make certain advances of money and to extend certain financial accommodation to Grantor (the "Loans") in the amounts and manner set forth in that certain Loan and Security Agreement by and between Bank and Grantor, dated as of the date hereof (as the same may be amended, modified or supplemented from time to time, the "Loan Agreement"; capitalized terms used herein are used as defined in the Loan Agreement). Bank is willing to make the Loans to Grantor, but only upon the condition, among others, that Grantor shall grant to Bank a security interest in certain Copyrights, Trademarks, Patents, and Mask Works to secure the obligations of Grantor under the Loan Agreement.

B. Pursuant to the terms of the Loan Agreement, Grantor has granted to Bank a security interest in all of Grantor's right, title and interest, whether presently existing or hereafter acquired, in, to and under all of the Collateral.

NOW, THEREFORE, for good and valuable consideration, receipt of which is hereby acknowledged, and intending to be legally bound, as collateral security for the prompt and complete payment when due of its obligations under the Loan Agreement, Grantor hereby represents, warrants, covenants and agrees as follows:

AGREEMENT

To secure its obligations under the Loan Agreement, Grantor grants and pledges to Bank a security interest in all of Grantor's right, title and interest in, to and under its Intellectual Property Collateral (including, without limitation those Copyrights, Patents, Trademarks and Mask Works registered in the United States and listed on Schedules A, B, C, and D hereto), and including without limitation all proceeds thereof (such as, by way of example but not by way of limitation, license royalties and proceeds of infringement suits), the right to sue for past, present and future infringements, all rights corresponding thereto throughout the world and all re-issues, divisions continuations, renewals, extensions and continuations-in-part thereof.

This security interest is granted in conjunction with the security interest granted to Bank under the Loan Agreement. The rights and remedies of Bank with respect to the security interest granted hereby are in addition to those set forth in the Loan Agreement and the other Loan Documents, and those which are now or hereafter available to Bank as a matter of law or equity. Each right, power and remedy of Bank provided for herein or in the Loan Agreement or any of the Loan Documents, or now or hereafter existing at law or in equity shall be cumulative and concurrent and shall be in addition to every right, power or remedy provided for herein and the exercise by Bank of any one or more of the rights, powers or remedies provided for in this Intellectual Property Security Agreement, the Loan Agreement or any of the other Loan Documents, or now or hereafter existing at law or in equity, shall not preclude the simultaneous or later exercise by any person, including Bank, of any or all other rights, powers or remedies.

IN WITNESS WHEREOF, the parties have cause this Intellectual Property Security Agreement to be duly executed by its officers thereunto duly authorized as of the first date written above.


GRANTOR:

SONICS, INC.

Address of Grantor:

2440 W. El Camino Real, Suite 600
Mountain View, CA 94040

Attn: Chief Financial Officer

By: 
Title: PRESIDENT & CEO


BANK:

SILICON VALLEY BANK

Address of Bank:

3003 Tasman Drive
Santa Clara, CA 95054-1191

Attn: Loan Services

By: 
Title: SVP

Schedule A

United States Copyrights

Description

Registration/
Application
Number

Registration/
Application
Date

Gray Cary\PA\10344456.6
2501487.74

3

03/08/2004 MON 09:26 [TX/RX NO 5213] 004

PATENT
REEL: 017366 FRAME: 0513

Schedule B

United States Patents and Patent Applications

<u>Description</u>	<u>Registration/ Application Number</u>	<u>Registration/ Application Date</u>
A Fully Pipelined Fixed-Latency Communications System with a Real-Time Dynamic Bandwidth Allocation	5,948,089	9/7/1999
Communications System and Methods with Multi-Level Connection Identification	6,182,183	1/30/2001
Method and Apparatus for Communication Using a Distributed Multiplexed Bus	6,683,474 B2	1/27/2004
Communication System and Method for Different Quality of Service Guarantees For Different Data Flows	6,330,225	12/11/2001
Method and Apparatus for Scheduling Requests Using Ordered Stages of Scheduling Criteria	6,578,117	6/10/2003
Communications System and Methods with Multi-Level Connection Identification	09/721,044	11/21/2000
Logic System with Configurable Interface	09/634,045	8/8/2000
Communication System and Method with non-Blocking Shared Interface	09/802,405	3/29/2001
Method and Apparatus for Response Modes in a Pipelined Environment	09/872,126	6/1/2001
Method and Apparatus for Evaluation an Integrated Circuit Model	10/163,192	6/4/2001
Method and Apparatus for Scheduling A Resource to Meet Quality of Service Restrictions	09/977,602	10/12/2001
Method and Apparatus for Scheduling of Requests to A Dynamic Random Access Memory Device	09/977,510	10/12/2001
Method and Apparatus for Scheduling Requests to a Resource Using a Configurable Threshold	09/977,600	10/12/2001
Scalable Low Bandwidth Multicast Handling in Mixed Core Systems	10/144,073	5/10/2002
Communication System and Method with Configurable Posting Points	10/138,284	5/3/2002
Composing On-Chip Interconnects with Configurable Interface	10/138,946	5/3/2002
A Method and Apparatus for Optimizing Distributed Multiplexed Bus Interconnects	10/146,989	5/15/2002
On-Chip Inter-Network Performance Optimization Using Configurable Performance Parameters	10/138,772	5/3/2002

Gray Cary\PA\10344456.6
7501487.74

4

03/08/2004 MON 09:26 [TX/RX NO 5213] 005

PATENT
REEL: 017366 FRAME: 0514

Method and Apparatus for Error Handling in Networks	10/285,987	10/1/2002
Method and Apparatus for Automatic Configuration of Multiple On-Chip Interconnects	10/330,591	12/27/2002
Methods and Apparatus for Speculative Response Arbitration to Improve System Latency	10/286,721	11/1/2002
Methods and Apparatus for Configurable Address Mapping and Protection Architecture and Hardware for On-chip Systems	10/288,973	11/5/2002
A Method and Apparatus for Decomposing and Verifying Configurable Hardware	10/293,734	10/12/2002
A Method and Apparatus for Round Robin Resource Arbitration	10/418,624	4/18/2003
Various Methods and Apparatuses for Arbitration Among Blocks of Functionality	10/418,370	4/18/2003
Various Method and Apparatuses for Interfacing of a Protocol Monitor to Protocol Checkers and Functional Checkers	Application Number Not Yet Assigned	7/24/2003
Low Power Shared Link Arbitration	Application Number Not Yet Assigned	10/3/2003
Method and Apparatus for Establishing a Quality of Service Model	Application Number Not Yet Assigned	10/31/2003

Gray Cary\PA\10344456.6
2501487.24

5

03/08/2004 MON 09:26 [TX/RX NO 5213] 006

RECORDED: 12/20/2005

PATENT
REEL: 017366 FRAME: 0515