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OMB No. 0651-0027 Exp. 6/30/2005

U.S. DEPARTMENT OF COMMERCE  
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DEC 29 2005

To the Director of the U.S. Patent and Trademark Office: Please record the attached documents or the new address(es) below.

1. Name of conveying party(ies)

John Campbell

2. Name and address of receiving party(ies)

Name: Telairity Semiconductor, Inc.

Internal Address: \_\_\_\_\_

Street Address: 3375 Scott Boulevard, Ste. 300

City: Santa Clara

State: CA

Country: U.S. Zip: 95054

Additional name(s) of conveying party(ies) attached? ☒ Yes ☐ No

3. Nature of conveyance/Execution Date(s):

Execution Date(s) December 13, 2005  
(Campbell, DiGregorio);  
December 16, 2005 (Stevens)

- ☒ Assignment ☐ Merger  
☐ Security Agreement ☐ Change of Name  
☐ Joint Research Agreement  
☐ Government Interest Assignment  
☐ Executive Order 9424, Confirmatory License  
☐ Other \_\_\_\_\_

Additional name(s) & address(es) attached? ☐ Yes ☒ No

4. Application or patent number(s):

☐ This document is being filed together with a new application.

A. Patent Application No.(s)

11/237,304

B. Patent No.(s)

01/05/2006 BYRNE 00000206 201430 11237304

01 FC:802

40.00 BA

Additional numbers attached? ☐ Yes ☒ No

5. Name and address to whom correspondence concerning document should be mailed:

Name: Robert C. Colwell

Internal Address: \_\_\_\_\_

TOWNSEND AND TOWNSEND AND CREW LLP

Street Address: Two Embarcadero Center,

Eighth Floor

City: San Francisco

State: California Zip: 94111-3834

Phone Number: (415) 576-0200

Fax Number: (415) 576-0300

Email Address: @townsend.com

6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 1.21(h) & 3.41) \$ 40.00

- ☐ Authorized to be charged by credit card  
☒ Authorized to be charged to deposit account  
☐ Enclosed  
☐ None required (government interest not affecting title)

8. Payment Information

a. Credit Card Last 4 numbers \_\_\_\_\_

Expiration Date \_\_\_\_\_

b. Deposit Account Number 20-1430

Authorized User Name Townsend and

Townsend and Crew LLP

9. Signature:

Robert C. Colwell

Name of Person Signing

Atty. Reg. No. 27,431

December 27, 2005

Date

Total number of pages including cover sheet, attachments, and documents:

3

Documents to be recorded (including cover sheet) should be faxed to (703) 306-5995, or mailed to:  
Mail Stop Assignment Recordation Services, Director of the USPTO, P.O.Box 1450, Alexandria, VA 22313-1450

PATENT  
REEL: 017416 FRAME: 0426

1. Additional name(s) of conveying party(ies):  
(Continued from Page 1)

Kim R. Stevens  
Luigi DiGregorio

2. Additional name(s) and address(es) of receiving party(ies):  
(Continued from Page 1)

3. Additional application number(s) or patent number(s):  
(Continued from Page 1)

A. Patent Application No.(s)

B. Patent No.(s)

60667472 v1

**ASSIGNMENT OF PATENT APPLICATION**

JOINT

WHEREAS, the below-listed individuals, herein referred to as "Assignors," are the inventors of the invention described and set forth in the following application for United States Letters Patent:

**Power Grid Layout Techniques on Integrated Circuits**

Filed September 27, 2005, as U.S. Application No. 11/237,304


WHEREAS, **Telairity Semiconductor, Inc.**, a corporation of the State of California, located at 3375 Scott Boulevard, Suite 300, Santa Clara, California 95054, hereinafter referred to as "Assignee," is desirous of acquiring an interest in the invention and application and in any U.S. Letters Patent and Registrations which may be granted on the same;

For good and valuable consideration, receipt of which is hereby acknowledged by Assignors, Assignors have assigned, and by these presents do assign to Assignee all right, title and interest in and to the invention and application and to all foreign counterparts (including patent, utility model and industrial designs), and in and to any Letters Patent and Registrations which may hereafter be granted on the same in the United States and all countries throughout the world, and to claim the priority from the application as provided by the Paris Convention. The right, title and interest is to be held and enjoyed by Assignee and Assignee's successors and assigns as fully and exclusively as it would have been held and enjoyed by Assignors had this Assignment not been made, for the full term of any Letters Patent and Registrations which may be granted thereon, or of any division, renewal, continuation in whole or in part, substitution, conversion, reissue, prolongation or extension thereof.

Assignors further agree that they will, without charge to Assignee, but at Assignee's expense, (a) cooperate with Assignee in the prosecution of U.S. Patent applications and foreign counterparts on the invention and any improvements, (b) execute, verify, acknowledge and deliver all such further papers, including patent applications and instruments of transfer, and (c) perform such other acts as Assignee lawfully may request to obtain or maintain Letters Patent and Registrations for the invention and improvements in any and all countries, and to vest title thereto in Assignee, or Assignee's successors and assigns.

IN TESTIMONY WHEREOF, Assignors have signed their names on the dates indicated.

Date: December 13, 2005

  
\_\_\_\_\_  
John Campbell

Date: 12/16/05

  
\_\_\_\_\_  
Kim R. Stevens

Date: Dec. 13-2005

  
\_\_\_\_\_  
Luigi DiGregorio