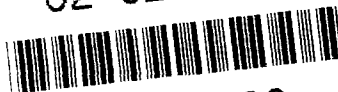


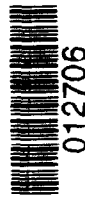
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Ying Jyh YEH (12/29/2005) and Chung Yin FANG
(12/29/2005)

Additional name(s) of conveying party(ies) attached? ☐ Yes ☒ No

2. Name and address of receiving party(ies)

Name: Silicon Integrated Systems Corp.

Internal Address: _____

Street Address: _____

NO. 180, KUNG TAO 5 RD., SEC. 2

City: Hsin Chu City

State: _____

Country: Taiwan, R.O.C. Zip: _____

Additional name(s) & address(es) attached: ☐ Yes ☒ No

3. Nature of conveyance/Execution Date(s):

Execution Date(s): in parentheses after inventor name

☒ Assignment ☐ Merger ☐ Change of Name

☐ Security Agreement ☐ Joint Research Agreement

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☐ Executive Order 9424, Confirmatory License

☐ Other _____

4. Application or patent number(s):

A. Patent Application No.(s)

NEW

☒ This document is being filed together with a new application.

B. Patent No.(s)

Additional numbers attached? ☐ Yes ☒ No

5. Name and address to whom correspondence concerning document should be mailed:

Name: Joe McKinney Muncy
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6. Total number of applications and patents involved:

1

7. Total fee (37 CFR 1.21(h) & 3.41) \$ 40.00

☐ Authorized to be charged by credit card

☐ Authorized to be charged to deposit account

☒ Enclosed

☐ None required (government interest not affecting title)

8. Payment Information

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9. Signature:

Signature

January 27, 2006

Date

Joe McKinney Muncy - 32,334

Name of Person Signing

#43,368

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Birch, Stewart, Kolasch & Birch, LLP

KM/sns

PATENT
REEL: 017511 FRAME: 0495

ASSIGNMENT OF PATENT APPLICATION

For good and valuable consideration, receipt of which hereby acknowledged,

I/We,

- (a) YING JYH YEH
(b) CHUNG YIN FANG

residing at

- (a) NO. 21-2, KANG CHENG RD., SHIAO KANG CHU KANG KOU LI, KAO HSIUNG CITY,
TAIWAN, R.O.C.
(b) 2F-4, NO. 68, TA HSUEH RD., HSIN CHU CITY, TAIWAN, R.O.C.

assign and transfer to: **Silicon Integrated Systems Corp.**

a corporation organized under the law of Taiwan and having a place of business at: **NO. 180, KUNG TAO 5 RD.,
SEC. 2, HSIN CHU CITY, TAIWAN, R.O.C.**

its successors and assigns, my entire right, title and interest, including the right of priority, in, to and under an application for Letters Patent of the United States entitled: **SYSTEM AND METHOD FOR DETECTING PROCESSING SPEED OF INTEGRATED CIRCUIT**

including divisions, reissues, continuations and extensions thereof, and all rights of priority without further authorization from me; I agree to execute all papers useful in connection with said United States application, and generally to do everything possible to aid said assignee, its successors, assigns and nominees, at their request and expense, in obtaining and enforcing patents for said invention in United States; and I request the Commissioner of Patents and Trademarks to issue all patents granted for said invention to the above-named assignee, its successors and assigns.

Joint Inventor (a)	YING JYH YEH		
Inventor's Signature	<i>Ying-Jyh Yeh</i>	Date	27 Dec. 05
Residence	TAIWAN	Citizenship	R.O.C.

Joint Inventor (b)	CHUNG YIN FANG		
Inventor's Signature	<i>Chung Yin Fang</i>	Date	27 Dec. 05
Residence	TAIWAN	Citizenship	R.O.C.

Joint Inventor (c)			
Inventor's Signature		Date	
Residence		Citizenship	

Joint Inventor (d)			
Inventor's Signature		Date	
Residence		Citizenship	

05P029-US