

02-06-2006



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PATENT

To the Director of the U.S. Patent and Trademark Office: Please record the attached documents or the new address(es) below.

1. Name of conveying party(ies)

CIRRUS LOGIC, INC.

2. Name and address of receiving party(ies)

Name: MAGNUM SEMICONDUCTOR, INC.

Internal Address:

Additional name(s) of conveying party(ies) attached? ☐ Yes ☒ No

3. Nature of conveyance/Execution Date(s):

Execution Date(s) June 30, 2005

☒ Assignment

☐ Merger

☐ Security Agreement

☐ Change of Name

☐ Joint Research Agreement

☐ Government Interest Assignment

☐ Executive Order 9424, Confirmatory License

☐ Other

Street Address: 46831 Lakeview Blvd.

City: Fremont

State: California

Country: United States Zip: 94538

Additional name(s) & address(es) attached? ☐ Yes ☒ No

4. Application or patent number(s):

☐ This document is being filed together with a new application.

A. Patent Application No.(s)

10/871,287; 10/828,914; 10/825,346; 10/853,692; 11/167,756;
11/167,877; 11/167,682; 10/706,014

B. Patent No.(s)

Additional numbers attached? ☐ Yes ☒ No

5. Name and address to whom correspondence concerning document should be mailed:

Name: James J. Murphy

Internal Address:

Street Address: 1700 Pacific Avenue, Suite 3300

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6. Total number of applications and patents involved:

7. Total fee (37 CFR 1.21(h) & 3.41) \$ 320.00

☐ Authorized to be charged by credit card

☐ Authorized to be charged to deposit account

☒ Enclosed

☐ None required (government interest not affecting title)

8. Payment Information

a. Credit Card Last 4 Numbers

Expiration Date

b. Deposit Account Number 20-0821

Authorized User Name Thompson & Knight

9. Signature:

Signature

Date

James J. Murphy

Name of Person Signing

Total number of pages including cover sheet, attachments, and documents:

31

Documents to be recorded (including cover sheet) should be faxed to (571) 273-0140, or mailed to:
Mail Stop Assignment Recordation Services, Director of the USPTO, P.O. Box 1450, Alexandria, V.A. 22313-1450

PATENT ASSIGNMENT

THIS PATENT ASSIGNMENT (this "Assignment") is made as of June 30, 2005, by Cirrus Logic, Inc., a Delaware corporation ("ASSIGNOR"), to Magnum Semiconductor, Inc., a Delaware corporation ("ASSIGNEE").

RECITALS

A. ASSIGNOR and ASSIGNEE have entered into an Asset Purchase Agreement dated as of May 20, 2005, as amended (the "Purchase Agreement"). All capitalized terms used herein but not otherwise defined shall have the meanings set forth in the Purchase Agreement.

B. ASSIGNOR owns the unpublished patent application filed or that has yet to be filed with the United States Patent and Trademark Office set forth on Exhibit A hereto (the "Patent Application").

C. Pursuant to the Purchase Agreement, ASSIGNOR desires to assign to ASSIGNEE all of ASSIGNOR's right, title and interest in and to the Patent Application.

AGREEMENT

NOW, THEREFORE, in consideration of the foregoing premises, the mutual covenants and agreements contained in the Purchase Agreement and the covenants and agreements in this Assignment and to induce ASSIGNEE to consummate the transactions contemplated by the Purchase Agreement, ASSIGNOR agrees as follows:

1. ASSIGNOR does hereby sell, transfer, convey, assign and deliver to ASSIGNEE all right, title and interest in and to the Patent Application, and any patents that may issue therefrom, including any foreign counterparts, divisions, continuations or reissues thereof, the same to be held by ASSIGNEE for ASSIGNEE's own use and enjoyment, and for the use and enjoyment of ASSIGNEE's successors, assigns and other legal representatives, as fully and entirely as the same would have been held and enjoyed by ASSIGNOR if this Assignment had not been made; together with all claims for damages by reason of past infringements of the Patent Application, along with the right to sue for and collect such damages for the use and benefit of ASSIGNEE and its successors, assigns and other legal representatives.

2. ASSIGNOR hereby authorizes and requests the Commissioner of Patents and Trademarks of the United States to issue the same to ASSIGNEE and its successors, assigns and other legal representatives in accordance with the terms of this instrument.

3. ASSIGNOR hereby covenants with ASSIGNEE and the successors and assigns of ASSIGNEE that, from time to time after the date hereof, ASSIGNOR will promptly execute and deliver to ASSIGNEE or shall promptly procure the execution and delivery of any and all such instruments of sale, transfer, conveyance, assignment and delivery, consents, assurances, powers of attorney and other instruments as may reasonably be requested by ASSIGNEE in order to vest

in ASSIGNEE all of ASSIGNOR's right, title and interest in and to the Application and carry out the purpose and intent of this Assignment and the Purchase Agreement.

[signature page to follow]

IN WITNESS WHEREOF, ASSIGNOR has executed this Assignment as of the date first set forth above.

CIRRUS LOGIC, INC.

By: David French

Name: David French

Title: CEO and President

ACKNOWLEDGMENT

State of Texas)
) ss:
County of Travis)

On this 30th day of June, 2005, before me, the undersigned, personally appeared David French personally known to me or proved to me on the basis of satisfactory evidence to be the person who executed this instrument on behalf of the corporation named herein, and acknowledged that s/he executed it in such representative capacity.

IN WITNESS WHEREOF, I have hereunto set my hand and official seal.

Karen Klein
Notary Public

My Commission Expires on June 11, 2007



[SIGNATURE PAGE TO PATENT ASSIGNMENT]

EXHIBIT A

Patent Application

<u>Title</u>	<u>Inventor(s)</u>	<u>Filing Date</u>	<u>Serial No.</u>
End-User Configurable Digital Versatile Disk Menus and Methods for Generating the Same	Wang, Samuel C. Jeanson, Matthieu Alves, Jeremy V.	12 November 2003	10/706,014

SANFRAN 109334 v2 (2K)

EXHIBIT A

Patent Application

Title	Inventor(s)	Filing Date	Serial No.
Versatile Video Interface Architecture for Video Compression and Decompression	Jeng, Fure-Ching Chen, Ting-Chung	28 June 2000	09/607,086

SANFRAN 109334 v2 (2K)

EXHIBIT A**Patent Application**

<u>Title</u>	<u>Inventor(s)</u>	<u>Filing Date</u>	<u>Serial No.</u>
Apparatus for Single-Chip MPEG2 Audio/Video Encoder and Decoder	Chen, Cheng-Tie (C.T.) Chen, Ting-Chung Jeng, Fure-Ching Cheng, Hown Cheng, Yung-Li Mahmood, Akhtar Huang, Chia-Chun Feng, Chenhui Lin, Fu-Huei	5 January 2001	09/755,767

EXHIBIT A

Patent Application

<u>Title</u>	<u>Inventor(s)</u>	<u>Filing Date</u>	<u>Serial No.</u>
Method and Apparatus for Real-Time Audio/Video Recording on Compact Disc Media	Smolenski, Michael J.	4 August 2000	09/632,472

EXHIBIT A

Patent Application

<u>Title</u>	<u>Inventor(s)</u>	<u>Filing Date</u>	<u>Serial No.</u>
Computationally Simple Strategy for Block Switching in Audio Compression Algorithms	Abdel-Hafez, Khader Srinivasan, Pramila	15 November 2000	09/713,427

SANFRAN 109334 v2 (2k)

EXHIBIT A

Patent Application

<u>Title</u>	<u>Inventor(s)</u>	<u>Filing Date</u>	<u>Serial No.</u>
Design of Digital Video Recorder/Player	Frankenberger, David J. Smolenski, Michael J. Su, John Walker, John Ren, Victor Konstantinides, Konstantinos	5 October 2000	09/680,564

SANFRAN 109314 v2 (2K)

EXHIBIT A

Patent Application

<u>Title</u>	<u>Inventor(s)</u>	<u>Filing Date</u>	<u>Serial No.</u>
Method and Apparatus for Recording Real-Time Audio/Video Information Onto Recordable Compact Disc Drives	Smolenski, Michael J. Su, John	15 December 2000	09/738,135

SANFRAN 1 09334 v2 (2K)

EXHIBIT A

Patent Application

<u>Title</u>	<u>Inventor(s)</u>	<u>Filing Date</u>	<u>Serial No.</u>
Fast Algorithms for Computing Transcendental Functions for Fixed-Point Digital Signal Processors	Zhou, Linjun	15 May 2001	09/858,839

SANFRAN 109334 v2 (2K)

EXHIBIT A

Patent Application

<u>Title</u>	<u>Inventor(s)</u>	<u>Filing Date</u>	<u>Serial No.</u>
Architecture for Hardware-Assisted Context Switching Between Register Group Dedicated to Time-Critical or Non-Time Critical Tasks Without Saving State	Cheng, Hown Feng, Chenhui	27 July 2001	09/917,312

EXHIBIT A

Patent Application

<u>Title</u>	<u>Inventor(s)</u>	<u>Filing Date</u>	<u>Serial No.</u>
Audio Clocking in Video Applications	Melanson, John L. Rygh, Mark P.	28 May 2004	10/856,436

EXHIBIT A

Patent Application

<u>Title</u>	<u>Inventor(s)</u>	<u>Filing Date</u>	<u>Serial No.</u>
Circuits, Systems and Methods for Real-Time De-Shuffling of Shuffled Audio Data	Mahmood, Akhtar Chen, Cheng-Tie (C.T.) Chen, Ting-Chung	17 June 2004	10/871,287

SANFRAN 109334 v2 (2K)

EXHIBIT A

Patent Application

<u>Title</u>	<u>Inventor(s)</u>	<u>Filing Date</u>	<u>Serial No.</u>
Dual-Mode Transform Calculation Systems and Methods	Ho, Kin Ning Chen, Ting-Chung Chen, Cheng-Tie (C.T.)	21 April 2004	10/828,914

EXHIBIT A

Patent Application

<u>Title</u>	<u>Inventor(s)</u>	<u>Filing Date</u>	<u>Serial No.</u>
Methods and Apparatus for Error Detection and Mitigation in Data Decoding Systems	Chen, Ting-Chung Chen, Cheng-Tie (C.T.) Ho, Kin Ning Ahmed, Syed Mahmood Mahmood, Akhtar	15 April 2004	10/825,346

SANFRAN 109334 v2 (2K)

EXHIBIT A

Patent Application

<u>Title</u>	<u>Inventor(s)</u>	<u>Filing Date</u>	<u>Serial No.</u>
Methods and Apparatus for Color Component Sub-Sampling and Up-Sampling Which Preserve Color Integrity During Repeated Video Encoding-Decoding Operations	Chen, Ting-Chung Huang, Chia-Chun	25 May 2004	10/853,692

EXHIBIT A

Patent Application

<u>Title</u>	<u>Inventor(s)</u>	<u>Filing Date</u>	<u>Serial No.</u>
Method and System for Switching Between Video Modes in Consumer Electronics	Douangphachanh, Phanthavong	25 August 2004	10/926,060

SANFRAN 109334 v2 (2K)

EXHIBIT A

Patent Application

<u>Title</u>	<u>Inventor(s)</u>	<u>Filing Date</u>	<u>Serial No.</u>
Recovery of Real Time Video Data After Power Loss	Wang, Samuel C.	20 July 2004	10/894,340

SANFRAN 109334 v2 (21Q)

EXHIBIT A

Patent Application

Telecine Conversion Detection for Progressive Scan Playback	Shah, Anup U.	24 September 2004	10/948,791
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SANFRAN 109314 v2 (2K)

EXHIBIT A

Patent Application

Optimal Buffering and Scheduling Strategy for Smooth Reverse in a DVD Player or the Like	Agrahara, Aravind C.	24 September 2004	10/948,745
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SANFRAN 109324 v2 (2K)

EXHIBIT A

Patent Application

Intelligent Caching Scheme for Streaming File Systems	Mahashin, Daniel S. Jeanson, Matthieu Su, John Alves, Jeremy V.	25 March 2005	11/089,459
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SANFRAN 109334 v2 (2K)

EXHIBIT A

Patent Application

Synchronized Streaming Layer with Presentation Layer	Agrahara, Aravind C.	25 March 2005	11/089,458
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SANFRAN 109334 v2 (2K)

EXHIBIT A

Patent Application

Sequential Decoding of Progressive Coded JPEGs	Kaithakapuzha, Suresh V.	3 November 2004	10/980,250
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SAMTRAN 109314 v2 (2K)

EXHIBIT A

Patent Application

Improvements in DVD Player/Recorder and Consumer Audio/Video Products	Wang, Samuel C. Douangphachanh, Phanthavong Agrahara, Aravind C. Shah, Anup U. Ucar, Haluk Kaithakapuzha, Sukesh V. Mahashin, Daniel S. Jeanson, Matthieu Su, John Alves, Jeremy V.	6 July 2004	60/585,072
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SANFRAN 109334 v2 (2K)

EXHIBIT A

Patent Application

Circuits and Methods for Detecting 2:2 Encoded Video and Systems Utilizing the Same	Pillay, Sanjay R Bounds, Brian F. Gallagher, William L. "Lynn"	27 June 2005	Atty Docket No. 1545-CV
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SANFRAN 109334 v2 (2K)

EXHIBIT A

Patent Application

Circuits and Methods for Deinterlacing Video Display Data and Systems Using the Same	Pillay, Sanjay R. Bounds, Brian F. Gallagher, William L. "Lynn"	27 June 2005	Atty Docket No. 1546-CV
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SANFRAN 109334 v2 (2K)

EXHIBIT A

Patent Application

Systems and Methods for Detecting a Change in a Sequence of Interlaced Data Fields Generated From a Progressive Scan Source	Pillay, Sanjay R Bounds, Brian F. Gallagher, William L. "Lynn"	27 June 2005	Atty Docket No. 1547-CV
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SANFRAN 109334 v2 (2K)

EXHIBIT A

Patent Application

Technique for Minimizing Memory Bandwidth Used for Motion Compensation	Simpson, Miles Bell, Daniel J. Rygh, Mark P.	Unfiled	Atty Docket No. 1525-CV
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SANFRAN 109334 v2 (2E)

EXHIBIT A

Patent Application

Motion Based Video Noise Reduction with Edge Enhancement	Pillay, Sanjay R Bounds, Brian F. Gallagher, William L. "Lynn"	Unfiled	Atty Docket No. 1566-CV
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\$ANFRAN 109334 v2 (2K)

EXHIBIT A

Patent Application

Edge Detection for Directional Interpolation	Gallagher, William L. "Lynn" Bounds, Brian F. Pillay, Sanjay R	Unfiled	Atty Docket No. 1572-CV
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SANFRAN 109334 v2 (2X)