## PATENT ASSIGNMENT

Electronic Version v1.1 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

## **CONVEYING PARTY DATA**

Name	Execution Date
Ralf LERNER	12/08/2005
Uwe ECKOLDT	12/08/2005

## **RECEIVING PARTY DATA**

Name:	X-Fab Semiconductor Foundries AG	
Street Address:	Haarbergstrasse 67	
City:	Erfurt	
State/Country:	GERMANY	
Postal Code:	99097	

#### PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	10530343

## **CORRESPONDENCE DATA**

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ATTORNEY DOCKET NUMBER:	60291.000034
NAME OF SUBMITTER:	Daniel G. Vivarelli, Jr.

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**REEL: 017642 FRAME: 0118** 

PATENT REEL: 017642 FRAME: 0119

2

Total number of pages including co

sheet, attachments, and document:

Daniel G. Vivarelli, Jr.

Name of Person Signing

# **Confirmatory assignment**

#### Ralf LERNER

Josef-Albers-Strasse 20 99085 Erfurt, Germany

and

#### **Uwe ECKOLDT**

Im Dorfe 60 99448 Hohenfelden, Germany

have invented

RECORDED: 05/19/2006

"Trench Isolation in Substrate Wafers including Logic Semiconductor Devices and Power Semiconductor Devices"

for which PCT application PCT/DE2003/003332 was filed on October 8, 2003 continued in USA as 10/530,343.

By way of "claiming" the inventors have transferred said invention under German law of inventors, Section 9 ArbNErfG to the assignee

## X-Fab Semiconductor Foundries AG

Haarbergstrasse 67 99097 Erfurt, Germany

The inventors have acquired a right of monetary compensation under Section 9,12 ArbNErfG.

We do herby authorize and request the Director of the United States Patent and Trademark Office to issue any and all letters patent which may be granted upon said United States applications, or upon said improvements or any parts thereof when granted, to said Assignee.

In Witness whereof, we have hereunto set our hand and seal.

12.8.05		
Date	Ralf Lerner	
12.8.00	u. Foliall	
Date	Uwe Eckoldt	

PATENT REEL: 017642 FRAME: 0120