

PATENT ASSIGNMENT

Electronic Version v1.1

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SUBMISSION TYPE:

NEW ASSIGNMENT

NATURE OF CONVEYANCE:

ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
NEC Corporation	04/25/2006

RECEIVING PARTY DATA

Name:	Quantum Cores, LLC
Street Address:	2711 Centerville Road
Internal Address:	Suite 400
City:	Wilmington
State/Country:	DELAWARE
Postal Code:	19808

PROPERTY NUMBERS Total: 10

Property Type	Number
Patent Number:	5036296
Patent Number:	5309467
Patent Number:	5421954
Patent Number:	5425042
Patent Number:	6117753
Patent Number:	6181454
Patent Number:	6359908
Patent Number:	6314133
Patent Number:	6292510
Patent Number:	5442227

CORRESPONDENCE DATA

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PATENT

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ATTORNEY DOCKET NUMBER:

IVQC-0001

NAME OF SUBMITTER:

/jennifer j. keeler/

Total Attachments: 5

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Assignment

To whom it may concern:

Whereas, NEC Corporation, a Japanese corporation, having a place of business at 7-1, Shiba 5-chome, Minato-ku, Tokyo 108-8001, Japan, hereinafter called Assignor, is the owner and assignee of record of all right, title, and interest in and to the patents listed on the attached Schedule A ("Listed Assets"); and

Whereas, Quantum Cores, LLC, a limited liability company incorporated under the laws of Delaware, having an address at 2711 Centerville Road, Suite 400, Wilmington, DE 19808, U.S.A., hereinafter called Assignee, desires to acquire the Assignor's entire right, title, and interest in the Listed Assets;

Now therefore, for valuable consideration, receipt whereof is hereby acknowledged;

Assignor hereby sells, assigns, transfers and conveys to Assignee, its successors and assigns, the entire right, title and interest in and to all of the Listed Assets, including (i) all rights to recover for past, present and future infringement of the Listed Assets, (ii) all rights in and all rights to obtain reissues, reexaminations, extensions, continuations, continuations in part, continuing prosecution applications, divisions of the Listed Assets in the United States of America and Germany, and (iii) causes of action (whether currently pending, filed, or otherwise) and other enforcement rights under the Listed Assets, including, without limitation, all rights under the Listed Assets and/or under or on account of any of the Listed Assets to (a) damages, (b) injunctive relief, and (c) any other remedies of any kind for past, current and future infringement.

IN WITNESS WHEREOF, Assignor has executed this Assignment by proper persons duly authorized.

NEC CORPORATION

By:

Name:

Title: General Manager

Date: 25 / 4 / 2006

(Signature certification *MUST* be attached)

Signature Verified by
The Tokyo
Chamber of Commerce & Industry



Kazuhiro Omori

APR. 26. 2006

etc X-000168

Schedule A

US Patents

<u>Patent No.</u>	<u>Issue Date (Y/M/D)</u>	<u>Serial No.</u>	<u>Filing Date (Y/M/D)</u>	<u>Title; First Listed Inventor</u>
6181454	01/01/30	09/064505	98/04/23	ADAPTIVE THRESHOLD DIGITAL RECEIVER CIRCUIT FOR OPTICAL SIGNALS; [First Listed Inventor: Takeshi Nagahori]
6359908B1	02/03/19	09/136380	98/08/19	FRAME SYNCHRONOUS CIRCUIT CONTRIBUTING TO SDH SIGNAL; [First Listed Inventor: Masaaki Soda]
5309467	94/05/03	07/957798	92/10/08	SEMICONDUCTOR LASER WITH INGAAS OR INGAASP ACTIVE LAYER; [First Listed Inventor: Tomoji Terakado]
5421954	95/06/06	07/990192	92/12/14	PROCESS OF PATTERNING COMPOUND SEMICONDUCTOR FILM IN HALOGEN CONTAINING ETCHING; [First Listed Inventor: Norikazu Takado]
5442227	95/08/15	08/180058	94/01/11	FIELD EFFECT TRANSISTOR HAVING A LOW THRESHOLD VOLTAGE SHIFT; [First Listed Inventor: Muneo Fukaishi]
5425042	95/06/13	08/259436	94/06/14	REFRACTIVE INDEX

<u>Patent No.</u>	<u>Issue Date (Y/M/D)</u>	<u>Serial No.</u>	<u>Filing Date (Y/M/D)</u>	<u>Title; First Listed Inventor</u>
				CONTROL OPTICAL SEMICONDUCTOR DEVICE [First Listed Inventor: Masaaki Nido]
6117753	00/09/12	08/565984	95/12/01	METHOD OF MANUFACTURING COMPOUND SEMICONDUCTOR INTEGRATED CIRCUIT; [First Listed Inventor: Kiichi Hamamoto]
5036296	91/07/30	07/582147	90/09/13	FREQUENCY TRACKING CIRCUIT USING SAMPLES EQUALIZED AT DIFFERENT SAMPLING INSTANTS OF SAME CLOCK PERIOD; [First Listed Inventor: Shousei Yoshida]
6314133B1	01/11/06	09/243565	99/02/03	AUTOMATIC EQUALIZER CAPABLE OF SURELY SELECTING A SUITABLE SAMPLE TIMING A METHOD FOR GENERATING SAMPLING CLOCK USED FOR THE SAMPLE TIMING AND A RECORDING MEDIUM USABLE IN CONTROL OF THE AUTOMATIC EQUALIZER; [First Listed Inventor: Yoshikazu Kakura]
6292510B1	01/09/18	09/253244	99/02/19	AUTOMATIC EQUALIZATION METHOD AND AUTOMATIC EQUALIZER; [First Listed Inventor: Yoshikazu Kakura]

German Patents

<u>Patent No.</u>	<u>Issue Date</u> (Y/M/D)	<u>Serial No.</u>	<u>Filing Date</u> (Y/M/D)	<u>Title,</u> <u>First Inventor</u>
69804044 T2 (EP 0874460)	02/03/06	98107401.6	98/04/23	ADAPTIVE THRESHOLD DIGITAL RECEIVER CIRCUIT FOR OPTICAL SIGNALS; [First Listed Inventor: Takeshi Nagahori]