

PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
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Name	Execution Date
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Property Type	Number
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Patent Number:	6822920
Patent Number:	6847573
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Total Attachments: 3

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ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, Silicon7, Inc. having offices at Sang Woo Bldg., 5th Floor, 513-2 Yatap-dong, Bundang-ku, Seongnam-si, Kyeongki-do, Korea 463-839, ("*Assignor*"), does hereby sell, assign, transfer and convey unto Devar-Toi Foundation LLC, a Delaware corporation, having a registered agent address at 2711 Centerville Road, Suite 400, Wilmington, DE 19808 ("*Assignee*") or its designees, all of Assignor's right, title and interest in and to the patent applications and patents listed below, any patents, registrations, or certificates of invention issuing on any patent applications listed below, the inventions disclosed in any of the foregoing, any and all counterpart United States, international and foreign patents, applications and certificates of invention based upon or covering any portion of the foregoing, and all reissues, re-examinations, divisionals, renewals, extensions, provisionals, continuations and continuations-in-part of any of the foregoing (collectively "*Patent Rights*");

<u>Patent No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title and Inventor(s)</u>
US 6,392,958 B1	USA	Apr. 02, 2001	Title: Asynchronous SRAM compatible memory device using DRAM cell and method for driving the same Inventor: Sun Hyoung Lee
US 6,822,920 B2	USA	Aug. 12, 2003	Title: SRAM-compatible memory device employing DRAM cells Inventors: In Sun Yoo, Sun Hyoung Lee, Dong Woo Shin
US 6,847,573 B2	USA	Jun. 21, 2003	Title: Synchronous SRAM-compatible memory device including DRAM array with internal refresh Inventors: Sun Hyoung Lee, In Sun Yoo, Dong Woo Shin
204944	Taiwan	Jan. 20, 2001	Title: Asynchronous SRAM compatible memory device using DRAM cell and method for driving the same Inventor: Sun Hyoung Lee, Seoul (KR)
220525	Taiwan	Aug. 05, 2003	Title: SRAM-compatible memory device employing DRAM cells Inventors: In Sun Yoo, Sun Hyoung Lee, Dong Woo Shin
222067	Taiwan	Jun. 20, 2003	Title: Synchronous SRAM-compatible memory device and method of driving the same Inventors: Sun Hyoung Lee, In Sun Yoo, Dong Woo Shin
0367690	Korea	Dec. 04, 2000	Title: Asynchronous SRAM using DRAM cell and operating method thereof Inventor: Sun Hyoung Lee, Seoul (KR)
0481819	Korea	Aug. 27, 2002	Title: SRAM-compatible and synchronous memory device being controlled by a signal, the signal activating in chip disable period Inventors: In Sun Yoo, Sun Hyoung Lee, Dong Woo Shin
0481818	Korea	Jul. 24, 2002	Title: SRAM-compatible and burst accessible synchronous memory device using DRAM cell and operating method thereof Inventors: Sun Hyoung Lee, In Sun Yoo, Dong Woo Shin

Document Id:

Assignor represents, warrants and covenants that: (i) it is the sole owner, assignee and holder of record title to the Patent Rights identified above, (ii) it has obtained and properly recorded previously executed assignments for all patent applications and patents identified above as necessary to fully perfect its rights and title therein in accordance with governing law and regulations in each respective jurisdiction, and (iii) it has full power and authority to make the present assignment. Assignor shall indemnify and hold harmless Assignee for any breach of the foregoing.

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The terms and conditions of this Assignment shall inure to the benefit of Assignee, its successors, assigns and other legal representatives, and shall be binding upon Assignor, its successor, assigns and other legal representatives.

