FORM PTO-1595 261/6	7	2-27-2006 	U.S. DEPARTMENT OF COMMERCE Patent and Trademark Office
OMB No. 0651-0011 (exp. 4/94)			Attorney Docket No. 400.401US01
To the Honorable Commissioner of Pate	ents and	03185 <u>311</u>	ginal documents or copy thereof.
1. Name of conveying party(ies):  2. Name and address of receiving party(ies):			
Seiichi Aritome (January 30, 2006)			
		Name: Micron Technology, Inc.	
		Address: 8000 S Boise,	South Federal Way Idaho 83716
Additional name(s) of conveying party(ics) attached? _Yes _X No			FEB 9 : 2008
3. Nature of conveyance:			
X Assignment Merger		Additional name(s) & address(es) attached?Yes _X_No	
Security Agreement Change of Name			<u>.</u>
Other			
Execution Date: <u>See Above</u>		ı	
4. Application number(s) or patent number(s);			
If this document is being filed together with a new application, the execution date of the application is: 01/30/2006			
A. Patent Application No(s). 11 355,830 B. Patent No(s).			
Title: PROGRAMMING METHOD TO REDUCE GATE COUPLING INTERFERENCE FOR NON-VOLATILE			
MEMORY			
Additional numbers attached? Yes X			
5. Name and address of party to who concerning document should be maile		6. Total numbe	r of applications and patents involved: <u>1</u>
Name: Andrew C. Walsoth Address: Leffert Jay & Polglaze, P.A.		7. Total fee (37	CFR §3.41): \$ <u>40.00</u>
P.O. Box 581009 Minneapolis, MN 55458-10	09	_X_Enclo	sed
- -		Author	ized to be charged to deposit account
8. Please overpaym		8. Please charge overpayments to	e any additional fees or credit any Deposit account number: <u>501373</u>
DO NOT USE THIS SPACE			
9. Statement and signature.			·
To the best of my knowledge and belief, copy of the original document,	the foregoing informa	ation is true and correct and	d any attached copy is an original or a true
	///	[h]// <del>//-</del>	z/. / .
Andrew C. Walsoth Reg. No. 43,234 Name of Person Signing  Name of Person Signing			<u> </u>
Total number	of pages including c	over sheet, attachments, a	and document(s): 3
Mail doc		I with required cover sheet i	information to:
P.O. Box 1450 Alexandria, Virginia 22313-1450			

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40.00 OP

**PATENT** 

**REEL: 017760 FRAME: 0882** 

## ASSIGNMENT

WHEREAS, I, <u>Seiichi Aritome</u>, residing at <u>2222 S. Swallowtail Lane</u>, <u>Boise</u>, <u>Idaho</u>, <u>83706</u> made certain new and useful inventions and improvements for which I executed an application for Letters Patent of the United States herewith, and which is entitled <u>PROGRAMMING METHOD TO REDUCE GATE COUPLING INTERFERENCE FOR NON-VOLATILE MEMORY</u>.

AND WHEREAS, <u>Micron Technology</u>, <u>Inc.</u>, a corporation organized and existing under and by virtue of the laws of the State of <u>Delaware</u>, and having an office and place of business at <u>8000 South Federal Way</u>, <u>Boise</u>, <u>Idaho</u>, <u>83716</u>, (hereinafter "Assignee") is desirous of acquiring the entire right, title and interest in and to said inventions, improvements and application and in and to the Letters Patent to be obtained therefor;

NOW, THEREFORE, to all whom it may concern, be it known that for good and valuable consideration, the receipt and sufficiency whereof is hereby acknowledged, I have sold, assigned, and transferred, and by these presents do sell, assign and transfer unto said Assignee, its successors or assigns, the entire right, title and interest for all countries in and to all inventions and improvements disclosed in the aforesaid application, and in and to the said application, all divisions, continuations, continuations-in-part, or renewals thereof, all Letters Patent which may be granted therefrom, and all reissues or extensions of such patents, and in and to any and all applications which have been or shall be filed in any foreign countries for Letters Patent on the said inventions and improvements, including an assignment of all rights under the provisions of the International Convention, and all Letters Patent of foreign countries which may be granted therefrom; and we do hereby authorize and request the Commissioner of Patents and Trademarks to issue any and all United States Letters Patent for the aforesaid inventions and improvements to the said Assignee as the assignee of the entire right, title and interest in and to the same, for the use of the said Assignee, its successors and assigns.

AND, for the consideration aforesaid, I do hereby agree that I and my respective executors and legal representatives will make, execute and deliver any and all other instruments in writing including any and all further application papers, affidavits, assignments and other documents, and will communicate to said Assignee, its successors and representatives all facts known to me relating to said improvements and the history thereof and will testify in all legal proceedings and generally do all things which may be necessary or desirable more effectually to secure to and vest in said Assignee, its successors or assigns the entire right, title and interest in and to the said improvements, inventions, applications, Letters Patent, rights, titles, benefits, privileges and advantages hereby sold, assigned and conveyed, or intended so to be.

AND, furthermore I covenant and agree with said Assignee, its successors and assigns, that no assignment, grant, mortgage, license or other agreement affecting the rights and property herein conveyed has been made to others by me/us and that full right to convey the same as herein expressed is possessed by me.

PATENT REEL: 017760 FRAME: 0883 Docket No.: 400,401US01 Serial No.: Unknown

Micron Docket No. 2005-0606.00/U\$

Filing Date: Herewith

Title: PROGRAMMING METHOD TO REDUCE GATE COUPLING INTERFERENCE FOR NON-VOLATILE MEMEORY

Page 2 of 2

IN TESTIMONY WHEREOF, I have hereunto set my hand this 30ft day of Jan., 2006.

**RECORDED: 06/08/2006** 

STATE OF Idaho )

COUNTY OF Qda )

On this 30 day of Tanuary, 2006 before me personally appeared Seiichi Aritome to me known and known to me to be the person described in and who executed the foregoing instrument, and he duly acknowledged to me that he executed the same for the uses and purposes therein set forth.

[SEAL]

**PATENT** 

**REEL: 017760 FRAME: 0884**